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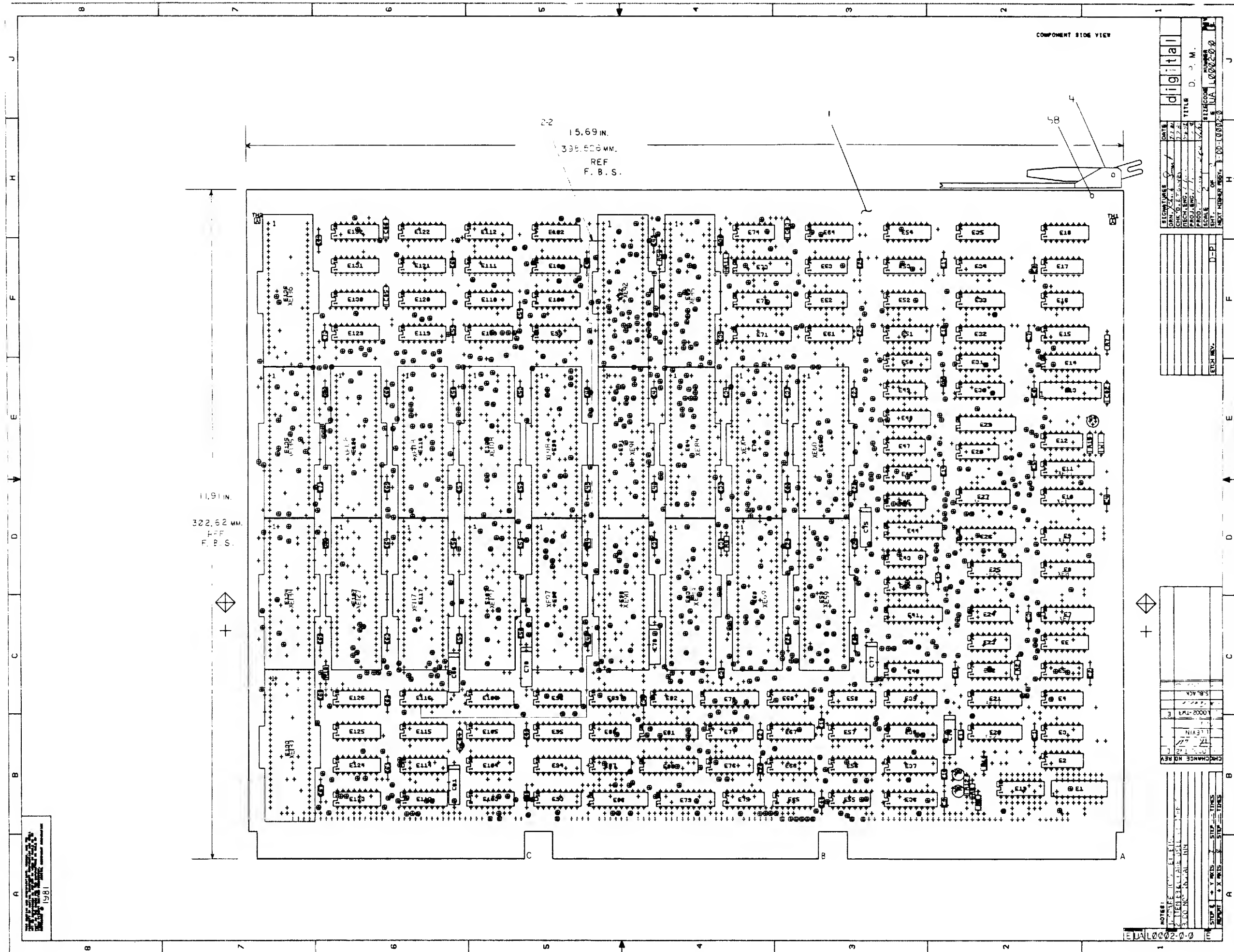
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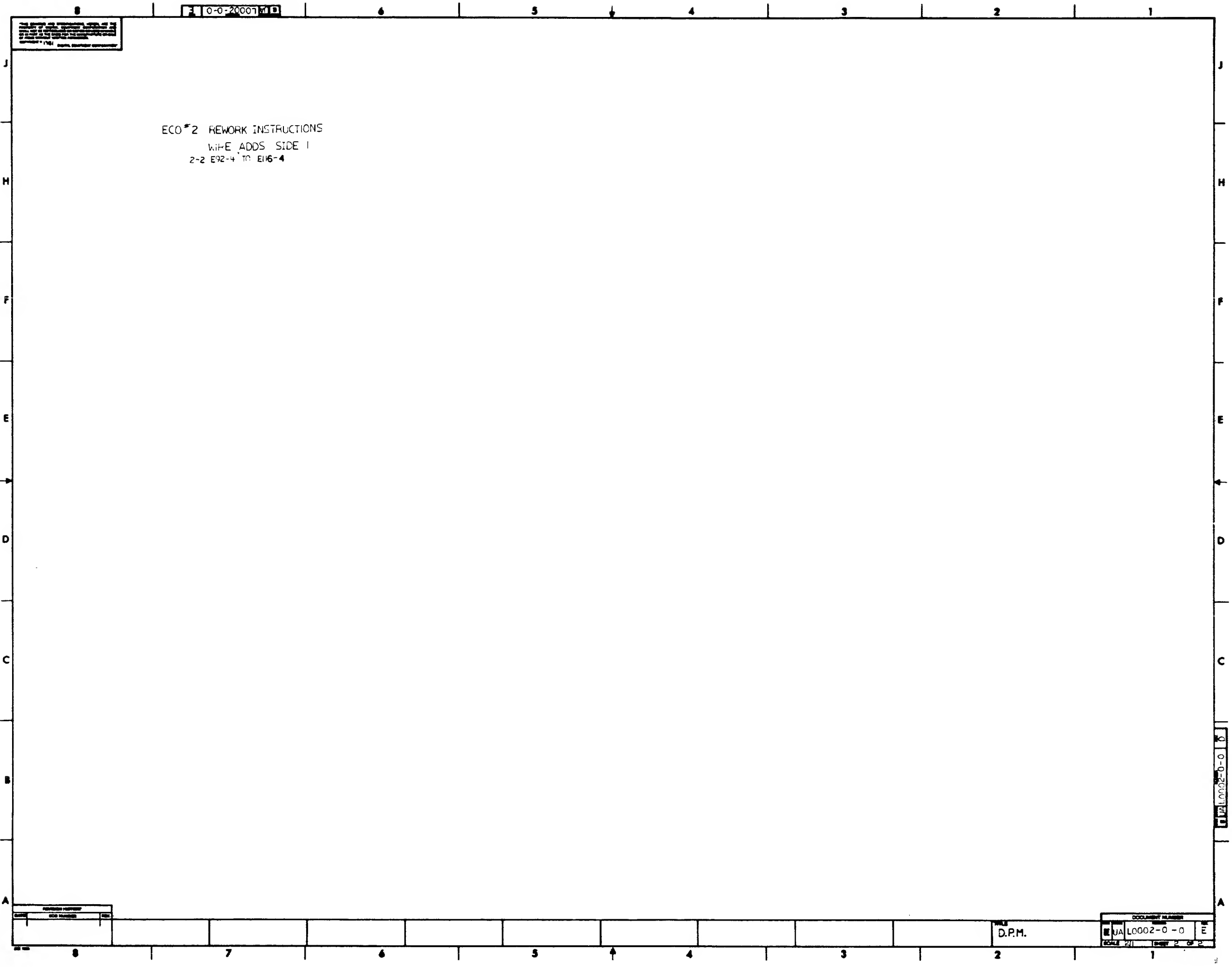
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ECO #2 REWORK INSTRUCTIONS  
WIPE ADDS SIDE 1  
2-2 E92-4 TO E116-4

DATE	BY	APP'D

D.P.M.

DOCUMENT NUMBER	
UA	L0002-0-0
SCALE 2/1	SHEET 2 OF 2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
					00	
1	1	E-MD-5013555-0-0	5013555-00	ETCHED CIRCUIT BOARD	1	
2	2	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6	C75-C80
3	3		1012784-00	.047 MFD 50V +80-20% CER	73	C1-C35,C37-C74
4	4		1210711-02	/REPLACED BY 12-16988-02	1	
5	5		1215924-00	SOCKET IC W/METAL CONT	22	XE59-XE60,XE69-XE70,XE83-XE85, CONT XE90-XE92,XE97,XE98,XE107,XE108, CONT XE117,XE118,XE127,XE128, CONT XE133-XE136
6	6		1300005-04	R NETWORK 15-470 5.0 % 16PIN	2	E61,E89
7	7		1300365-00	1.0 K .25 W 5.0 % CC	1	R10
8	8		1301890-00	560.0 .25 W 5.0 % CC	2	R1,R2
9	9		1811660-21	*** THIS ITEM IS NOT USED ***	-	
10	10		1910532-00	74S00 NAND GATE-QUAD 2IN	5	E17,E24,E31,E52,E35
11	11		1910533-00	74S03 NAND GATE-QUAD 2IN,0	1	E42
12	12		1910534-00	74S04 INVERTER GATE-HEX 1I	4	E4,E5,E55,E57
13	13		1910535-00	74S05 INVERTER GATE-HEX 1	1	E68
14	14		1910536-00	74S10 NAND GATE-TRIPLE 3IN	4	E5,E49,E63,E75
15	15		1910539-00	74S20 NAND GATE-DUAL 4INPU	1	E62
16	16		1910540-00	74S22 NAND GATE-DUAL 4INPU	1	E22
17	17		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1	E3
18	18		1910545-00	74S112 FF-JK DUAL,EDGE TRIG	1	E56
19	19		1910547-00	74S153 MUX 1 OF 4 (DUAL)	2	E34,E64
20	20		1910548-00	74S157 MUX 1 OF 2 (QUAD)	1	E6
21	21		1910552-00	74S194 SHIFT REG.,4BIT RIGH	1	E30
22	22		1910956-00	74S151 MUX 1 OF 8	9	E18,E32,E33,E46,E54,E66,E67,E76, CONT E77
23	23		1911573-00	74S280 PARITY GEN/CHKR,9BIT	8	E12,E16,E28,E47,E74,E82,E87,E88
24	24		1911675-00	74S138 DECODER/DEMUX 3-8 LIN	1	E48
25	25		1912389-00	74S08 AND GATE-QUAD 2IN,PO	1	E43
26	26		1912586-00	DM 85S68N REGISTER,64BIT EDGE	4	E36-E39

REVISION HISTORY			BASIC PART NO: L0002		DRN: D.SIREEN		DATE: 19-OCT-79		D I G I T A L									
ENG	ECO NUMBER	REV	SECTION A OF A															
---	INITIAL	B	SECTION VARIATION INDEX		CHK'D: E.T. GERRY		DATE: 19-OCT-79		TITLE PARTS LIST									
D.L.	L0002-TW001	C	[A] 00						D.P.M.									
			[B]															
			[C]		DES.ENG: D. LI		DATE: 19-OCT-79											
			[D]															
			[E]															
			[F]		RESP.ENG.: D. LI		DATE: 19-OCT-79		DOCUMENT NUMBER									
			[G]						SIZE CODE NUMBER REV									
			[H]						K PL L0002-0-DBP C									
			[I]		MFG.ENG.: VANCE PARKER		DATE: 8-FEB-80											
			[J]															
			[K]															
			[L]															
			[M]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #									
			[N]		E-UA-L0002-0-0		B-DD-L0002-0-0		Z1257.PLS 14									
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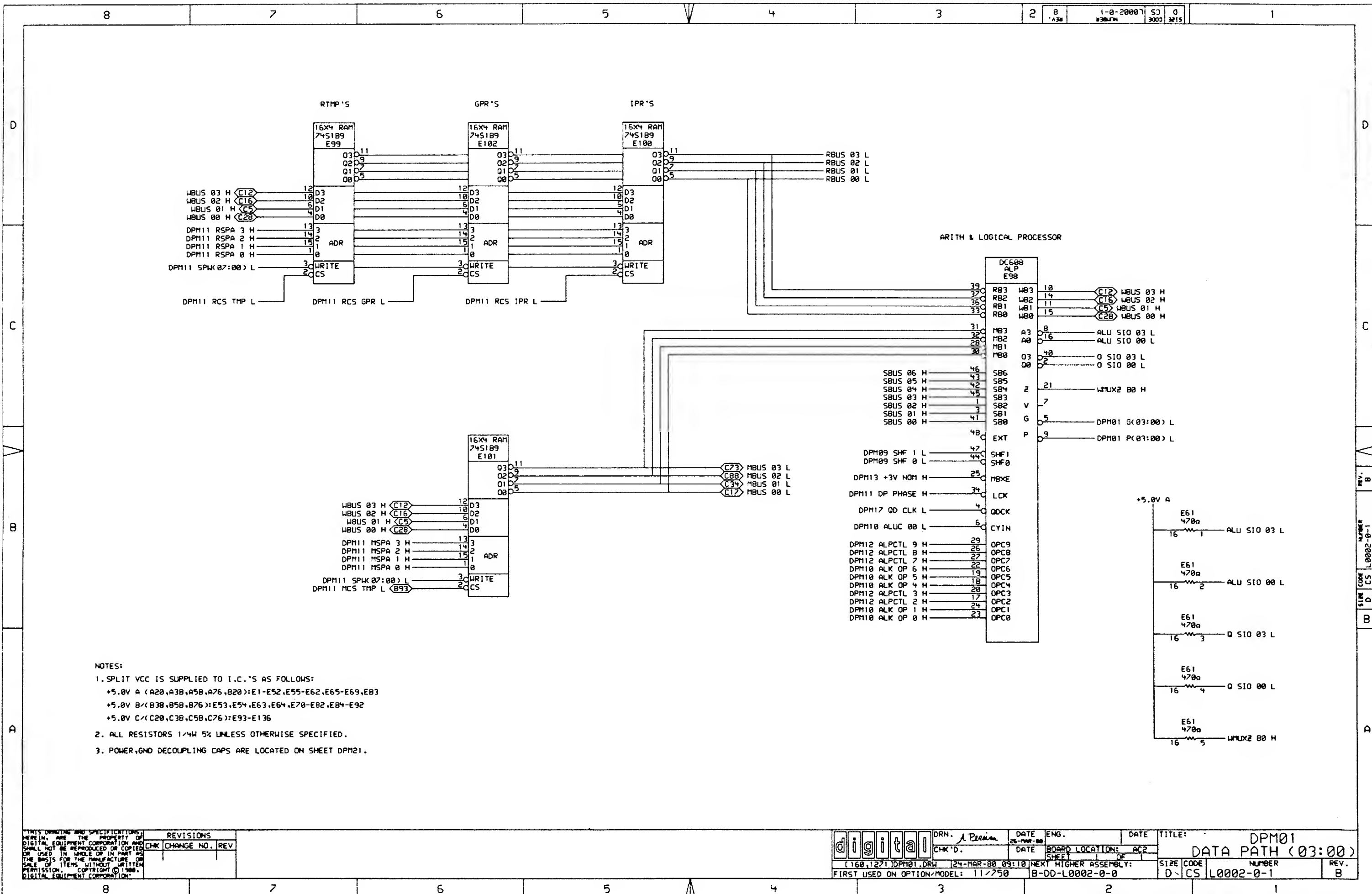
TWJ

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
					00		
27	27		1912661-00	74S189 MEMORY READ/WRITE	32		E93-E96,E99-E106,E109-E116,
28	28		1912746-00	DEC 74S37 NAND GATE-QUAD 2IN	2	CONT	E119-E126,E129-E132
29	29		1913462-00	74S240 OCTAL BUFFER,INVERTI	1		E53,E65
30	30		1913493-00	74S241 OCTAL BUFFER,TRI-STA	1		E40
31	31		1913671-00	74S374 FF-D OCTAL TRISTATE	8		E41
32	32		1913839-00	74LS165 SHIFT REG.,8BIT	2		E14,E26,E29,E44,E79-E81,E86
33	33		1914085-00	74S260 NOR GATE-DUAL,POS	1		E50,E58
34	34		1914214-00	LS374 FF-D OCTAL EDGE TRIG	5		E23
35	35		1914694-00	DC 620A BIPOLAR,LS,400-GATE	1		E13,E71-E73,E78
36	36		1914682-00	DC 608B BIPOLAR,LS,400-GATE	8		E92
37	37		1914684-00	DC 610B BIPOLAR,LS,400-GATE	1	CONT	E97,E98,E107,E108,E117,E118,
38	38		1914686-00	DC 612B BIPOLAR,LS,400-GATE	1		E127,E128
39	39		1914687-00	DC 613B BIPOLAR,LS,400-GATE	4		E70
40	40		1914688-00	DC 614C BIPOLAR,LS,400-GATE	1		E91
41	41		1914689-00	DC 615B BIPOLAR,LS,400-GATE	1		E133-E136
42	42		1914690-00	DC 616C BIPOLAR,LS,400-GATE	1		E84
43	43		1914691-00	DC 617C BIPOLAR,LS,400-GATE	1		E90
44	44		1914695-00	DC 621C BIPOLAR,LS,400-GATE	1		E85
45	45		1914696-00	DC 622B BIPOLAR,LS,400-GATE	1		E69
46	46		1914703-00	DC 629C BIPOLAR,LS,400-GATE	1		E59
47	47		23553A2-00	A2-05	1		E83
48	48		23904A9-00	A9-01	1		E60
49	49		23618F1-00	F1-01	1		E51
50	50		23619F1-00	F1-02	1		E15
51	51		23021F2-00	F2-01	1		E20
52	52		23022F2-00	F2-01	1		E21
53	53		23023F2-00	F2-01	1		E7
54	54		23024F2-00	F2-02	1		E25
55	55		23025F2-00	F2-02	1		E27
56	56		23026F2-00	F2-02	1		E8
57	57		23027F2-00	F2-02	1		E9
58	58		9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12		E10
59	59		1302379-00	75.0 .25 W 5.0 % CC	6		E11
60	60		1503121-00	2N 2369 NPN 350MW SI N	2		R5-R9,R11
61	61		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	1		Q2,Q3
62	62		1215006-03	SOCKET 18PIN IC LOW PROFILE	9		E2
63	63		1215935-00	GASKET, THERMAL .50"X.80"	22		XE7-XE11,XE20,XE21,XE25,XE27
64	64		1215936-00	HEAT SINK, FORCED CONVECTION	22		
65	65		1305125-00	383.0 .25 W 1.0 % RN5SD-F10	1		R12

66 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

D	I	G	I	T	A	L	TITLE		SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							D.P.M.				K	PL	L0002-0-DBP	C

TW



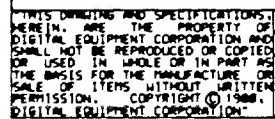
NOTES:

- SPLIT VCC IS SUPPLIED TO I.C.'S AS FOLLOWS:  
+5.0V A (A20,A30,A50,A70,B20):E1-E52,E55-E62,E65-E69,EB3  
+5.0V B (B30,B50,B70):E53,E54,E63,E64,E70-E82,EB4-E92  
+5.0V C (C20,C30,C50,C70):E93-E136
- ALL RESISTORS 1/4W 5% UNLESS OTHERWISE SPECIFIED.
- POWER,GND DECOUPLING CAPS ARE LOCATED ON SHEET DPM21.

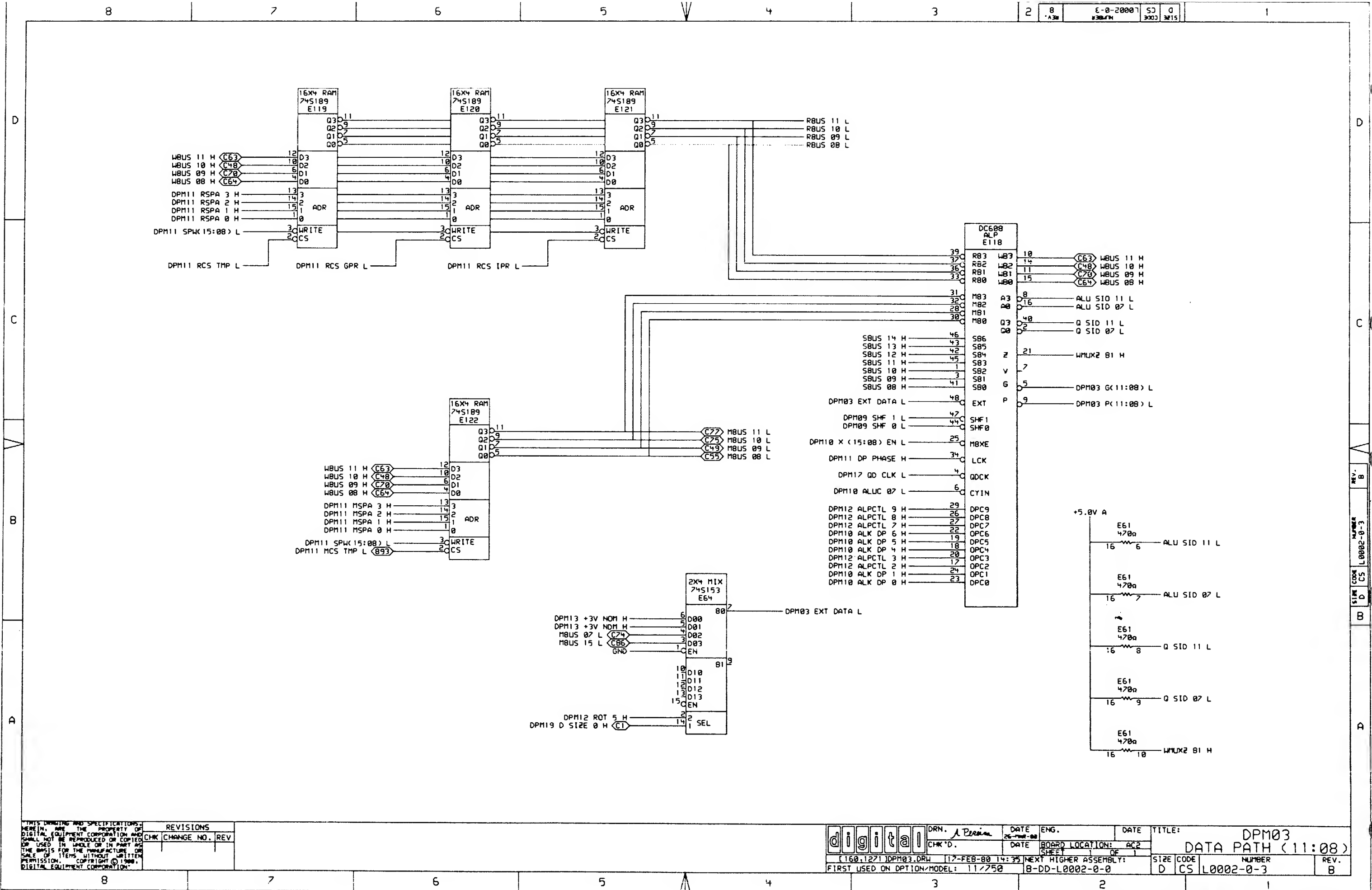
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REVISIONS		
CHK	CHANGE NO.	REV

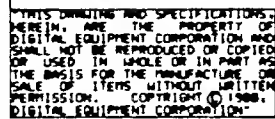
digital	DRN. A. P. Berman	DATE 26-MAR-80	ENG.	DATE	TITLE: DPM01
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF 1	DATA PATH (03:00)
FIRST USED ON OPTION/MODEL: 11/250		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE NUMBER REV.	D- CS L0002-0-1 B



digital	DRN. <i>1 Prelim</i>	DATE <i>26-FEB-88</i>	ENG.	DATE	TITLE: <i>DPM02</i>				
	CHK'D.	DATE	BOARD LOCATION: <i>AC2</i>		<i>DATA PATH (07:04)</i>				
[160,127] DPM82.DRW		[17-FEB-88 14:32]		NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: <i>11/750</i>		<i>B-DD-L0002-0-0</i>				<i>D</i>	<i>C5</i>	<i>L0002-0-2</i>	<i>8</i>

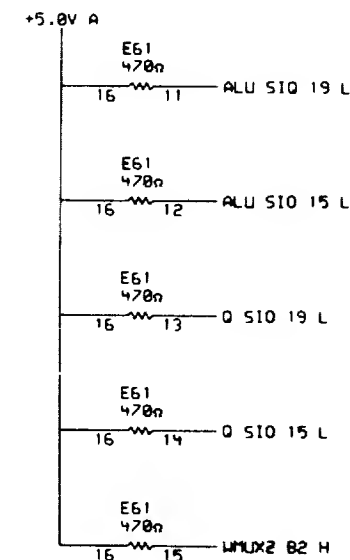
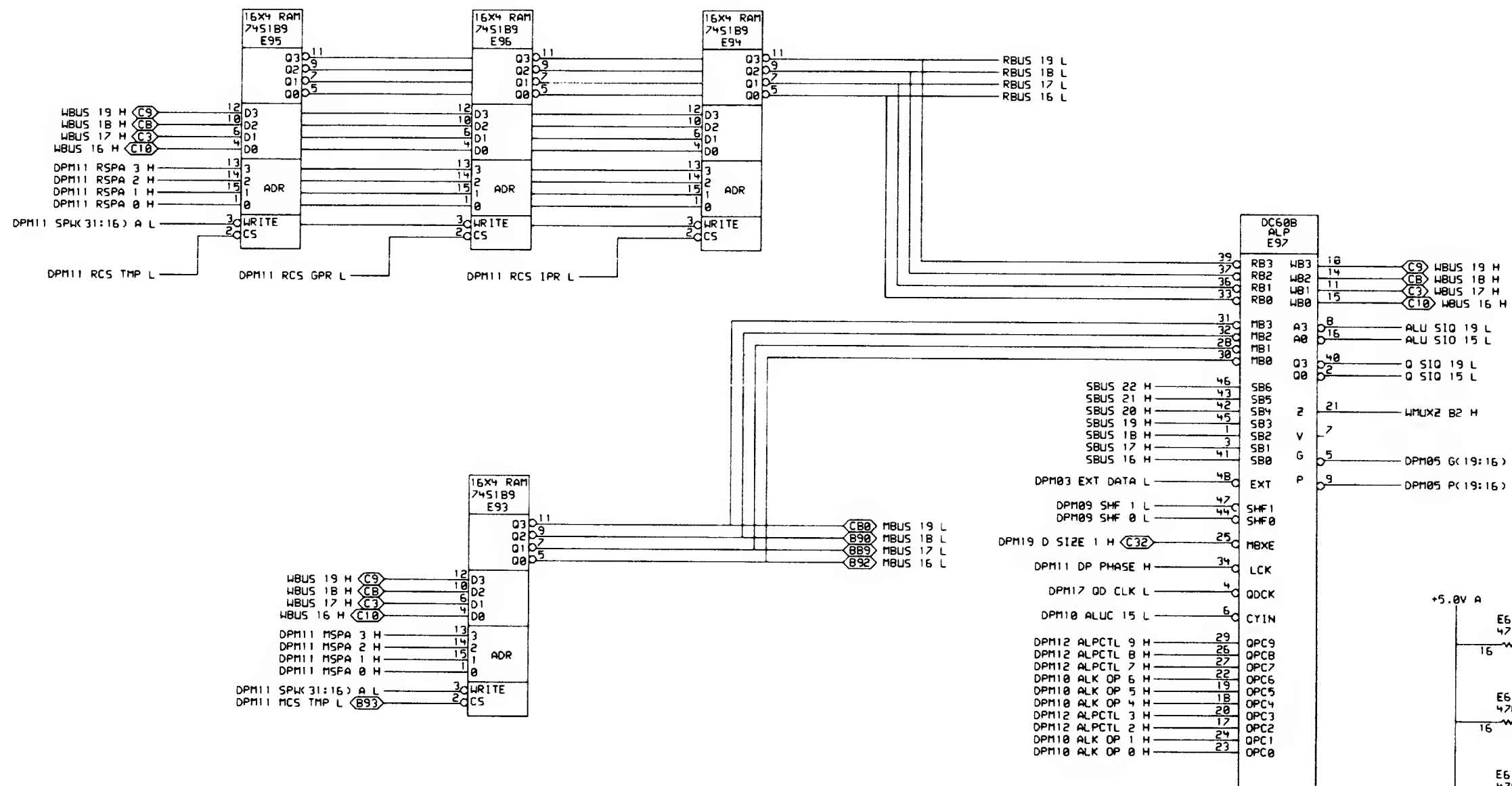






DPM04  
DATA PATH 615:12

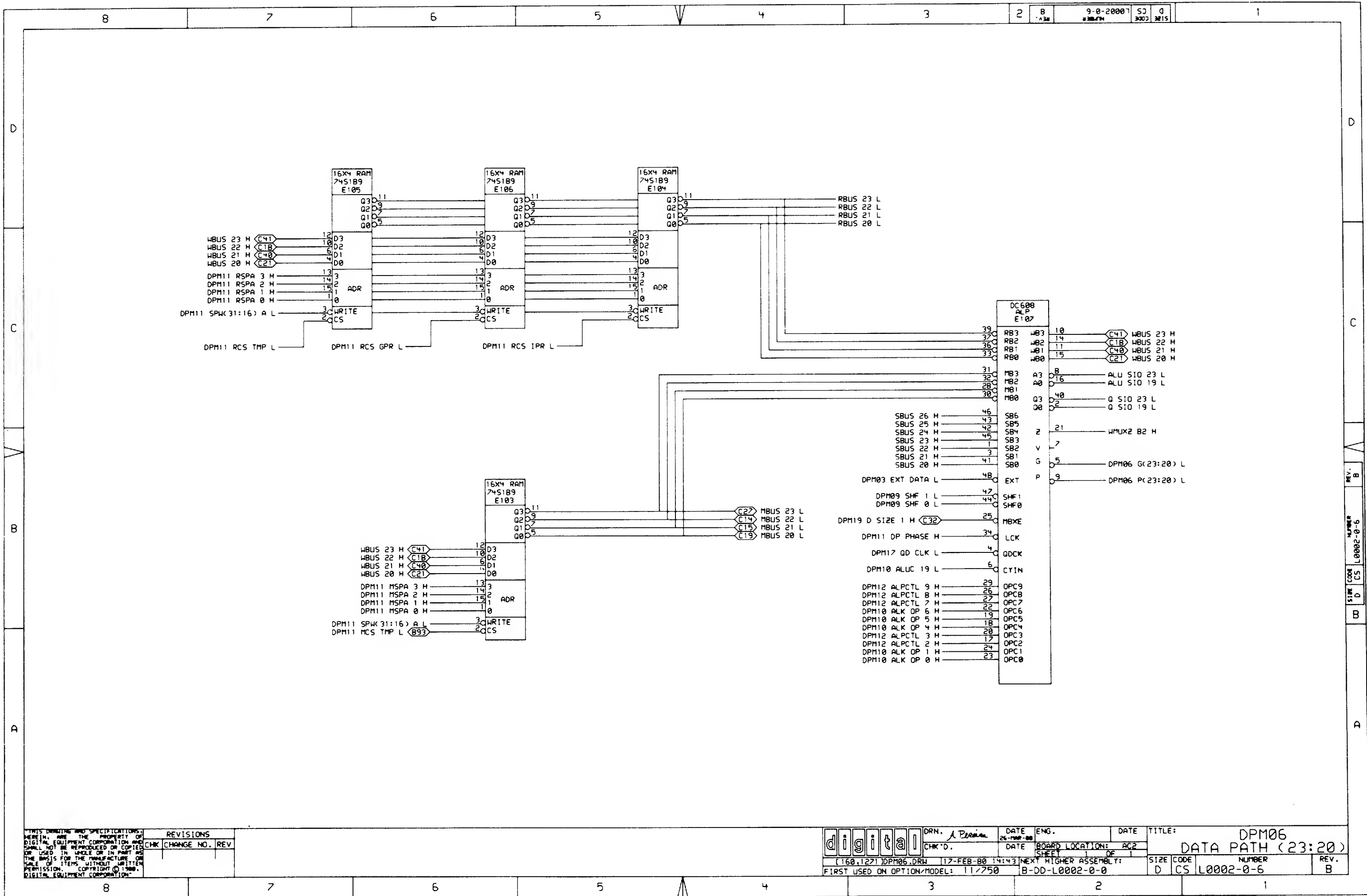
COMETCHIPS, (160,1271) DPM04.DPL, SCALE 2, "D" RELEASE BOX  
COMETCHIPS NORMAL SIZE 160,1271.1 36-MAR-88 11:38



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REVISIONS  
CHK CHANGE NO. REV

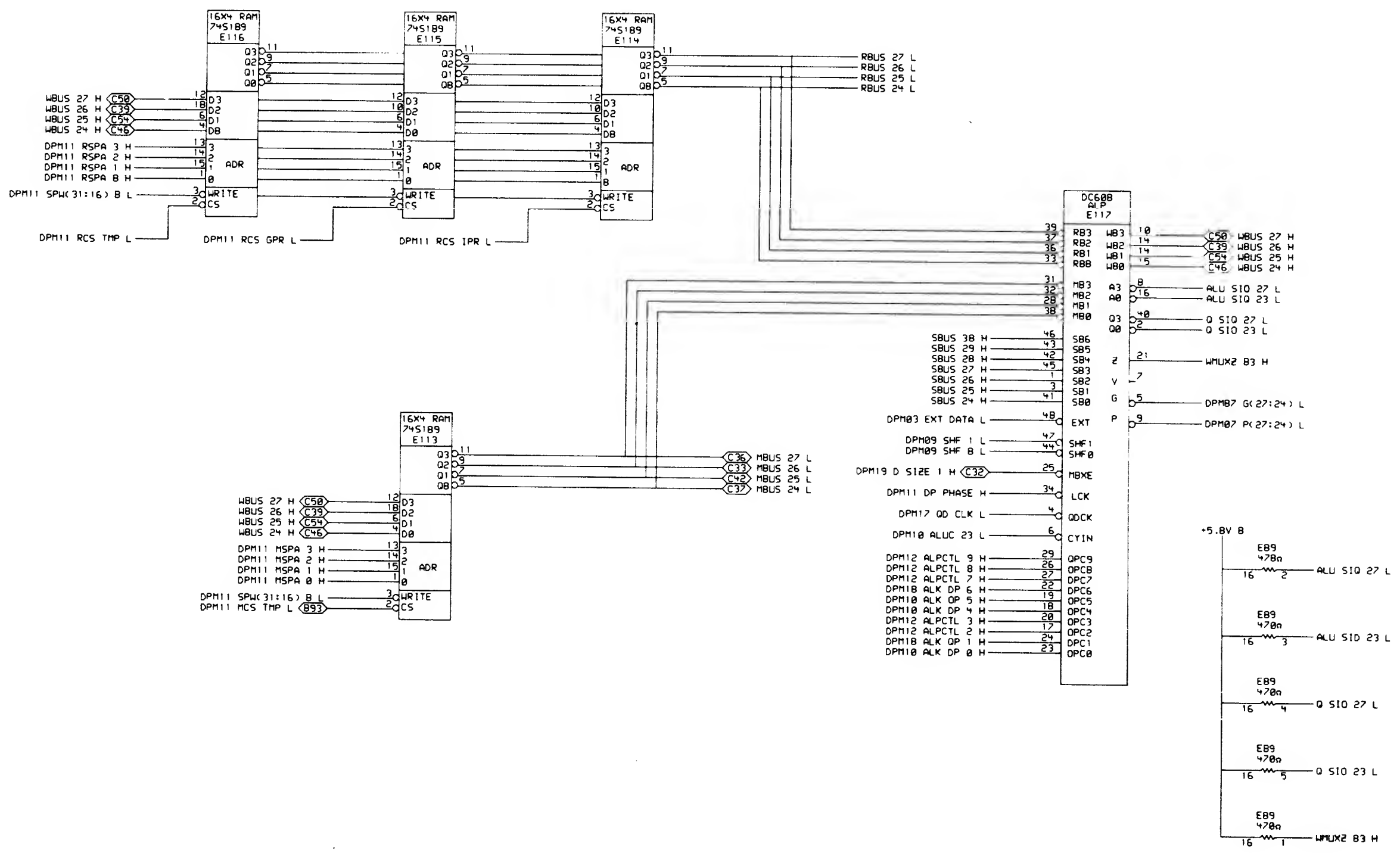
digital	DRN. A. P. P.	DATE 26-MAR-80	ENG.	DATE	TITLE: DPM05
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET 1 OF 1	DATA PATH (19:16)
(160,1271) DPM05.DRW		12-FEB-80 14:41	NEXT HIGHER ASSEMBLY:	SIZE CODE D	NUMBER CS L0002-0-5
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0002-0-0		REV. B	



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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN. <i>A. P. P. P.</i>	DATE 26-MAR-80	ENG.	DATE	TITLE: DPM06
	CHK'D.	DATE 17-FEB-80 14:43	BOARD LOCATION: AC2	SHEET 1 OF 1	DATA PATH (23:20)
FIRST USED ON OPTION/MODEL: 117750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE	NUMBER
				D CS	L0002-0-6
				REV.	B



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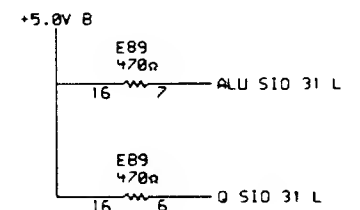
REVISIONS		
CHK	CHANGE NO.	REV

digital

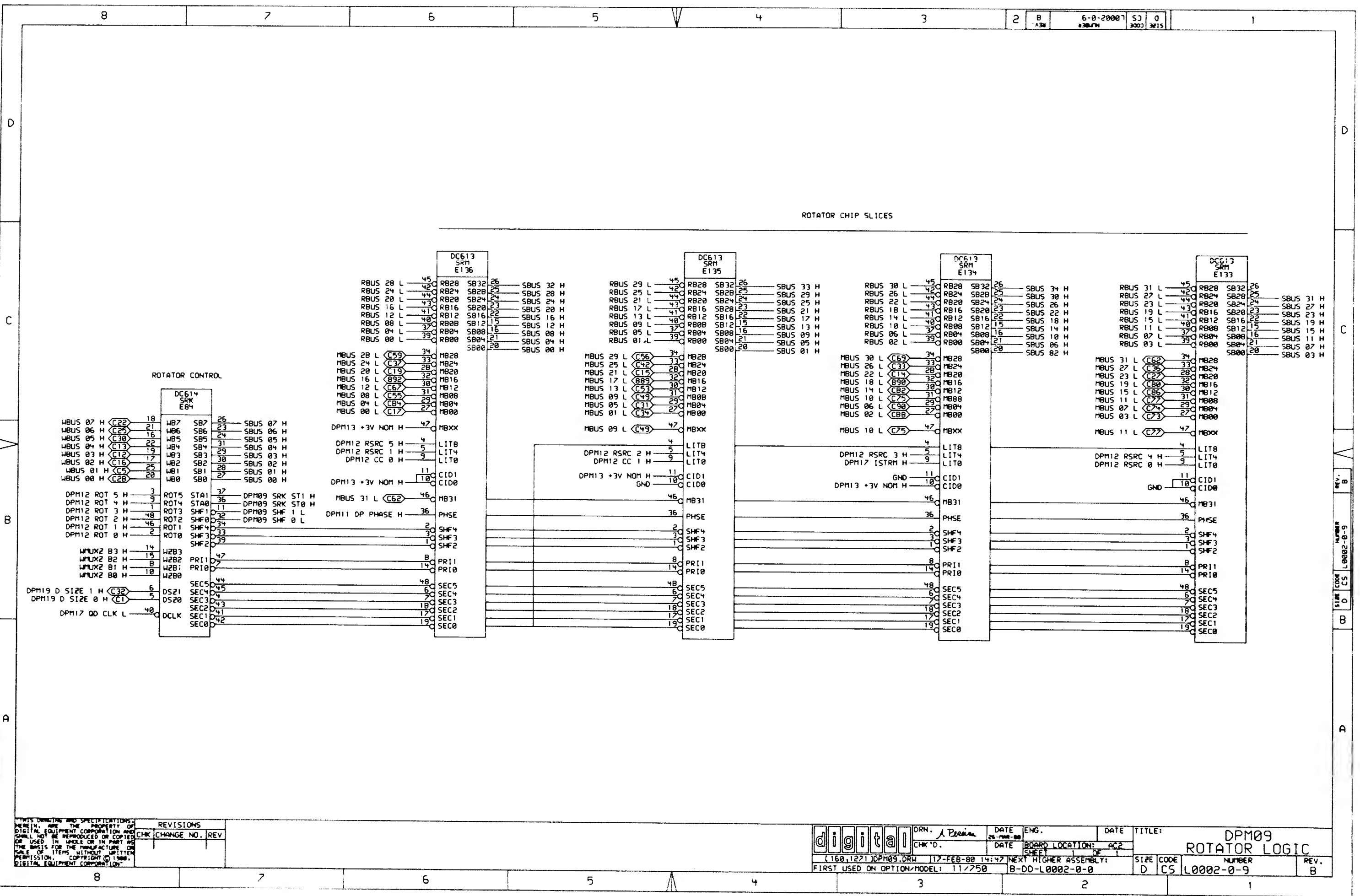
DRN. A. P. DATE 26-FEB-80 ENG. DATE  
CHK'D. DATE BOARD LOCATION: AC2  
SHEET 1 OF 1

160,1271 DPM07.DRW 17-FEB-80 14:44 NEXT HIGHER ASSEMBLY:  
FIRST USED ON OPTION/MODEL: 11/250 B-DD-L0002-0-0

TITLE: DPM07  
DATA PATH (27:24)  
SIZE CODE NUMBER REV.  
D CS L0002-0-7 B



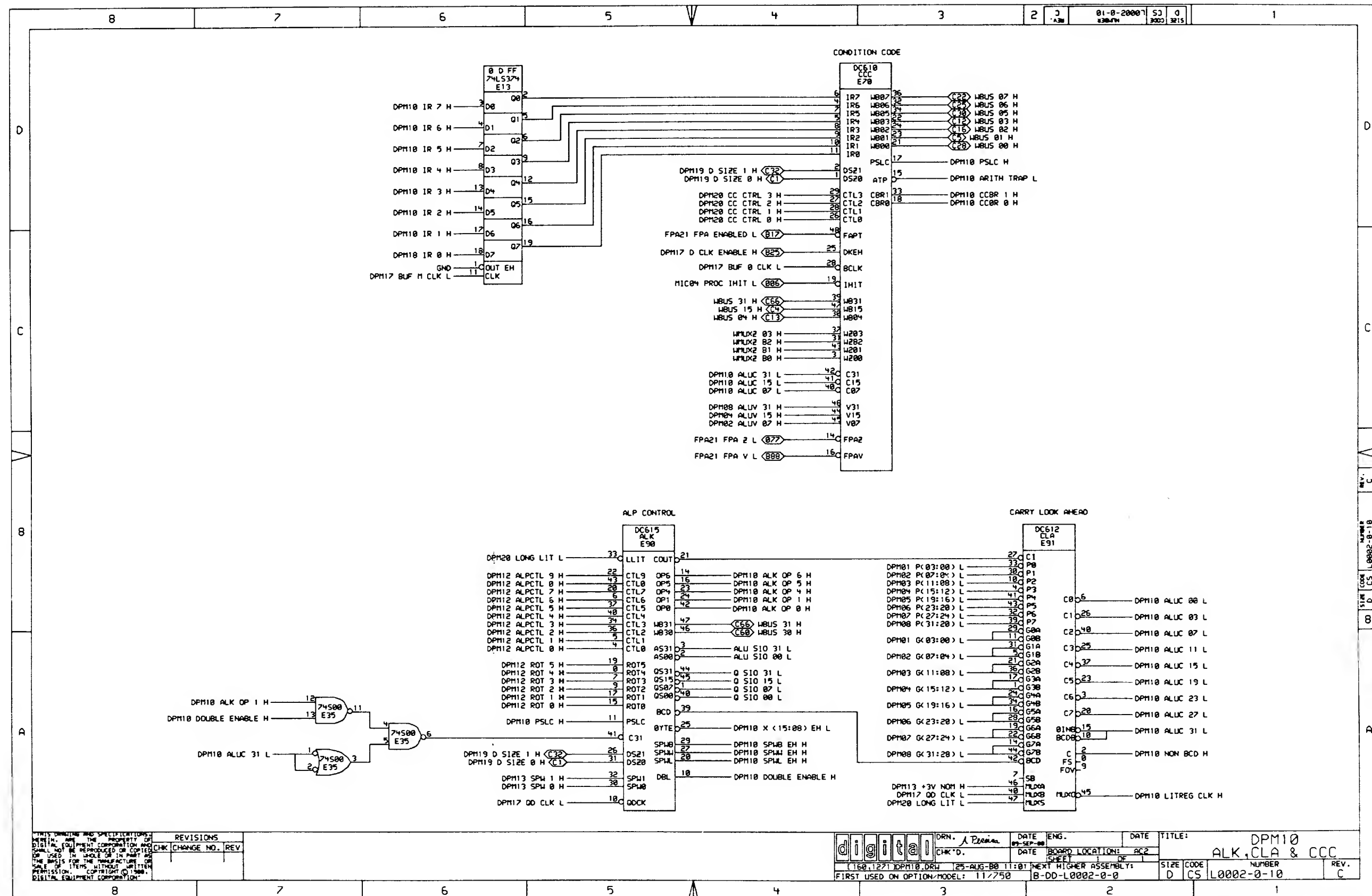
SIZE	CODE	NUMBER	REV.
D	CS	L0002-0-8	B



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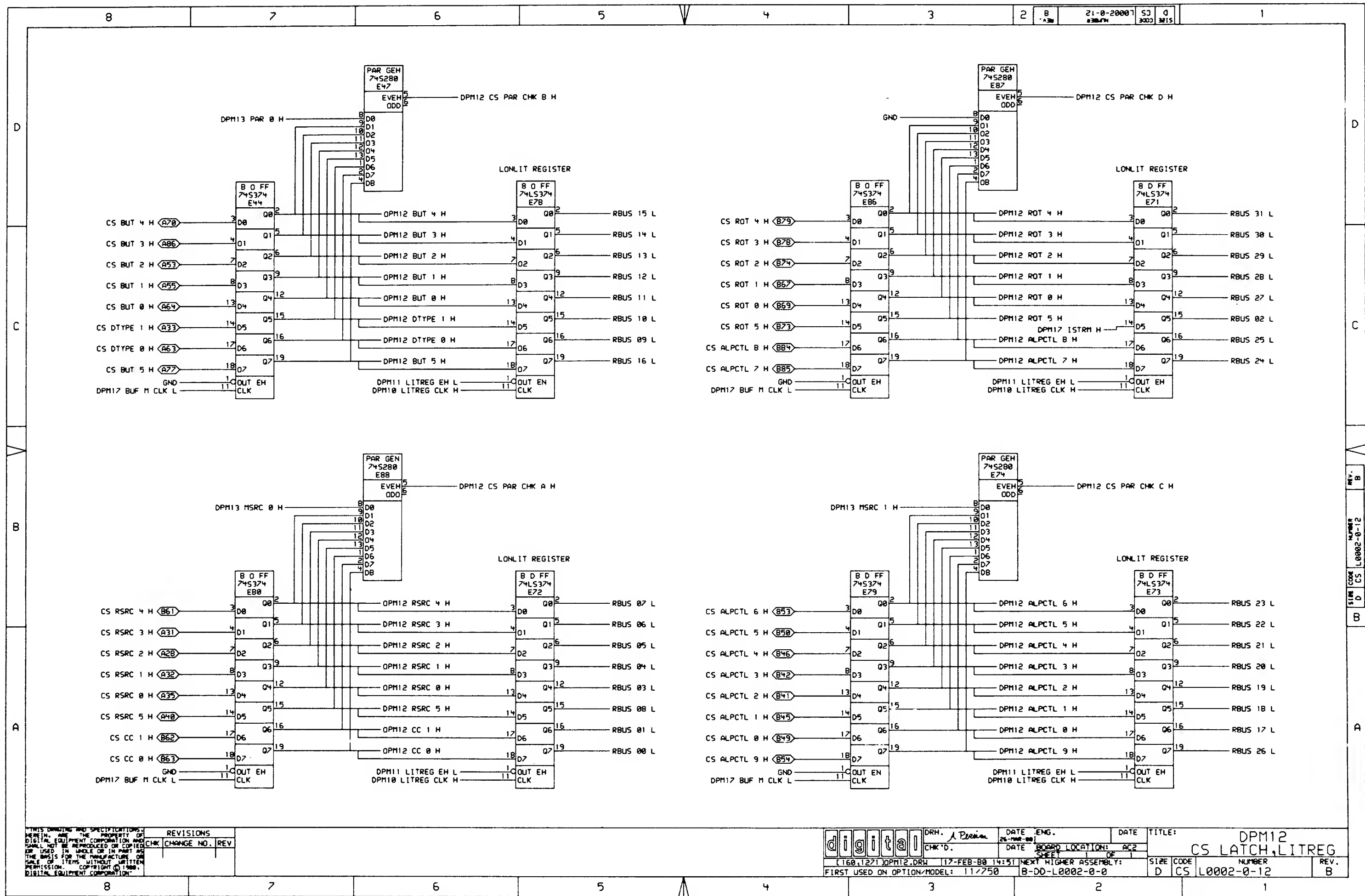
REVISIONS		
CHK	CHANGE NO.	REV

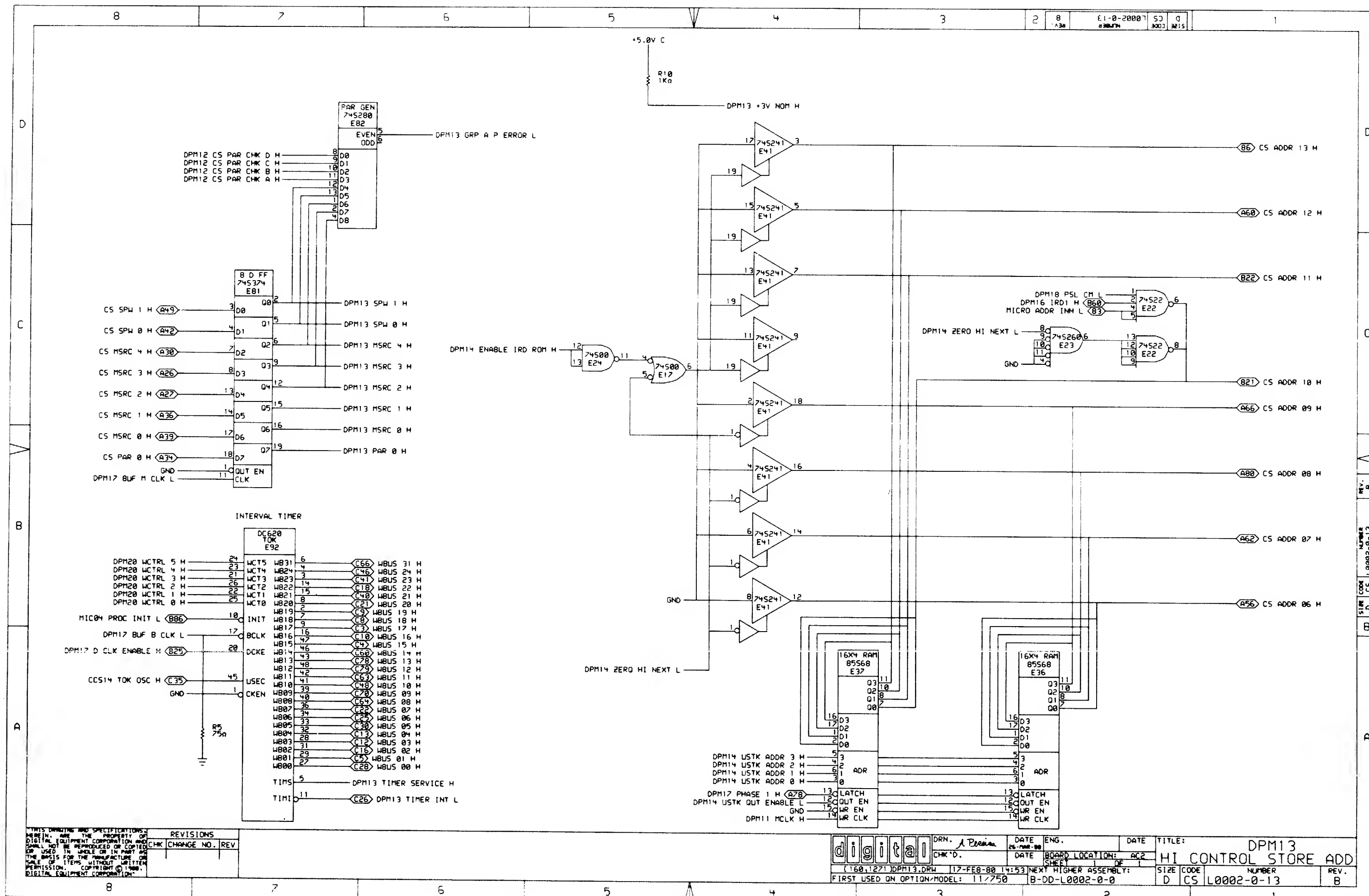
digital	DRN. A. P. P. P.	DATE 26-MAR-88	ENG.	DATE	TITLE: DPM09 ROTATOR LOGIC
	CHK'D.	DATE	BOARD LOCATION: AC2	SHEET	OF
FIRST USED ON OPTION/MODEL: 11/750		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE D CS	NUMBER L0002-0-9





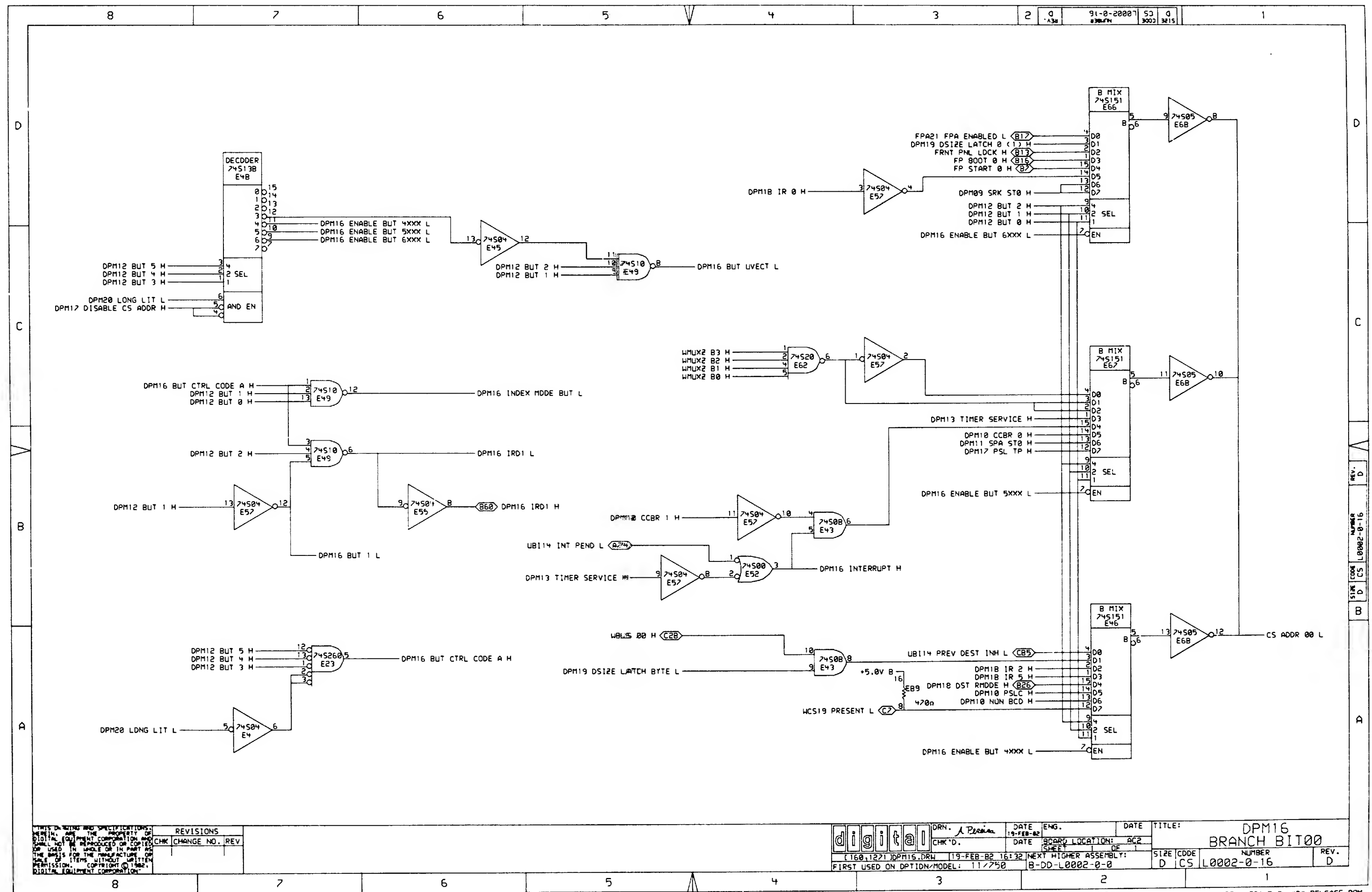




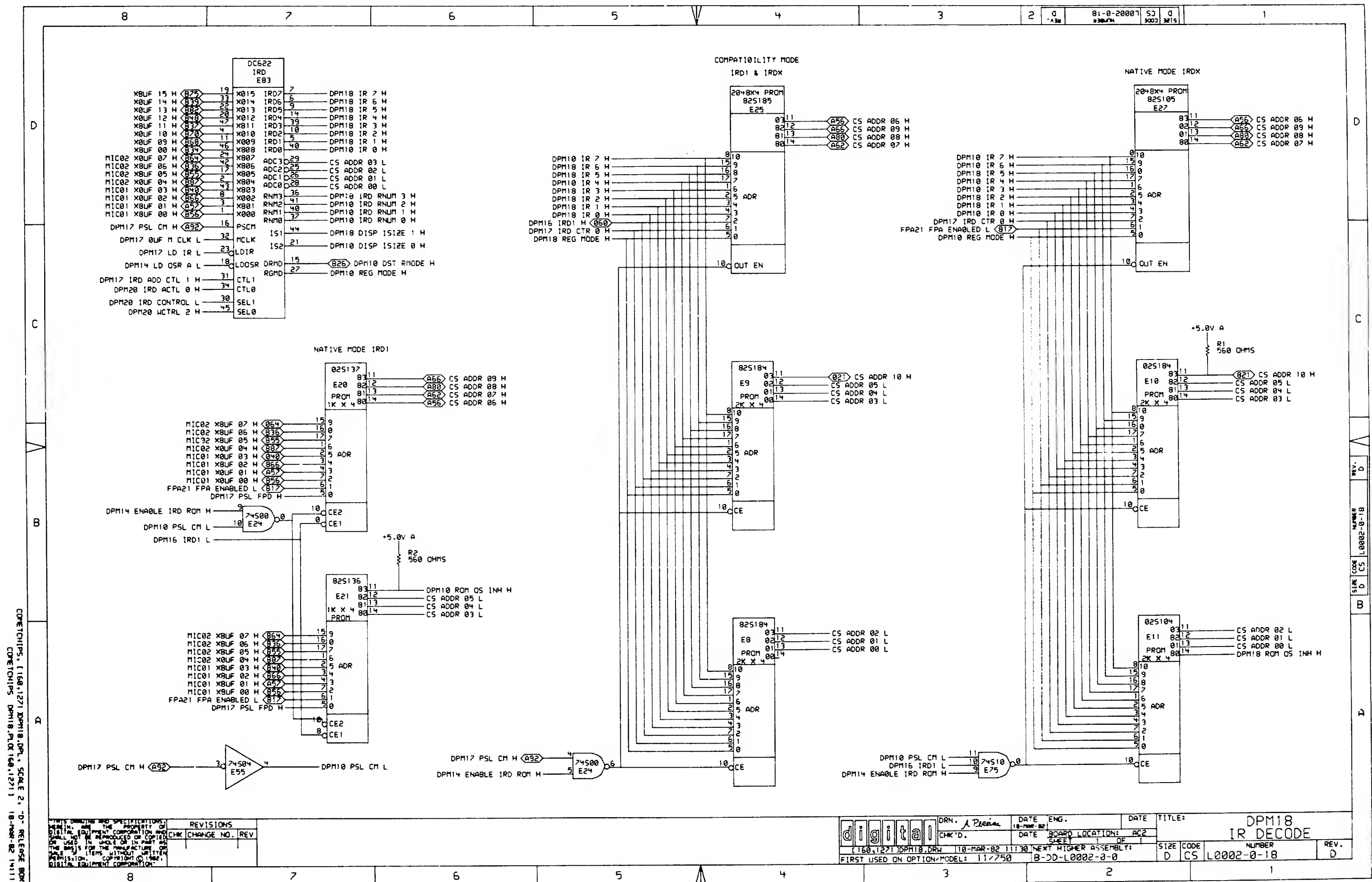








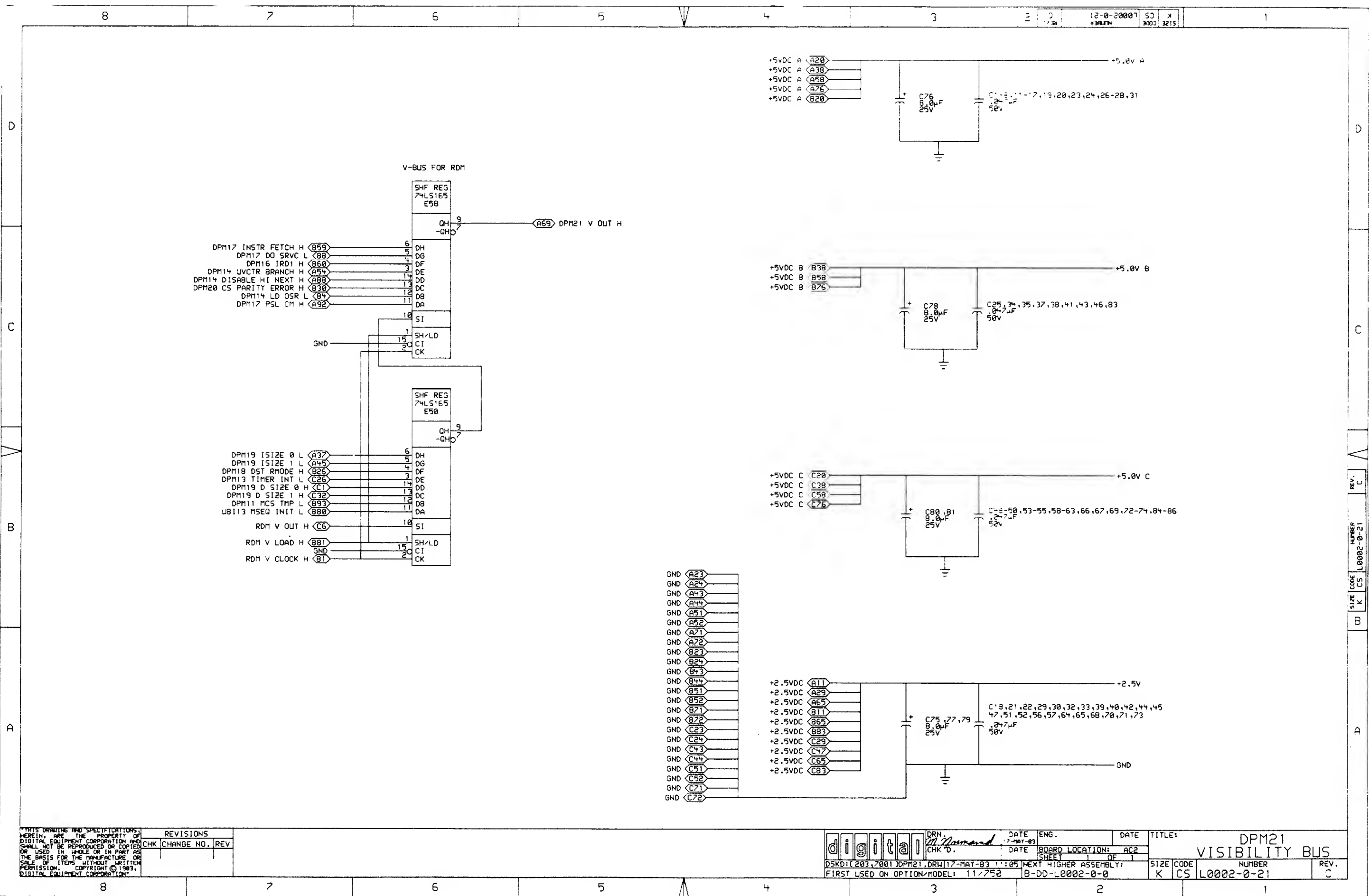












SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)
ALU SIO 00 L	01,10	CS CC 1 H	12	DPM02 G<07:04> L	10,02
ALU SIO 03 L	01,02	CS CLKX H	14	DPM02 P<07:04> L	10,02
ALU SIO 07 L	03,02	CS DTYPE 0 H	12	DPM03 EXT DATA L	03,05,06,07,08,04
ALU SIO 11 L	03,04	CS DTYPE 1 H	12	DPM03 G<11:08> L	10,03
ALU SIO 15 L	05,04	CS HNEXT PAR H	20	DPM03 P<11:08> L	10,03
ALU SIO 19 L	05,06	CS ISTRM H	17	DPM04 ALUV 15 H	10,04
ALU SIO 23 L	07,06	CS JSR H	14	DPM04 G<15:12> L	10,04
ALU SIO 27 L	07,08	CS LIT 0 H	17,20	DPM04 P<15:12> L	10,04
ALU SIO 31 L	08,10	CS LIT 1 H	17,20	DPM05 G<19:16> L	10,05
CCS14 CPU OSC OUT H	17	CS MISC CTL 0 H	17	DPM05 P<19:16> L	10,05
CCS14 TOK OSC H	13	CS MISC CTL 1 H	17	DPM06 G<23:20> L	10,06
CLK CTRL 0 H	17	CS MISC CTL 2 H	17	DPM06 P<23:20> L	10,06
CLK CTRL 1 H	17	CS MISC CTL 3 H	17	DPM07 G<27:24> L	10,07
CS ADDR 00 H	14	CS MISC CTL 4 H	17	DPM07 P<27:24> L	10,07
CS ADDR 00 L	10,14,17,16	CS MSRC 0 H	13	DPM08 ALUV 31 H	10,08
CS ADDR 01 H	14	CS MSRC 1 H	13	DPM08 G<31:20> L	10,08
CS ADDR 01 L	10,14,17,15	CS MSRC 2 H	13	DPM08 P<31:20> L	10,08
CS ADDR 02 H	14	CS MSRC 3 H	13	DPM09 SHF 0 L	09,05,01,06,02,07,03,08,04
CS ADDR 02 L	10,14,17,15	CS MSRC 4 H	13	DPM09 SHF 1 L	09,05,01,06,02,07,03,08,04
CS ADDR 03 H	14	CS NEXT 00 H	14	DPM09 SRK ST0 H	16,09
CS ADDR 03 L	10,14,17	CS NEXT 01 H	14	DPM09 SRK ST1 H	15,09
CS ADDR 04 H	14	CS NEXT 02 H	14	DPM10 ALK OP 0 H	10,05,01,06,02,07,03,08,04
CS ADDR 04 L	10,14,17	CS NEXT 03 H	14	DPM10 ALK OP 1 H	10,05,01,06,02,07,03,08,04
CS ADDR 05 H	14	CS NEXT 04 H	14	DPM10 ALK OP 4 H	10,05,01,06,02,07,03,08,04
CS ADDR 05 L	10,14,17	CS NEXT 05 H	14	DPM10 ALK OP 5 H	10,05,01,06,02,07,03,08,04
CS ADDR 06 H	10,13	CS PAR 0 H	13	DPM10 ALK OP 6 H	10,05,01,06,02,07,03,08,04
CS ADDR 07 H	10,13	CS PAR 1 H	20	DPM10 ALUC 00 L	10,01
CS ADDR 08 H	10,13	CS ROT 0 H	12	DPM10 ALUC 03 L	10,02
CS ADDR 09 H	10,13	CS ROT 1 H	12	DPM10 ALUC 07 L	10,03
CS ADDR 10 H	10,13	CS ROT 2 H	12	DPM10 ALUC 11 L	10,04
CS ADDR 11 H	13	CS ROT 3 H	12	DPM10 ALUC 15 L	10,05
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DRN. DE-1000  
CHK'D.  
DATE 21-MAR-88  
ENGINEER  
BOARD LOCATION: AC2  
SHEET 1 OF 1  
FIRST USED ON OPTION/MODEL: 11/750

DATE 21-MAR-88  
TITLE: DPM22 FORWARD REFERENCE  
SIZE CODE NUMBER REV.  
D CS L0002-0-22 B

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DPM11 RSPA 2 N	11,05,01,06,02,07,03,00,04	DPM13 MSRC 4 H	13,11	DPM17 IRD CTR 0 H	19,10,17																																																																																																																																																																																																																																																																																																																				
DPM11 RSPA 3 H	11,05,01,06,02,07,03,00,04	DPM13 PAR 0 H	12,13	DPM17 IRD CTR 1 H	19,14,17																																																																																																																																																																																																																																																																																																																				
DPM11 SPA ST0 H	16,11	DPM13 SPW 0 H	11,13,10	DPM17 IRD CTR 2 H	19,14,17																																																																																																																																																																																																																																																																																																																				
DPM11 SPA ST1 H	15,11	DPM13 SPW 1 H	11,13,10	DPM17 IRD LD RNUM H	17,11																																																																																																																																																																																																																																																																																																																				
DPM11 SPWK07:00> L	11,01,02	DPM13 TIMER INT L	21,13	DPM17 ISTRM H	20,17,19,12,09																																																																																																																																																																																																																																																																																																																				
DPM11 SPWK15:00> L	11,03,04	DPM13 TIMER SERVICE H	16,17,13	DPM17 LATCH UTRAP L	17																																																																																																																																																																																																																																																																																																																				
DPM11 SPWK31:16> A L	11,05,06	DPM14 CLKX H	20,14,17	DPM17 LD IR L	17,10																																																																																																																																																																																																																																																																																																																				
DPM11 SPWK31:16> 0 L	11,07,00	DPM14 DISABLE NI NEXT H	21,14	DPM17 LIT 0 H	20,17,19,14,11																																																																																																																																																																																																																																																																																																																				
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DPM12 ALPCTL 1 N	12,10	DPM14 FPA WAIT L	14,17	DPM17 M CLK ENABLE H	17,20																																																																																																																																																																																																																																																																																																																				
DPM12 ALPCTL 2 N	12,10,05,01,06,02,07,03,00,04	DPM14 JSR H	20,14	DPM17 M CLK L	17																																																																																																																																																																																																																																																																																																																				
DPM12 ALPCTL 3 N	12,10,05,01,06,02,07,03,00,04	DPM14 LD DSR A L	19,14,17,10	DPM17 MISC CTL 0 H	20,17																																																																																																																																																																																																																																																																																																																				
DPM12 ALPCTL 4 H	12,10	DPM14 LD DSR L	14,21,17	DPM17 MISC CTL 1 H	20,17																																																																																																																																																																																																																																																																																																																				
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DPM12 ALPCTL 6 H	12,10	DPM14 NEXT 01 H	20,14	DPM17 MISC CTL 3 H	20,17																																																																																																																																																																																																																																																																																																																				
DPM12 ALPCTL 7 H	12,10,05,01,06,02,07,03,00,04	DPM14 NEXT 02 H	14,20	DPM17 MISC CTL 4 H	20,17																																																																																																																																																																																																																																																																																																																				
DPM12 ALPCTL 8 H	12,10,05,01,06,02,07,03,00,04	DPM14 NEXT 03 H	14,20	DPM17 MKEN H	17																																																																																																																																																																																																																																																																																																																				
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<div>B</div> <table><tr><td colspan="2">THIS DRAWING AND SPECIFICATIONS ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT (C) 1980, DIGITAL EQUIPMENT CORPORATION.</td><td colspan="2">REVISIONS</td><td colspan="2">digital</td><td colspan="2">DRN. 027000</td><td colspan="2">DATE 21-MAR-80</td><td colspan="2">ENG. AC2</td><td colspan="2">DATE 11-MAR-80</td><td colspan="2">BOARD LOCATION: AC2</td><td colspan="2">SHEET 1 OF 1</td><td colspan="2">TITLE: DPM23 FORWARD REFERENCE</td></tr><tr><td colspan="2"></td><td colspan="2">CHK CHANGE NO. REV</td><td colspan="2"></td><td colspan="2">L160,1271 DPM23.DRW</td><td colspan="2">19-MAR-80 12:54</td><td colspan="2">NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0</td><td colspan="2">SIZE CODE</td><td colspan="2">NUMBER</td><td colspan="2">REV.</td><td colspan="2"></td></tr><tr><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2">FIRST USED ON OPTIDN/MODEL: 11/750</td><td colspan="2"></td><td colspan="2"></td><td colspan="2">D CS</td><td colspan="2">L0002-0-23</td><td colspan="2">B</td><td colspan="2"></td></tr></table> <div>A</div>								THIS DRAWING AND SPECIFICATIONS ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT (C) 1980, DIGITAL EQUIPMENT CORPORATION.		REVISIONS		digital		DRN. 027000		DATE 21-MAR-80		ENG. AC2		DATE 11-MAR-80		BOARD LOCATION: AC2		SHEET 1 OF 1		TITLE: DPM23 FORWARD REFERENCE				CHK CHANGE NO. REV				L160,1271 DPM23.DRW		19-MAR-80 12:54		NEXT HIGHER ASSEMBLY: B-DD-L0002-0-0		SIZE CODE		NUMBER		REV.										FIRST USED ON OPTIDN/MODEL: 11/750						D CS		L0002-0-23		B																																																																																																																																																																																																																																																									
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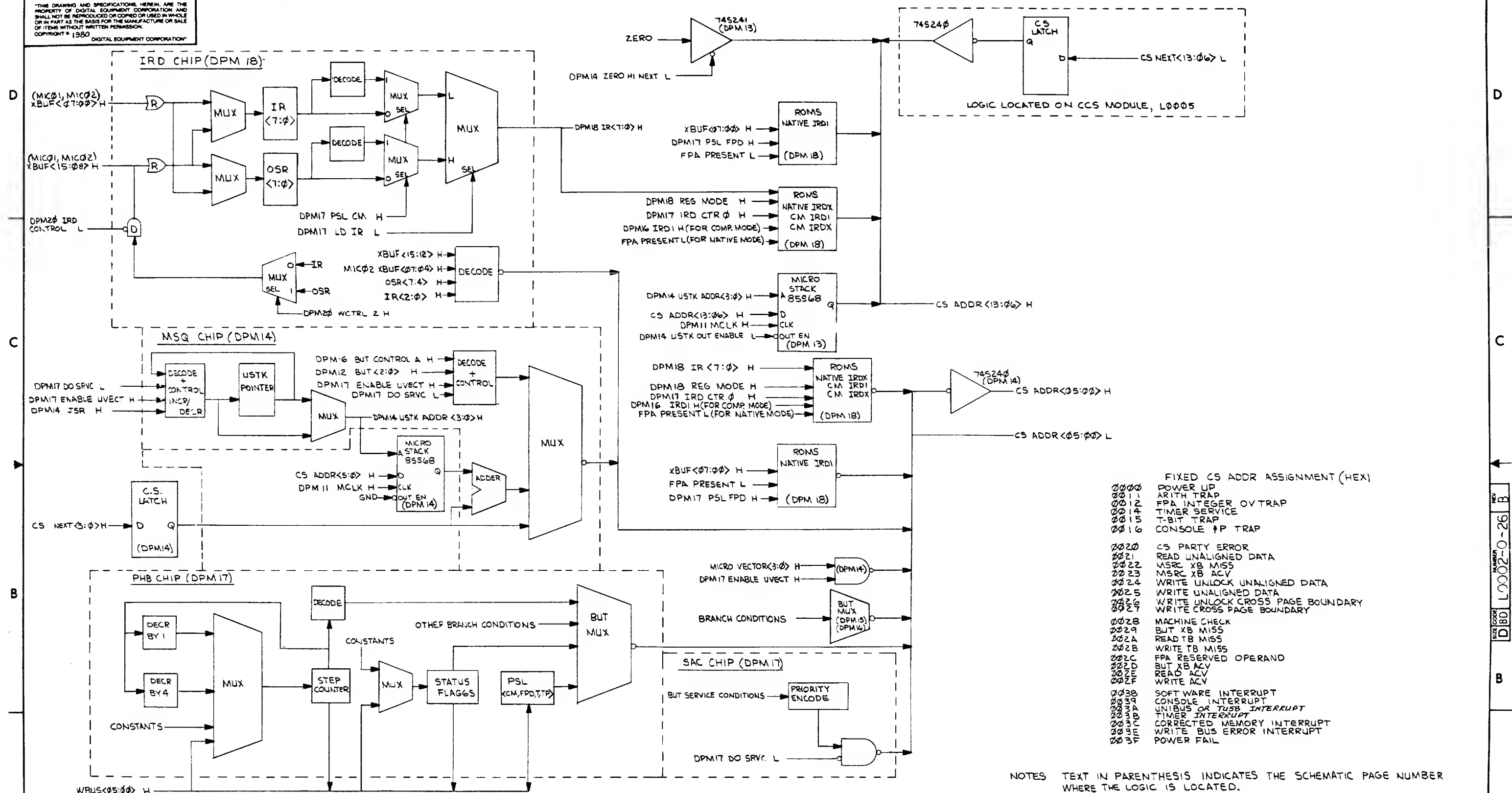
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CHK	CHANGE NO.	REV

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57. <u>Signal 57</u>	57
58. <u>Signal 58</u>	58
59. <u>Signal 59</u>	59
60. <u>Signal 60</u>	60
61. <u>Signal 61</u>	61
62. <u>Signal 62</u>	62
63. <u>Signal 63</u>	63
64. <u>Signal 64</u>	64
65. <u>Signal 65</u>	65
66. <u>Signal 66</u>	66
67. <u>Signal 67</u>	67
68. <u>Signal 68</u>	68
69. <u>Signal 69</u>	69
70. <u>Signal 70</u>	70
71. <u>Signal 71</u>	71
72. <u>Signal 72</u>	72
73. <u>Signal 73</u>	73
74. <u>Signal 74</u>	74
75. <u>Signal 75</u>	75
76. <u>Signal 76</u>	76
77. <u>Signal 77</u>	77
78. <u>Signal 78</u>	78
79. <u>Signal 79</u>	79
80. <u>Signal 80</u>	80
81. <u>Signal 81</u>	81
82. <u>Signal 82</u>	82
83. <u>Signal 83</u>	83
84. <u>Signal 84</u>	84
85. <u>Signal 85</u>	85
86. <u>Signal 86</u>	86
87. <u>Signal 87</u>	87
88. <u>Signal 88</u>	88
89. <u>Signal 89</u>	89
90. <u>Signal 90</u>	90
91. <u>Signal 91</u>	91
92. <u>Signal 92</u>	92
93. <u>Signal 93</u>	93
94. <u>Signal 94</u>	94
95. <u>Signal 95</u>	95
96. <u>Signal 96</u>	96
97. <u>Signal 97</u>	97
98. <u>Signal 98</u>	98
99. <u>Signal 99</u>	99
100. <u>Signal 100</u>	100

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```

0000    FIXED CS ADDR ASSIGNMENT (HEX)
0001    POWER UP
0002    ARITH TRAP
0003    FPA INTEGER OV TRAP
0004    TIMER SERVICE
0005    T-BIT TRAP
0006    CONSOLE ↑P TRAP

0020    CS PARTY ERROR
0021    READ UNALIGNED DATA
0022    MSRC XB MISS
0023    MSRC XB ACV
0024    WRITE UNLOCK UNALIGNED DATA
0025    WRITE UNALIGNED DATA
0026    WRITE UNLOCK CROSS PAGE BOUNDARY
0027    WRITE CROSS PAGE BOUNDARY

0028    MACHINE CHECK
0029    BUT XB MISS
002A    READ TB MISS
002B    WRITE TB MISS
002C    FPA RESERVED OPERAND
002D    BUT XB ACV
002E    READ ACV
002F    WRITE ACV

0030    SOFTWARE INTERRUPT
0031    CONSOLE INTERRUPT
0032    UNIBUS OR TUSB INTERRUPT
0033    TIMER INTERRUPT
0034    CORRECTED MEMORY INTERRUPT
0035    WRITE BUS ERROR INTERRUPT
0036    POWER FAIL

```

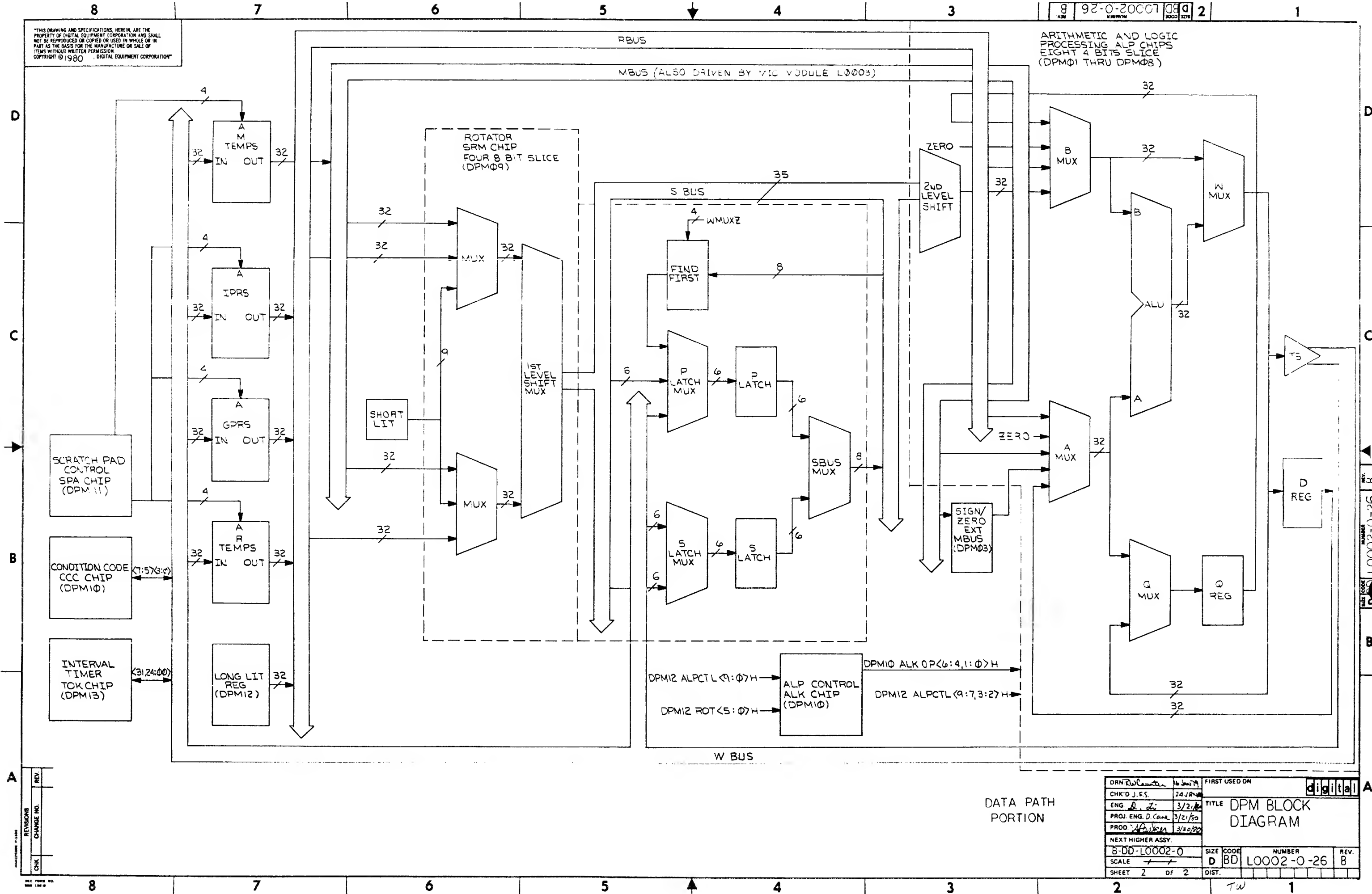
NOTES TEXT IN PARENTHESIS INDICATES THE SCHEMATIC PAGE NUMBER WHERE THE LOGIC IS LOCATED.

## MICRO SEQ. PORTION

OPN <i>Orion House</i>	DATE <i>16 Jan 80</i>	TITLE <i>DPM</i> <b>BLOCK DIAGRAM</b>		<div style="border: 1px solid black; padding: 2px; display: inline-block;">digital</div> <b>A</b>	
CHGT <i>J.F. Sullivan</i>	DATE <i>JAN 24 80</i>				
DESIGN <i>Dominic Li</i>	DATE <i>MAR 21, 80</i>				
RESP <i>Dominic Li</i>	DATE <i>MAR 21, 80</i>	DOCUMENT NUMBER			
UPLG ENG <i>W. Panken</i>	DATE <i>3/20/80</i>	SIZE	CODE	NUMBER	REV
NEXT HIGHER DOC <i>B-DD-10002-0</i>		<b>D</b>	<b>BD</b>	<i>L0002-0-26</i>	<b>B</b>
		SCALE <i>1/8"</i>	SHEET	<i>1 of 2</i>	

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ARITHMETIC AND LOGIC  
PROCESSING ALP CHIPS  
EIGHT 4 BITS SLICE  
(DPM01 THRU DPM08)



DATA PATH  
PORTION

DRN R/W Counter	16 bits	FIRST USED ON	digital
CHK'D J.E.S.	24 JAN 80	TITLE	DPM BLOCK DIAGRAM
ENG. D. Zi	3/21/80	SIZE	D BD
PROJ. ENG. D. Cane	3/21/80	NUMBER	LO002-0-26
PROD. J. J. J.	3/20/80	REV.	B
NEXT HIGHER ASSY.		SCALE	1
B-DD-LO002-0		SHEET	2 OF 2
SCALE		DIST.	
SHEET	2	OF	2



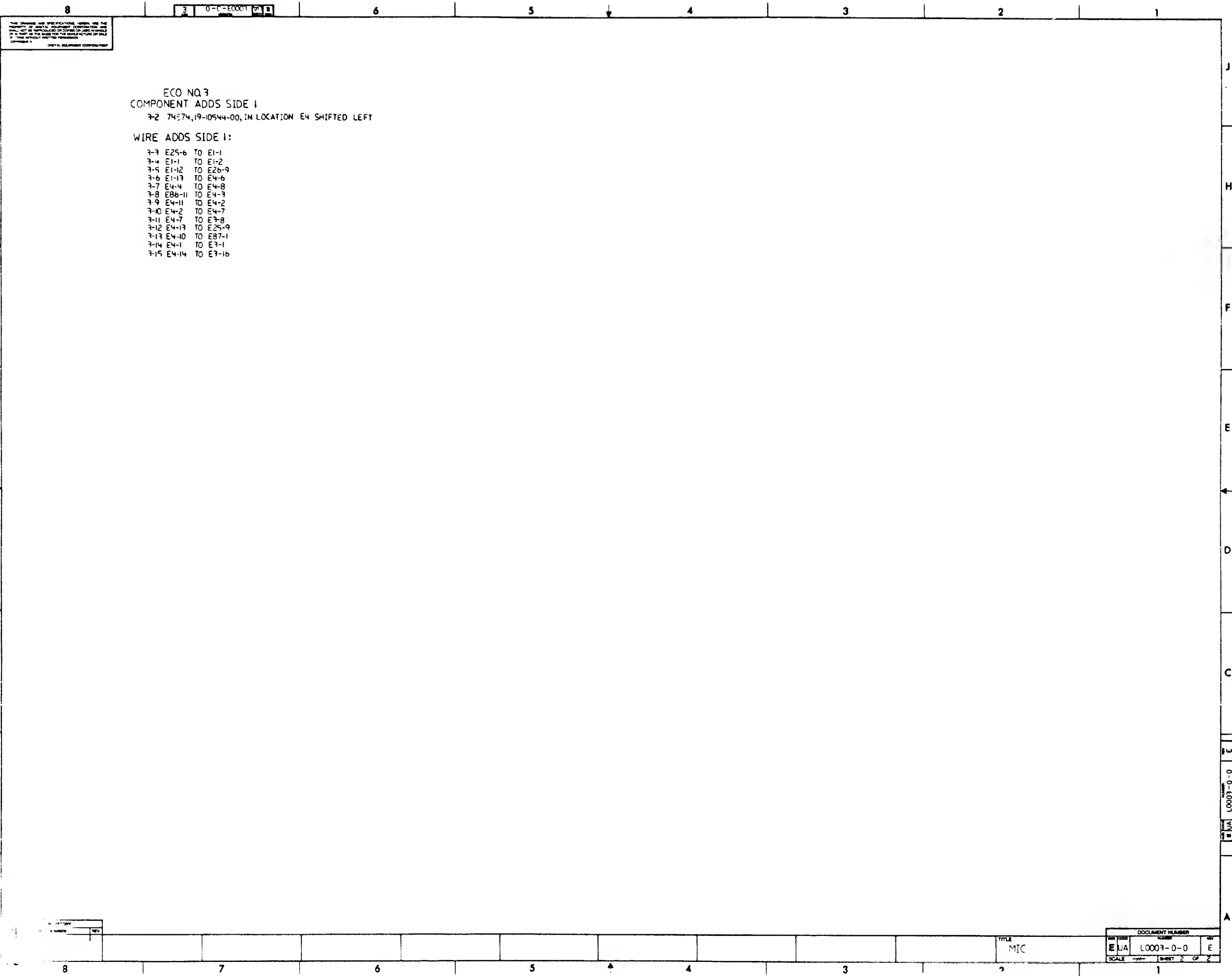






[illegible]





REV	DATE

DOCUMENT NUMBER	
EUA	L0003-0-0
SCALE 1:1	
SHEET 2 OF 2	

AUTOMATED BY PRTLST.3L(32)

PARTS LIST

SHEET A1 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY	PER VARIATION	REFERENCE DESIGNATOR
1	1	E-MD-5013693-0-0	5013693-00	DRILL & ETCH DRAWING	1		
2	2		1012784-00	.047 MFD 50V +80-20% CER	69		C9-C23,C25,C26,C29-C41,C43-C81
3	3	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6		C1-C6
4	4		1313580-00	360.0 .25 W 5.0 % CC	18		R1-R18
5	5		1910532-00	74500 NAND GATE-QUAD 2IN	1		E65
6	6		1910534-00	74504 INVERTER GATE-HEX 1I	2		E85,E87
7	7		1910536-00	74510 NAND GATE-TRIPLE 3IN	1		E91
8	8		1910542-00	74564 A-0-1 GATE 4-2-3-2	3		E24,E47,E89
9	9		1910547-00	745153 MUX 1 OF 4 (QUAD)	3		E100-E102
10	10		1910549-00	745158 MUX 1 OF 2 (QUAD)	2		E5,E79
11	11		1910541-00	74540 NAND GATE-DUAL 4IN,B	1		E38
12	12		1910957-00	745175 FF-D QUAD COMMON CLO	3		E3,E23,E25
13	13		1911573-00	745280 PARITY GEN/CHKR,9BIT	17		E6,E27,E48,E59,E68,E98,
						CONT	E105-E108,E111,E144,E145,E150,
						CONT	E157,E159,E166
14	14		1911641-00	SN 745257 MUX,QUAD 2 TO 1	4		E18,E39,E60,E80
15	15		1911712-00	74551 AND-OR GATE-INVERT 0	2		E26,E92
16	16		1916310-00	RAM,256X1,TRI STATE	2		E112,E143
17	17		1912096-00	OEC 74586 XOR GATE,QUAD 2IN	1		E96
18	18		1912097-00	SN 745182 LOOK AHD CARRY GEN	1		E164
19	19		1912388-00	74502 NOR GATE-QUAD 2IN,PO	1		E88
20	20		1912389-00	74508 AND GATE-QUAD 2IN,PO	1		E90
21	21		1912746-00	DEC 74537 NAND GATE-QUAD 2IN	2		E94,E95
22	22		1913340-00	74532 OR GATE-QUAD 2IN	2		E22,E93
23	23		1913493-00	745241 OCTAL BUFFER,TRI-STA	5		E7,E28,E49,E69,E97
24	24		1913662-00	RAM 1KX1 16 PIN TT	50		E8-E16,E29-E37,E50-E58,E70-E78,
						CONT	E117-E123,E129-E135
25	25		1913671-00	745374 FF-D OCTAL TRISTATE	5		E2,E139,E146,E153,E160
26	26		1913839-00	74LS165 SHIFT REG. 8BIT	2		E44,E84
27	27		1913888-00	OC 102A EQUALS CHECKER 8BIT	6		E109,E110,E151,E152,E158,E165

REVISION HISTORY		BASIC PART NO: L0003		DRN: O.SIREEN		DATE: 31-MAY-79		O I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: F.CAROFALO		DATE: 31-MAY-79		TITLE PARTS LIST	
INITIAL		B	SECTION VARIATION INDEX	DES.ENG: P.BINDER		DATE: 31-MAY-79		MIC	
D.L. L0003-TW001		C	[A] 00	RESP.ENG.: P.BINDER		DATE: 31-MAY-79		DOCUMENT NUMBER	
			[B]	MFG.ENG.: VANCE PARKER		DATE: 8-FEB-80		K	PL L0003-0-DBP
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:	EOIT #
			[D]	E-UA-L0003-0-0		8-DD-L0003-0-0		Z1258.PLS	11
			[E]						
			[F]						
			[G]						
			[H]						
			[I]						
			[J]						
			[K]						
			[L]						
			[M]						
			[N]						

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PARTS LIST

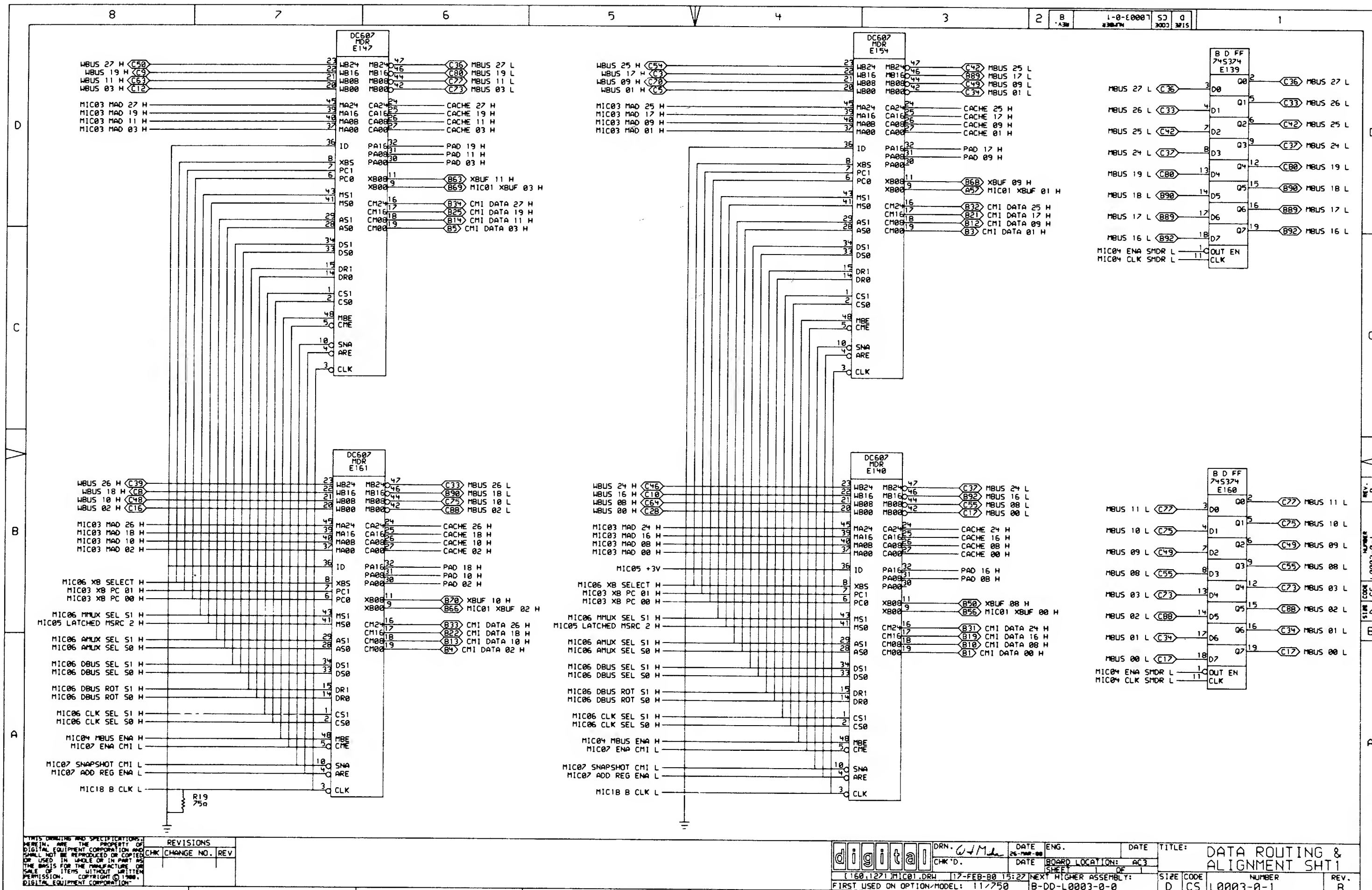
SHEET A2 OF A2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY 00	PER VARIATION	REFERENCE DESIGNATOR
28	28		1914085-00	745260 NOR GATE-DUAL,POS	3		E45,E67,E86
29	29		1914086-00	74530 NAND GATE-POS BIN	1		E64
30	30		1914681-00	DC 6078 BIPOLAR,LS,400-GATE	8	CONT	E140,E141,E147,E148,E154,E155, E161,E162
31	31		1914683-00	DC 609E BIPOLAR,LS,400-GATE	4		E142,E149,E156,E163
32	32		1914697-00	DC 623C BIPOLAR,LS,400-GATE	1		E103
33	33		1914698-00	DC 624E BIPOLAR,LS,400-GATE	1		E128
34	34		1914699-00	DC 6258 BIPOLAR,LS,400-GATE	1		E127
35	35		1914700-00	DC 6268 BIPOLAR,LS,400-GATE	1		E116
36	36		1914701-00	DC 6278 BIPOLAR,LS,400-GATE	1		E104
37	37		1914702-00	DC 6288 BIPOLAR,LS,400-GATE	1		E115
38	38		1915193-00	LS244 DRIVER,LINE OCTAL,T	1		E99
39	39		1915697-00	RAM 256X4 TRI-STATE	20	CONT	E19-E21,E40-E42,E61-E63,E81-E83, E113,E114,E124-E126,E136-E138 E66
40	40		1910537-00	74511 AND GATE-TRIPLE 3INP	1		
41	41		1210711-02	/REPLACED BY 12-16988-02	1		
42	42		9000024-01	EYELET, ROLLED FLANGE, .121 OD X	12		
43	43		1503121-00	2N 2369 NPN 350MW SI N	1		Q1
44	44		1302379-00	75.0 .25 W 5.0 % CC	4		R19-R21,R23
45	45		9009185-00	JUMPER, WIRE, INSULATED, BLACK 8	1		R22
46	46		1910544-00	74574 FF-D DUAL EDGE TRIGG	1		E46
47	47		1910878-00	7427 NOR GATE-TRIPLE 3IN	2		E43,E1
48	48		1215924-00	SOCKET IC W/METAL CONT	18	CONT	XE103,XE104,XE115,XE116,XE127, XE128,XE140-XE142,XE147-XE149, CONT XE154-XE156,XE161-XE163
49	49		1215935-00	GASKET, THERMAL .50"X.80"	18		
50	50		1215936-00	HEAT SINK, FORCED CONVECTION	18		

S1 NOTE: SPARE I.C. LOCATIONS ARE E4,E17  
S2 NOTE: SOME MODULES WILL HAVE 10-05305 INSTEAD OF 10-12084-01

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
+	+	+	+	+	+	+	MIC		K	PL	L0003-0-DBP	C

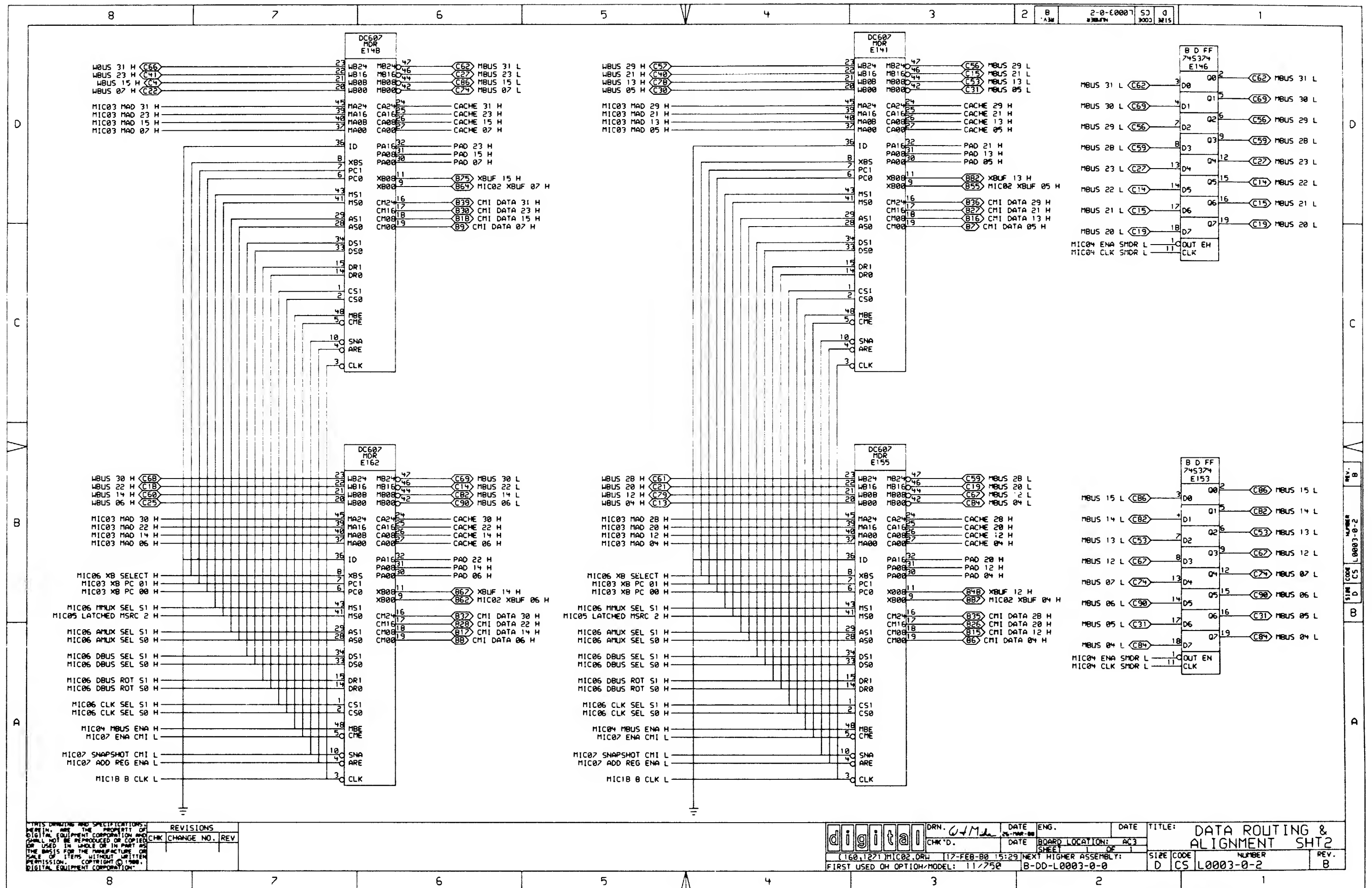


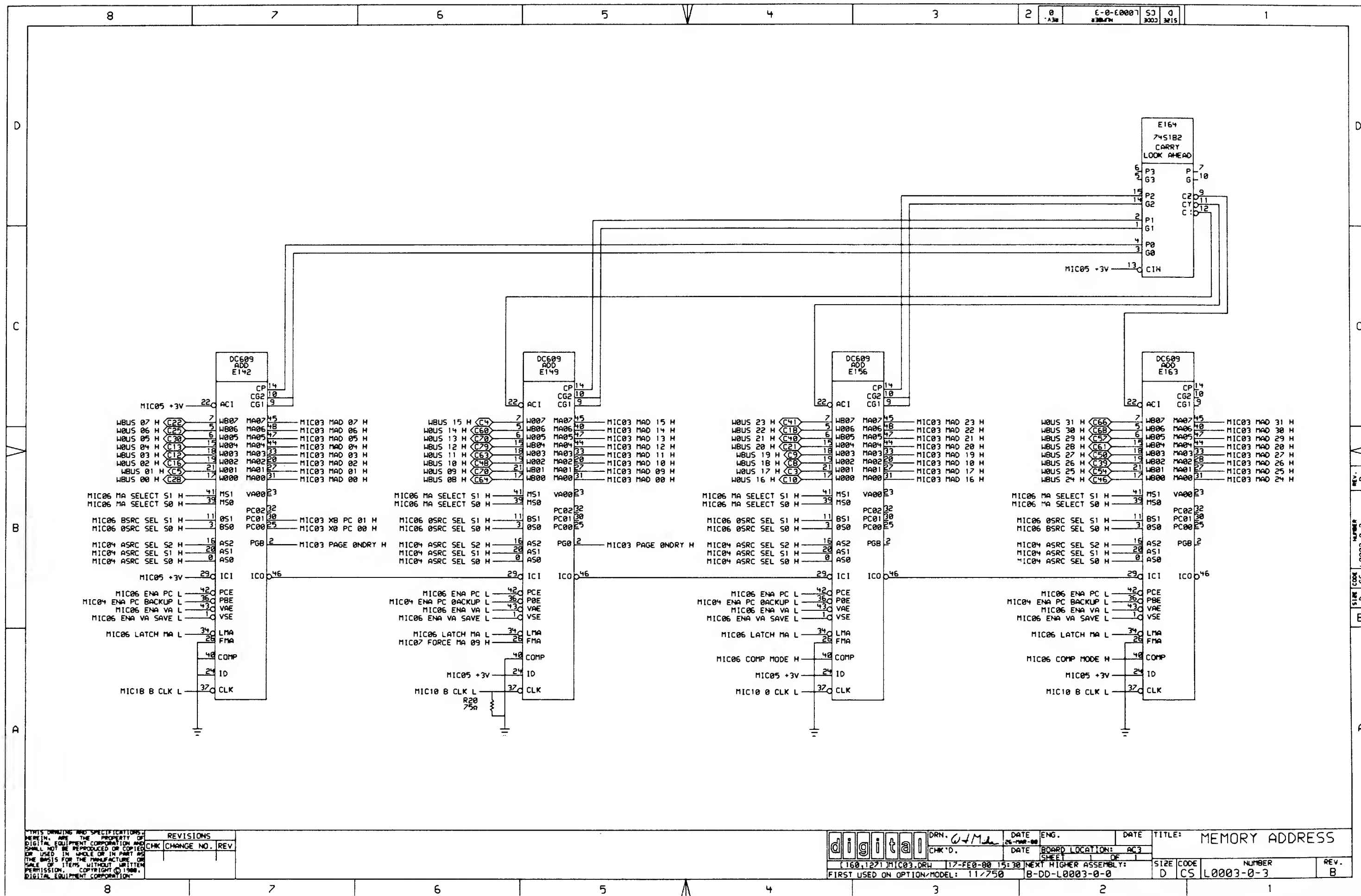


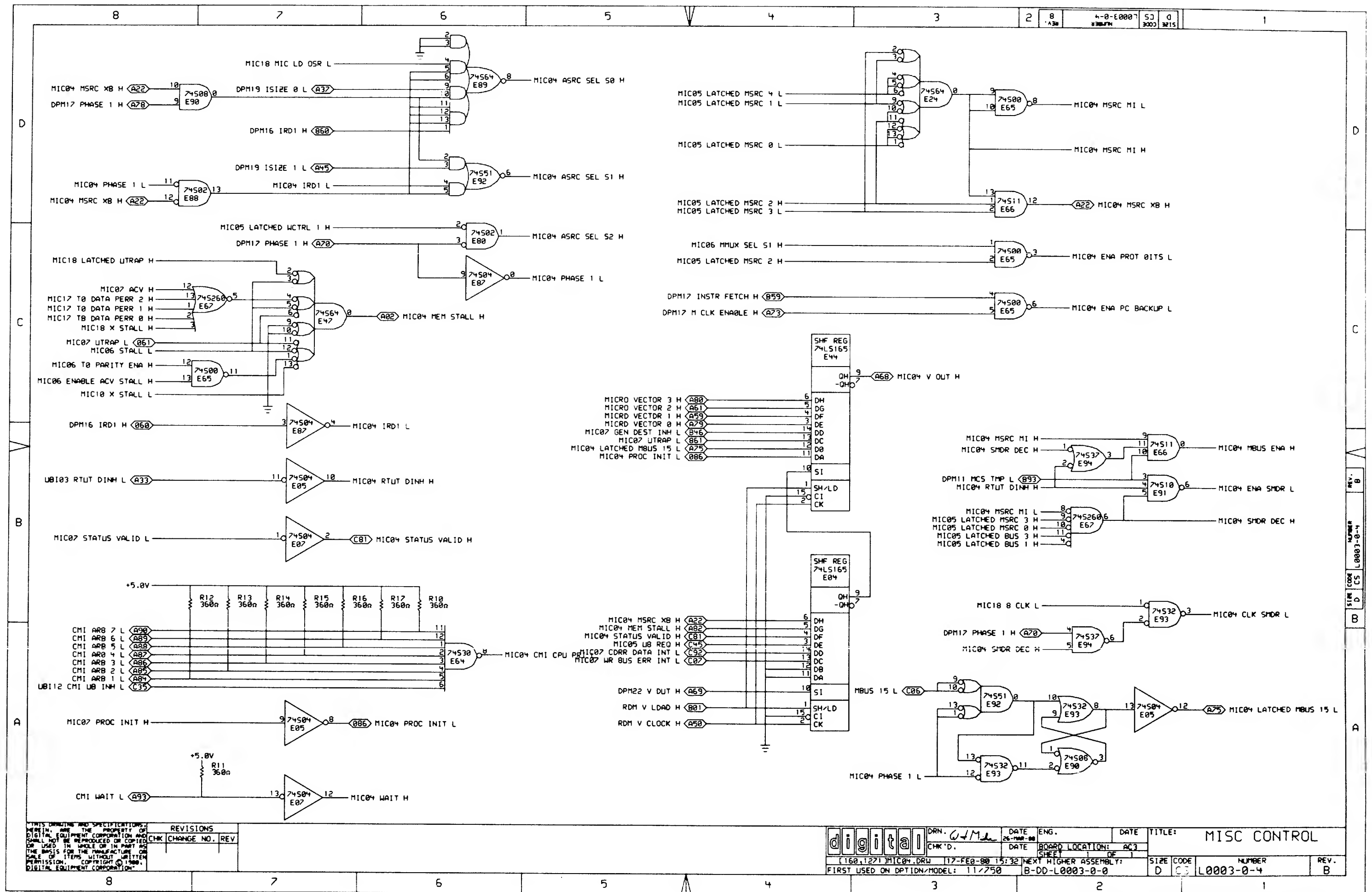
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REVISIONS	
CHK	CHANGE NO. REV

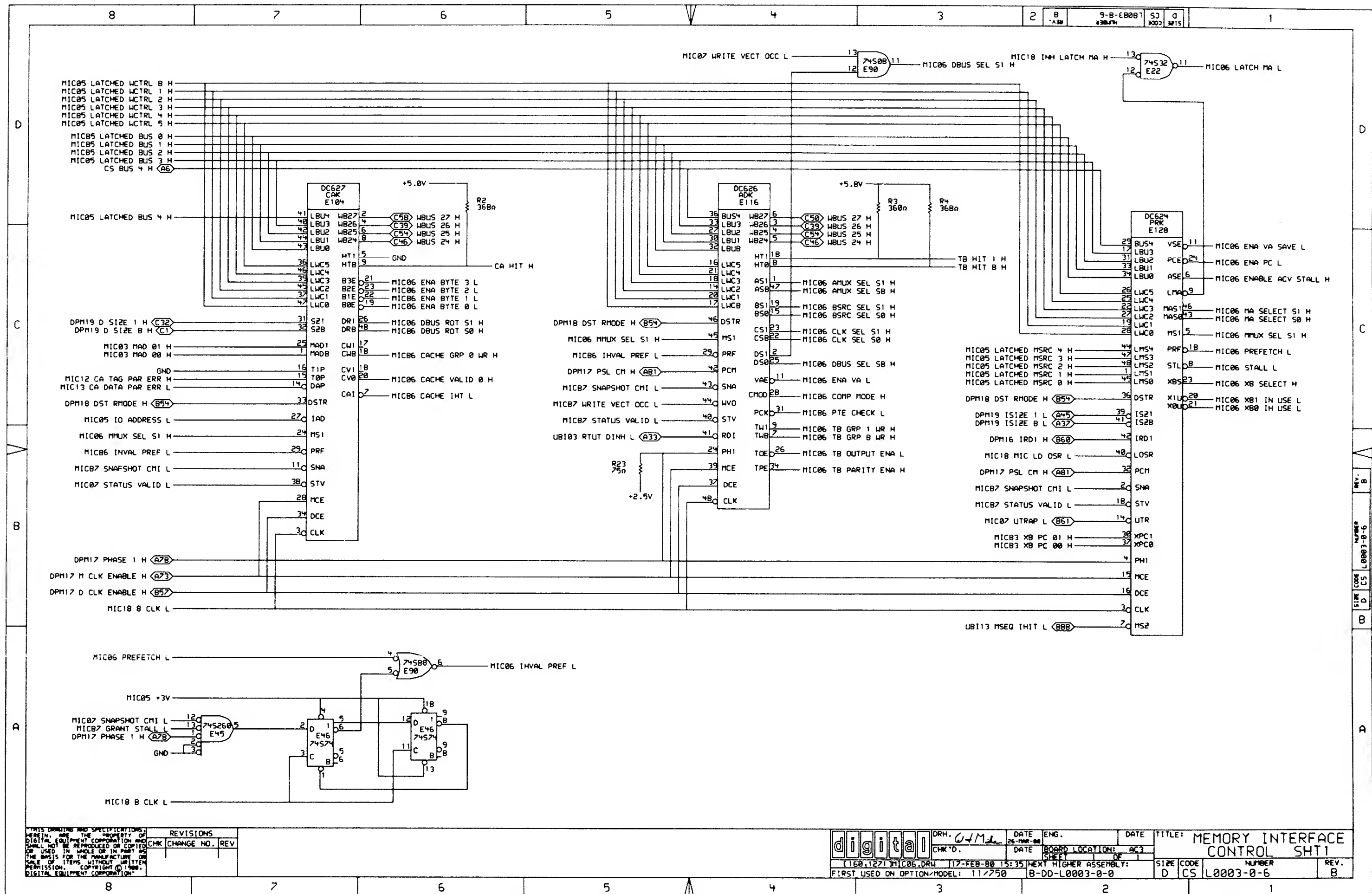
digital	DRN. 64M	DATE 26-FEB-88	ENG.	DATE	TITLE: DATA ROUTING & ALIGNMENT SHT1		
	CHK'D.	DATE	BOARD LOCATION: AC3	SHEET 1 OF 1			
FIRST USED ON OPTION/MODEL: 11/250					SIZE CODE D CS	NUMBER L0003-0-1	REV. B

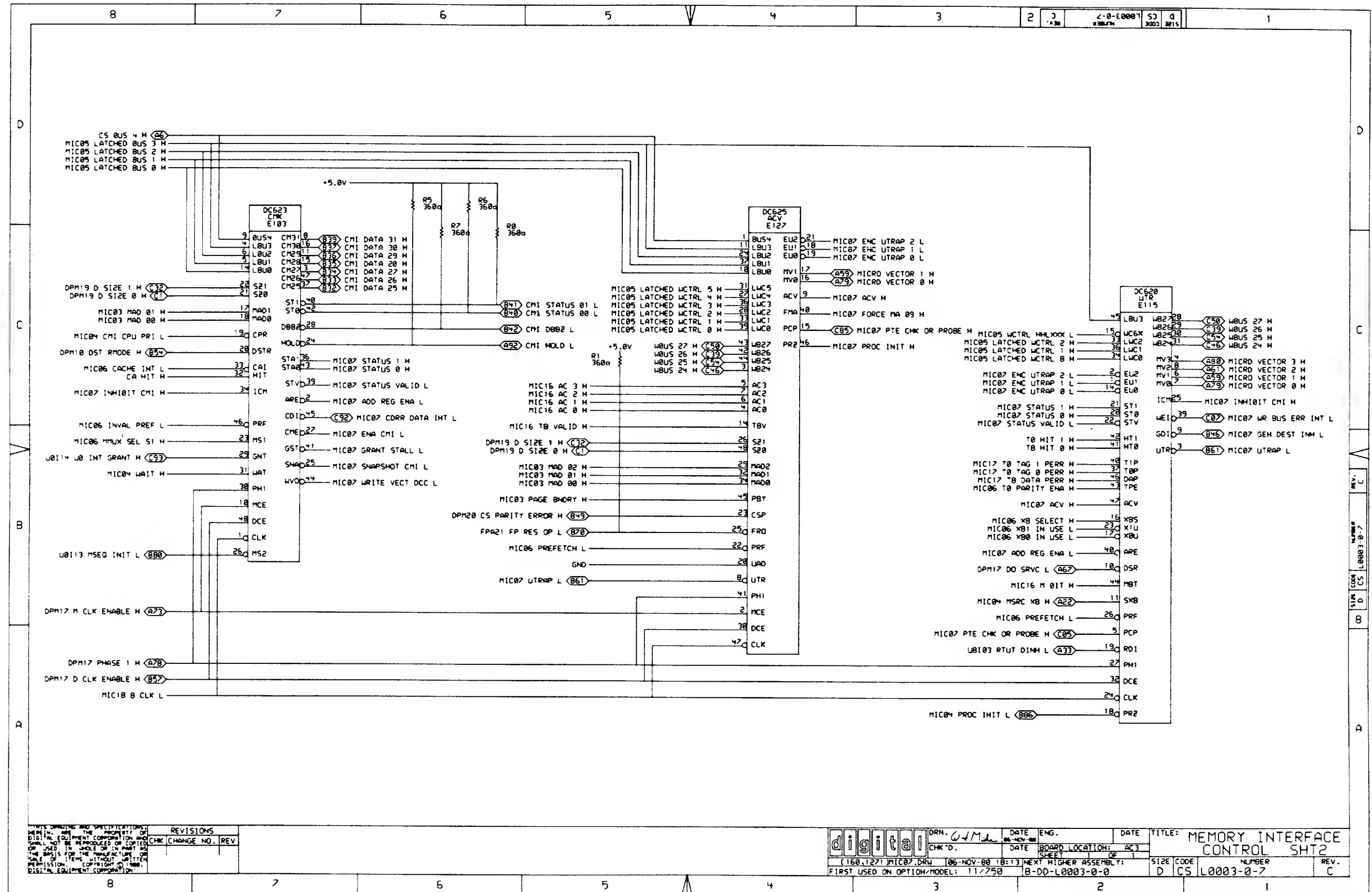




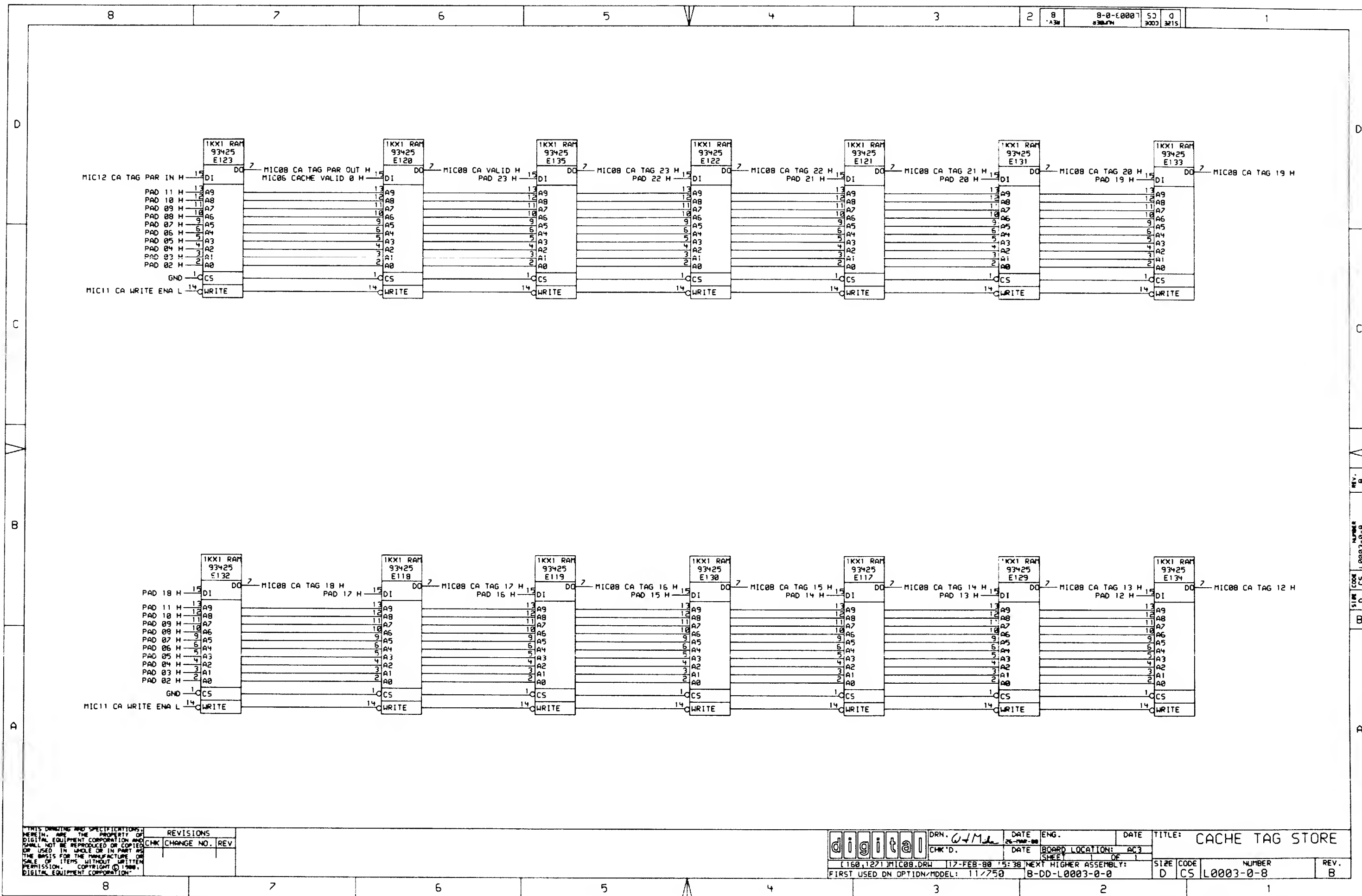










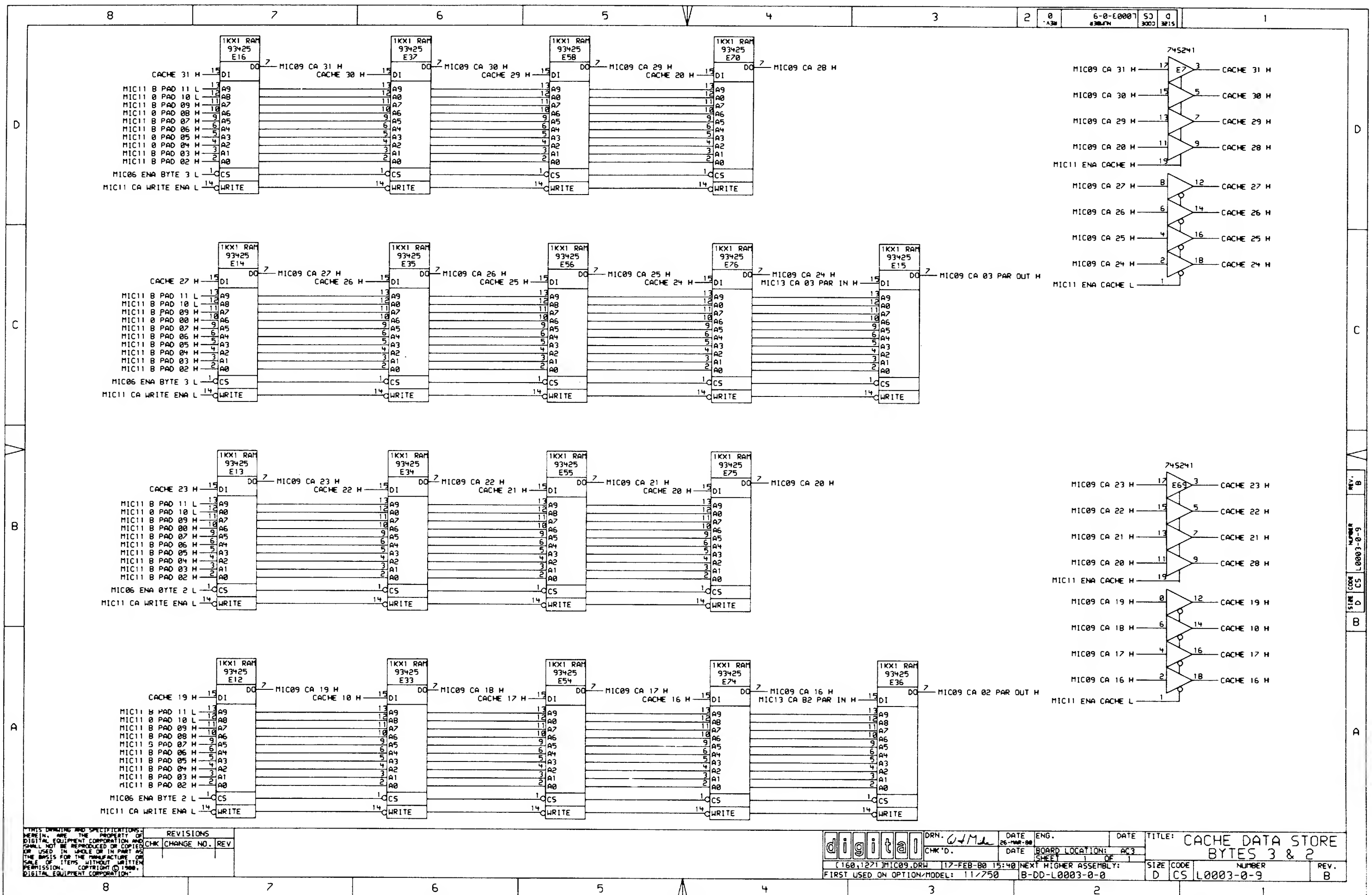


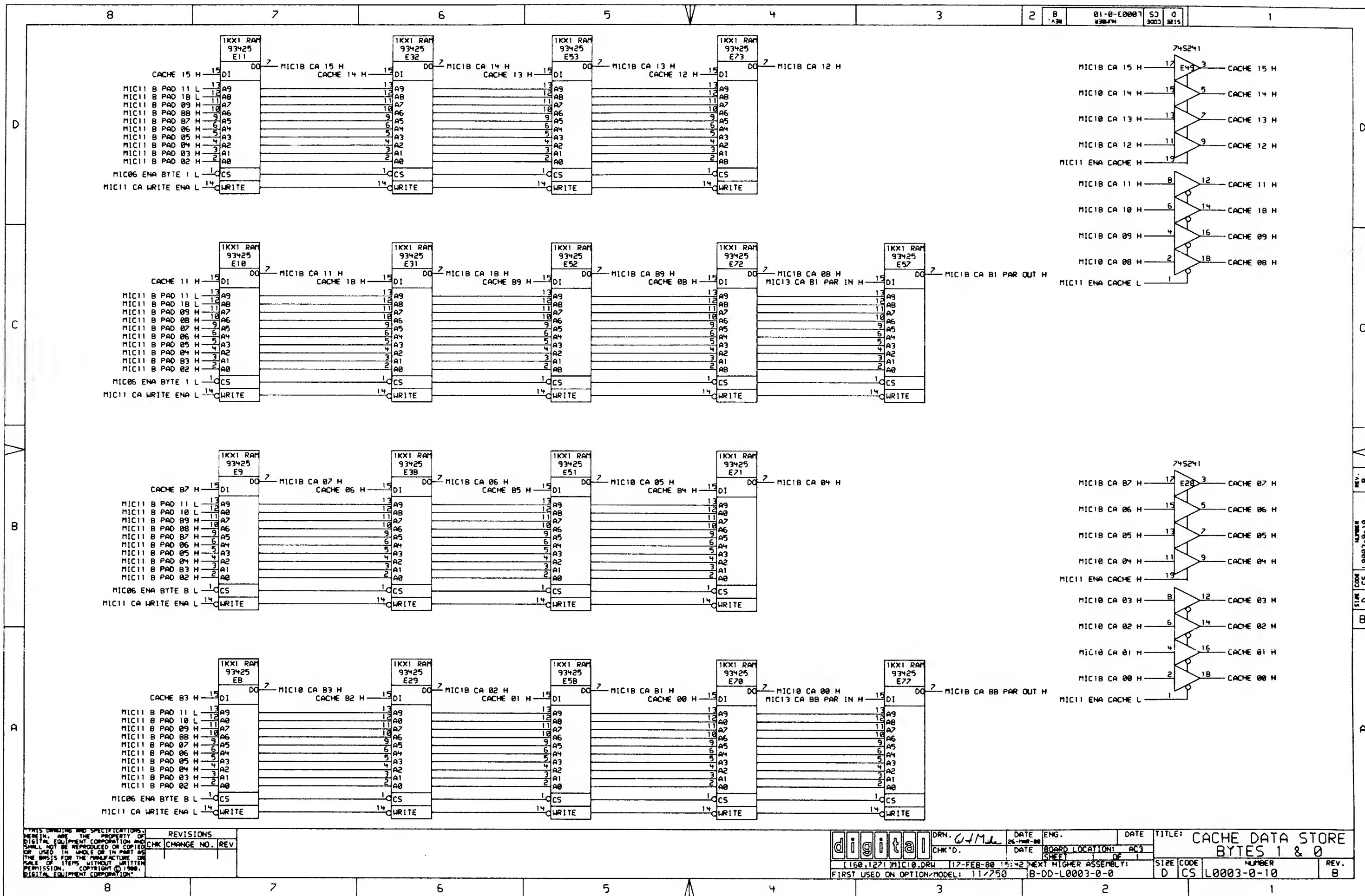
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. 64M	DATE 25-MAR-80	ENG.	DATE	TITLE: CACHE TAG STORE
	CHK'D.	DATE 11-FEB-80	BOARD LOCATION: AC3	SHEET 1 OF 1	
[160,127]MIC08.DRW 117-FEB-80 15:38 NEXT HIGHER ASSEMBLY:					SIZE CODE
FIRST USED ON OPTION/MODEL: 11/750					D CS L0003-0-8
					NUMBER
					REV. B



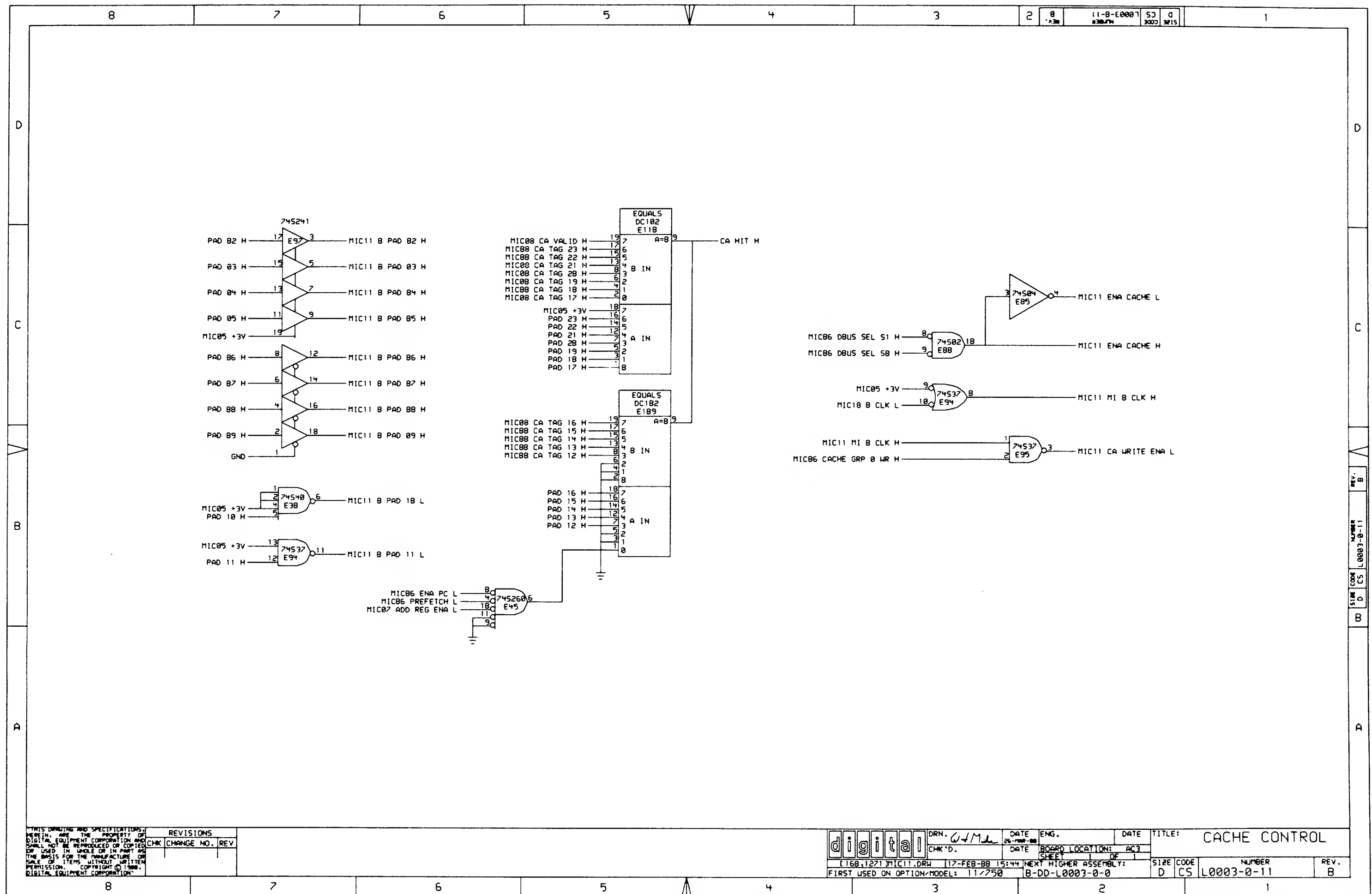




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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. 04Mde	DATE 26-FEB-80	ENG.	DATE	TITLE: CACHE DATA STORE
	CHK'D.	DATE 17-FEB-80	BOARD LOCATION: AC3	SHEET 1 OF 1	BYTES 1 & 0
FIRST USED ON OPTION/MODEL: 117750		NEXT HIGHER ASSEMBLY: B-DD-L0003-0-0		SIZE CODE D CS	NUMBER L0003-0-10
				REV. B	



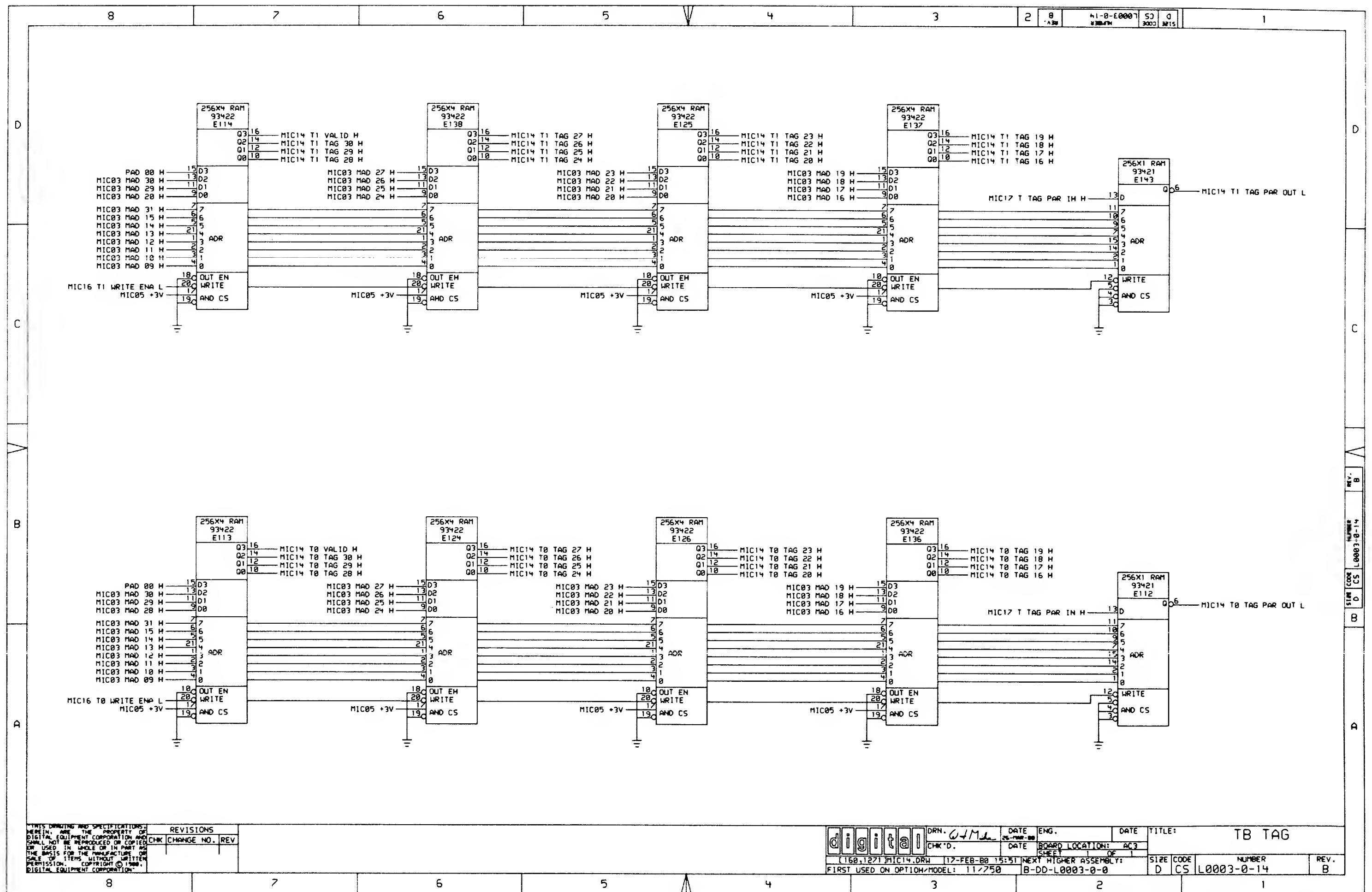
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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. W.M.	DATE 26-MAR-88	ENG.	DATE	TITLE: CACHE CONTROL	
	CHK'D.	DATE 17-FEB-88	DATE	BOARD LOCATION: AC3		
[168,127] MIC11.DRW		NEXT HIGHER ASSEMBLY:		SIZE CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0003-0-0		D CS	L0003-0-11	B







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REVISIONS  
CHK CHANGE NO. REV

digital

DRN. G4M  
CHK'D.

DATE 26-MAR-88  
DATE

ENG. BOARD LOCATION: AC3  
SHEET 1 OF 1

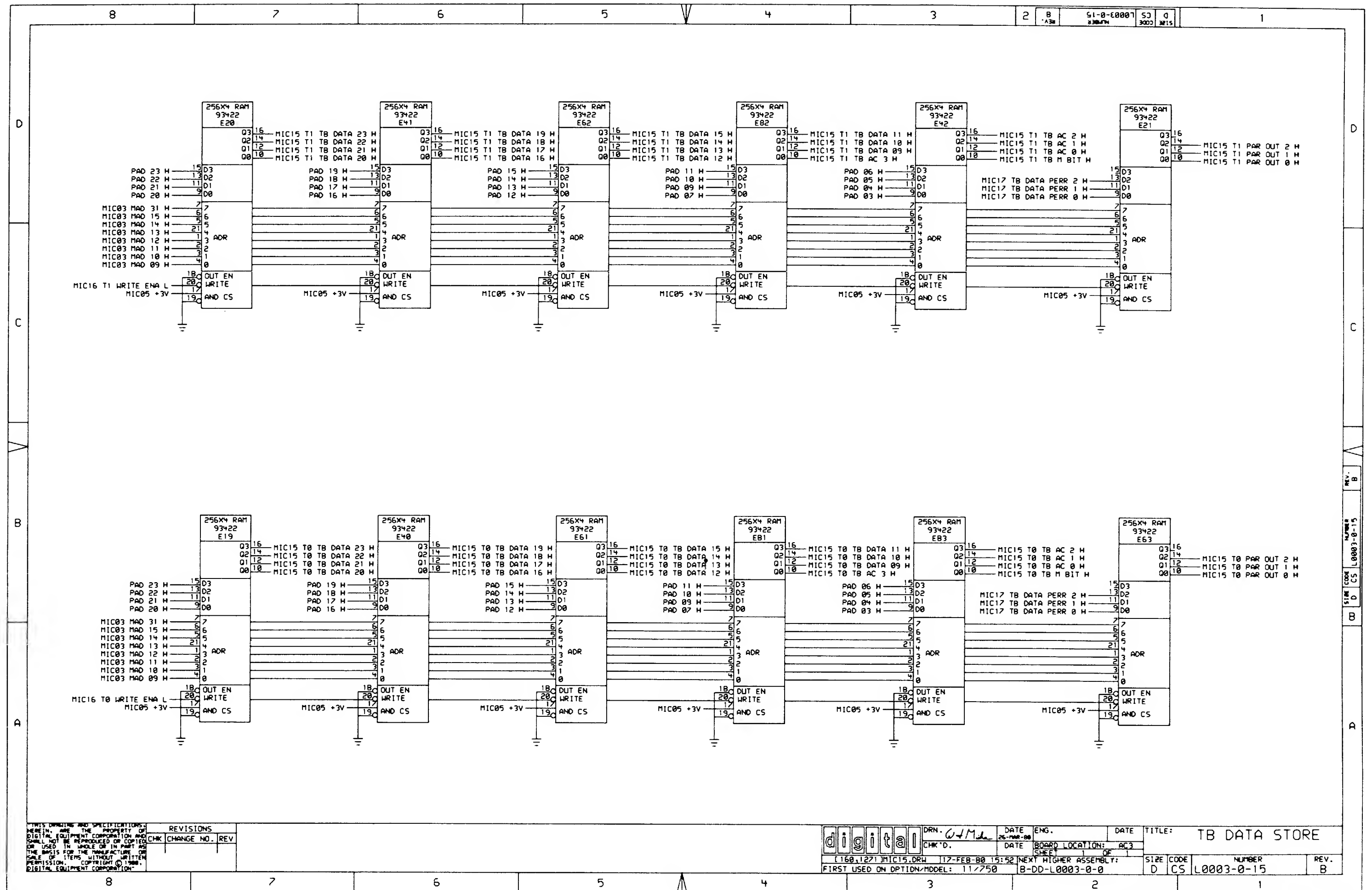
DATE

TITLE: TB TAG

160,1271 MIC14.DRW 17-FEB-88 15:51 NEXT HIGHER ASSEMBLY:  
FIRST USED ON OPTION/MODEL: 117750 B-DD-L0003-0-0

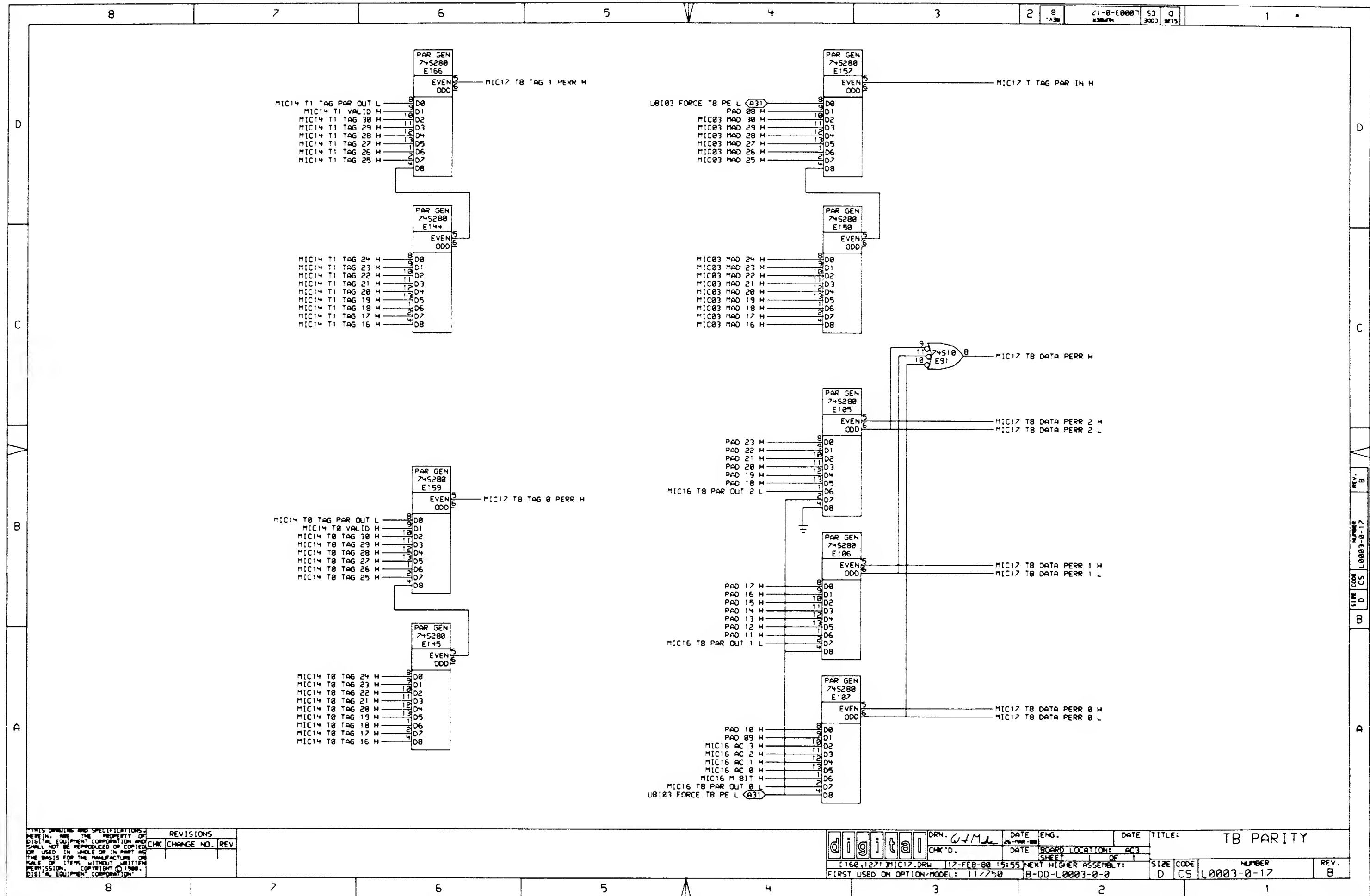
SIZE CODE D CS L0003-0-14

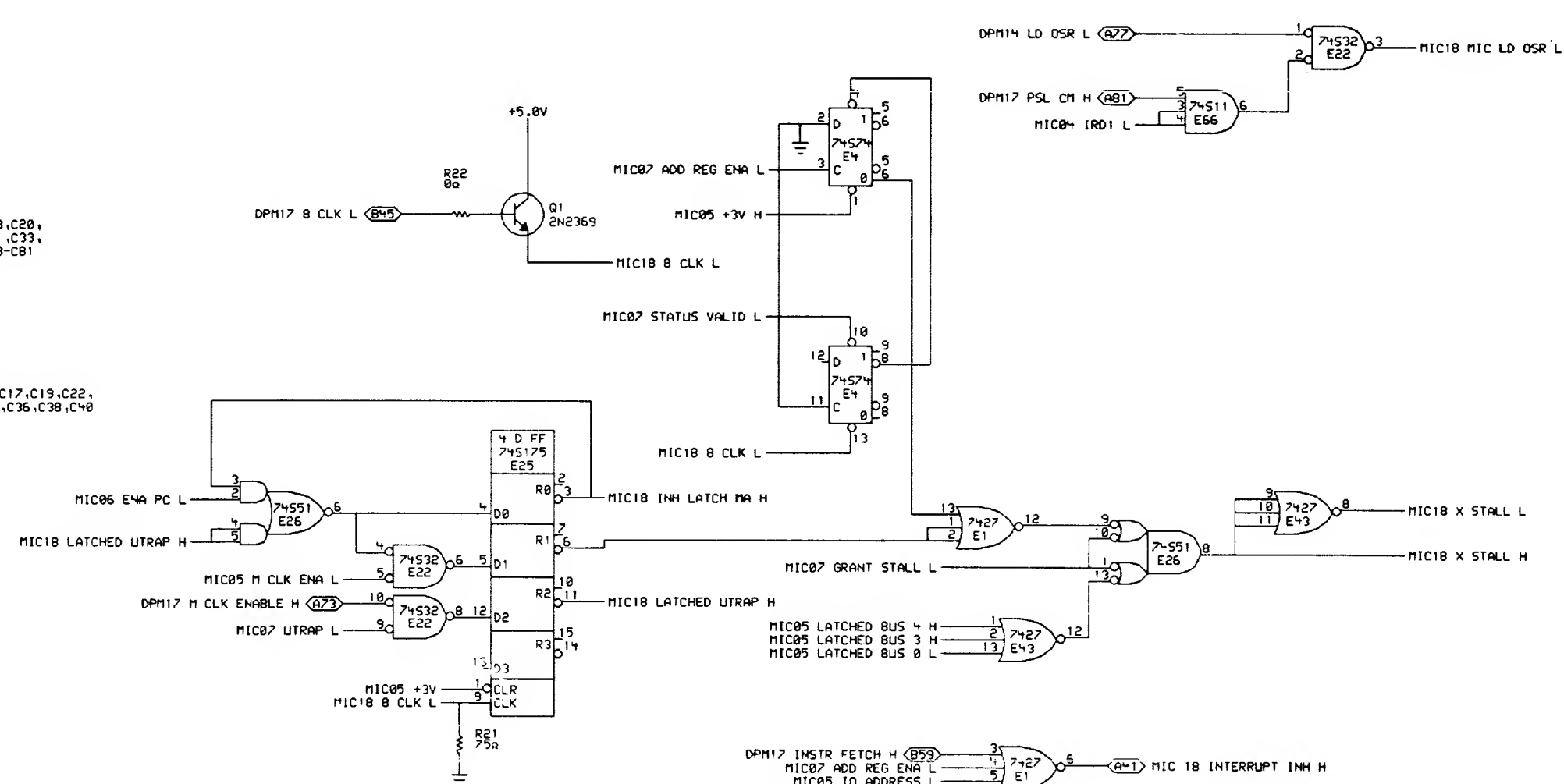
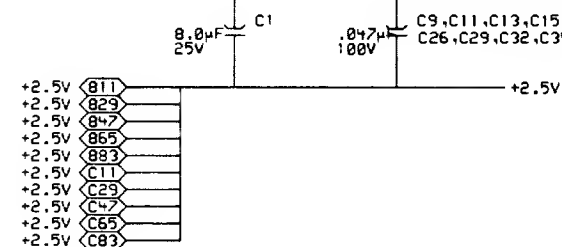
NUMBER REV. B











- NOTES:
1. SPARE LOCATION IS E17
  2. FOR TEST PURPOSES, THE +5V INNER PLANE IS SPLIT INTO THREE SECTIONS.  
FINGER PINS A20,A38,A58,A8,820,838,858,876  
SUPPLY POWER TO:  
E1-E102,E108-E114,E118-E126,R2,R5-R7,R9,R11-R18.  
FINGER PINS C20 AND C38 SUPPLY POWER TO:  
E103-E107,E115,E117,E127-E138,E143,E145,  
E151,E152,E158,E159,R1,R3,R4,R8,R10,Q1.  
FINGER PINS C58 AND C76 SUPPLY POWER TO:  
E139-E142,E146-E150,E153-E157,E160-E166.

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REVISIONS		
CHK	CHANGE NO.	REV

digital

DRN.

	DATE
--	------

E ENG

---

---

DATE \_\_\_\_\_

[illegible]

3

MIC

---

CON

TDQ

---

**Abstract**

8	7	6	5	4	3	2	1
61-0-00001 838674							
SD 0 3002 3015							
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)	
CA HIT H		07,06,11		CMI DATA 10 H		01	
CACHE 00 H		13,10,01		CMI DATA 11 H		01	
CACHE 01 H		13,10,01		CMI DATA 12 H		02	
CACHE 02 H		13,10,01		CMI DATA 13 H		02	
CACHE 03 H		10,13,01		CMI DATA 14 H		02	
CACHE 04 H		13,10,02		CMI DATA 15 H		02	
CACHE 05 H		13,10,02		CMI DATA 16 H		01	
CACHE 06 H		13,10,02		CMI DATA 17 H		01	
CACHE 07 H		10,13,02		CMI DATA 18 H		01	
CACHE 08 H		13,10,01		CMI DATA 19 H		01	
CACHE 09 H		13,10,01		CMI DATA 20 H		02	
CACHE 10 H		10,13,01		CMI DATA 21 H		02	
CACHE 11 H		10,13,01		CMI DATA 22 H		02	
CACHE 12 H		13,10,02		CMI DATA 23 H		02	
CACHE 13 H		13,10,02		CMI DATA 24 H		01	
CACHE 14 H		10,13,02		CMI DATA 25 H		07,01	
CACHE 15 H		10,13,02		CMI DATA 26 H		07,01	
CACHE 16 H		13,09,01		CMI DATA 27 H		07,01	
CACHE 17 H		09,13,01		CMI DATA 28 H		07,02	
CACHE 18 H		09,13,01		CMI DATA 29 H		07,02	
CACHE 19 H		09,13,01		CMI DATA 30 H		07,02	
CACHE 20 H		13,09,02		CMI DATA 31 H		07,02	
CACHE 21 H		09,13,02		CMI DB02 L		07	
CACHE 22 H		09,13,02		CMI HOLD L		07	
CACHE 23 H		09,13,02		CMI STATUS 00 L		07	
CACHE 24 H		13,09,01		CMI STATUS 01 L		07	
CACHE 25 H		13,09,01		CMI WAIT L		04	
CACHE 26 H		13,09,01		CS BUS 0 H		05	
CACHE 27 H		13,09,01		CS BUS 1 H		05	
CACHE 28 H		13,09,02		CS BUS 2 H		05	
CACHE 29 H		13,09,02		CS BUS 3 H		05	
CACHE 30 H		13,09,02		CS BUS 4 H		05,07,06	
CACHE 31 H		13,09,02		CS MSRC 0 H		05	
CMI AR0 1 L		04		CS MSRC 1 H		05	
CMI AR0 2 L		04		CS MSRC 2 H		05	
CMI AR0 3 L		04		CS MSRC 3 H		05	
CMI AR0 4 L		04		CS MSRC 4 H		05	
CMI AR0 5 L		04		CS WCTRL 0 H		05	
CMI AR0 6 L		04		CS WCTRL 1 H		05	
CMI AR0 7 L		04		CS WCTRL 2 H		05	
CMI DATA 00 H		01		CS WCTRL 3 H		05	
CMI DATA 01 H		01		CS WCTRL 4 H		05	
CMI DATA 02 H		01		CS WCTRL 5 H		05	
CMI DATA 03 H		01		DPM11 MCS TMR L		04	
CMI DATA 04 H		02		DPM14 LD OSR L		10	
CMI DATA 05 H		02		DPM16 IRDI H		04,06	
CMI DATA 06 H		02		DPM17 B CLK L		18	
CMI DATA 07 H		02		DPM17 D CLK ENABLE H		07,06	
CMI DATA 08 H		01		DPM17 DO SRVC L		07	
CMI DATA 09 H		01		DPM17 INSTR FETCH H		04,10	
DPM17 M CLK ENABLE H		10,04,05,07,06		DPM17 PHASE 1 H		06,04,07	
DPM17 PSL CM H		10,06		DPM18 DST RMODE H		07,06	
DPM19 D SIZE 0 H		07,06		DPM19 D SIZE 1 H		07,06	
DPM19 D SIZE 1 H		07,06		DPM19 ISIZE 0 L		04,06	
DPM19 ISIZE 0 L		04,06		DPM19 ISIZE 1 L		04,06	
DPM20 CS PARITY ERROR H		07		DPM22 V OUT H		04	
FPA21 FP RES OP L		07		MBUS 00 L		01	
MBUS 00 L		01		MBUS 01 L		01	
MBUS 01 L		01		MBUS 02 L		01	
MBUS 02 L		01		MBUS 03 L		01	
MBUS 03 L		01		MBUS 04 L		02	
MBUS 04 L		02		MBUS 05 L		02	
MBUS 05 L		02		MBUS 06 L		02	
MBUS 06 L		02		MBUS 07 L		02	
MBUS 07 L		02		MBUS 08 L		01	
MBUS 08 L		01		MBUS 09 L		01	
MBUS 09 L		01		MBUS 10 L		01	
MBUS 10 L		01		MBUS 11 L		01	
MBUS 11 L		01		MBUS 12 L		02	
MBUS 12 L		02		MBUS 13 L		02	
MBUS 13 L		02		MBUS 14 L		02	
MBUS 14 L		02		MBUS 15 L		04,02	
MBUS 15 L		04,02		MBUS 16 L		01	
MBUS 16 L		01		MBUS 17 L		01	
MBUS 17 L		01		MBUS 18 L		01	
MBUS 18 L		01		MBUS 19 L		01	
MBUS 19 L		01		MBUS 20 L		02	
MBUS 20 L		02		MBUS 21 L		02	
MBUS 21 L		02		MBUS 22 L		02	
MBUS 22 L		02		MBUS 23 L		02	
MBUS 23 L		02		MBUS 24 L		01	
MBUS 24 L		01		MBUS 25 L		01	
MBUS 25 L		01		MBUS 26 L		01	
MBUS 26 L		01		MBUS 27 L		01	
MBUS 27 L		01		MBUS 28 L		02	
MBUS 28 L		02		MBUS 29 L		02	
MBUS 29 L		02		MBUS 30 L		02	
MBUS 30 L		02		MBUS 31 L		02	
MBUS 31 L		02		MIC01 XBUF 00 H		01	
MIC01 XBUF 00 H		01		MIC01 XBUF 01 H		01	
MIC01 XBUF 01 H		01		MIC01 XBUF 02 H		01	
MIC01 XBUF 02 H		01		MIC01 XBUF 03 H		01	
MIC01 XBUF 03 H		01		MIC02 XBUF 04 H		02	
MIC02 XBUF 04 H		02		MIC02 XBUF 05 H		02	
MIC02 XBUF 05 H		02		MIC02 XBUF 06 H		02	
MIC02 XBUF 06 H		02					
NOTES:							
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.							
DATE: 06-NOV-00 DRAWN: W.M. DATE: 06-NOV-00 CHECKED: DATE: 06-NOV-00 SHEET: 1 OF 1 TITLE: MIC FORWARD REFERENCE FIRST USED ON OPTION/MODEL: 11-750 B-DD-L0003-0-0 SIZE: D CODE: CS NUMBER: L0003-0-19 REV: C							

8	7	6	5	4	3	2	1
02-8-00007		50	0				
3002		3002	3015				
D		C		B		A	
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)	
MIC02 XBUF 07 H		02		MIC04 MSRC X8 H		04,07	
MIC03 MAD 00 H		07,06,01,03		MIC04 PHASE 1 L		04	
MIC03 MAD 01 H		07,06,03,01		MIC04 PROC INIT L		04,07	
MIC03 MAD 02 H		07,03,01		MIC04 RTUT DINH H		04	
MIC03 MAD 03 H		03,01		MIC04 SMOR DEC H		04	
MIC03 MAD 04 H		03,02		MIC04 STATUS VALID H		04	
MIC03 MAD 05 H		02,03		MIC04 V OUT H		04	
MIC03 MAD 06 H		03,02		MIC04 WAIT H		04,07	
MIC03 MAD 07 H		03,02		MIC05 +3V		05,13,15,18,11,06,16,14,01,03	
MIC03 MAD 08 H		01,03		MIC05 CS CLK L		05	
MIC03 MAD 09 H		15,14,03,01		MIC05 ID ADDRESS L		05,06,10	
MIC03 MAD 10 H		15,14,03,01		MIC05 LATCHED BUS 0 H		05,07,06	
MIC03 MAD 11 H		15,14,01,03		MIC05 LATCHED BUS 0 L		05,10	
MIC03 MAD 12 H		15,14,03,02		MIC05 LATCHED BUS 1 H		05,04,07,06	
MIC03 MAD 13 H		15,14,02,03		MIC05 LATCHED BUS 2 H		05,07,06	
MIC03 MAD 14 H		15,14,03,02		MIC05 LATCHED BUS 3 H		05,10,04,07,06	
MIC03 MAD 15 H		15,14,02,03		MIC05 LATCHED BUS 4 H		05,10,06	
MIC03 MAD 16 H		14,01,17,16,03		MIC05 LATCHED MSRC 0 H		05,04,06	
MIC03 MAD 17 H		14,17,16,01,03		MIC05 LATCHED MSRC 0 L		05,04	
MIC03 MAD 18 H		14,17,16,03,01		MIC05 LATCHED MSRC 1 H		05,06	
MIC03 MAD 19 H		14,01,17,16,03		MIC05 LATCHED MSRC 1 L		05,04	
MIC03 MAD 20 H		14,17,16,02,03		MIC05 LATCHED MSRC 2 H		05,04,06,01,02	
MIC03 MAD 21 H		14,02,17,16,03		MIC05 LATCHED MSRC 3 H		05,04,06	
MIC03 MAD 22 H		14,17,16,03,02		MIC05 LATCHED MSRC 3 L		05,04	
MIC03 MAD 23 H		14,02,17,16,03		MIC05 LATCHED MSRC 4 H		05,06	
MIC03 MAD 24 H		14,01,17,16,03		MIC05 LATCHED MSRC 4 L		05,04	
MIC03 MAD 25 H		14,01,17,16,03		MIC05 LATCHED WCTRL 0 H		05,06,07	
MIC03 MAD 26 H		14,17,16,01,03		MIC05 LATCHED WCTRL 1 H		05,04,06,07	
MIC03 MAD 27 H		14,01,17,16,03		MIC05 LATCHED WCTRL 2 H		05,06,07	
MIC03 MAD 28 H		14,02,17,16,03		MIC05 LATCHED WCTRL 3 H		05,06,07	
MIC03 MAD 29 H		14,02,17,16,03		MIC05 LATCHED WCTRL 3 L		05	
MIC03 MAD 30 H		14,17,16,02,03		MIC05 LATCHED WCTRL 4 H		05,06,07	
MIC03 MAD 31 H		15,14,02,03		MIC05 LATCHED WCTRL 5 H		05,06,07	
MIC03 PAGE 0NDRT H		07,03		MIC05 M CLK ENA L		10,05	
MIC03 X0 PC 08 H		06,01,02,03		MIC05 T0 VAL H		05,16	
MIC03 X0 PC 01 H		06,01,02,03		MIC05 UB REQ H		04,05	
MIC04 ASRC SEL S0 H		04,03		MIC05 WCTRL HALXXX L		05,07	
MIC04 ASRC SEL S1 H		04,03		MIC06 AMUX SEL S0 H		06,01,02	
MIC04 ASRC SEL S2 H		04,03		MIC06 AMUX SEL S1 H		06,01,02	
MIC04 CLK SMOR L		04,01,02		MIC06 BSRC SEL S0 H		06,03	
MIC04 CMI CPU PRI L		04,07		MIC06 BSRC SEL S1 H		06,03	
MIC04 ENA PC BACKUP L		04,03		MIC06 CACHE GRP 0 WR H		11,06	
MIC04 ENA PRDT 0ITS L		04,05		MIC06 CACHE INT L		07,06	
MIC04 ENA SMOR L		04,01,02		MIC06 CACHE VALID 0 H		12,06,08	
MIC04 IRDI L		10,04		MIC06 CLK SEL S0 H		06,01,02	
MIC04 LATCHED MBUS 15 L		04		MIC06 CLK SEL S1 H		06,01,02	
MIC04 MBUS ENA H		04,01,02		MIC06 COMP MODE H		06,03	
MIC04 MEM STALL H		04		MIC06 DBUS ROT S0 H		06,01,02	
MIC04 MSRC HI H		04		MIC06 DBUS ROT S1 H		06,01,02	
MIC04 MSRC HI L		04		MIC06 DBUS SEL S0 H		11,06,01,02	
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)	
MIC06 DBUS SEL S1 H		11,06,01,02		MIC06 ENA 0YTE 0 L		10,06	
MIC06 ENA 0YTE 0 L		10,06		MIC06 ENA 0YTE 1 L		10,06	
MIC06 ENA 0YTE 1 L		10,06		MIC06 ENA 0YTE 2 L		09,06	
MIC06 ENA 0YTE 2 L		09,06		MIC06 ENA 0YTE 3 L		09,06	
MIC06 ENA PC L		10,11,06,03		MIC06 ENA VA L		06,03	
MIC06 ENA VA L		06,03		MIC06 ENA VA SAVE L		06,03	
MIC06 ENA VA SAVE L		06,03		MIC06 ENABLE ACV STALL H		04,06	
MIC06 ENABLE ACV STALL H		04,06		MIC06 INVAL PREF L		06,07	
MIC06 INVAL PREF L		06,07		MIC06 LATCH MA L		06,03	
MIC06 LATCH MA L		06,03		MIC06 MA SELECT S0 H		06,03	
MIC06 MA SELECT S0 H		06,03		MIC06 MA SELECT S1 H		06,03	
MIC06 MA SELECT S1 H		06,03		MIC06 MMUX SEL S1 H		04,07,06,01,02	
MIC06 MMUX SEL S1 H		04,07,06,01,02		MIC06 PREFETCH L		11,06,07	
MIC06 PREFETCH L		11,06,07		MIC06 PTE CHECK L		16,06	
MIC06 PTE CHECK L		16,06		MIC06 STALL L		04,06	
MIC06 STALL L		04,06		MIC06 T0 GRP 0 WR H		16,06	
MIC06 T0 GRP 0 WR H		16,06		MIC06 T0 GRP 1 WR H		16,06	
MIC06 T0 GRP 1 WR H		16,06		MIC06 T0 OUTPUT ENA L		16,06	
MIC06 T0 OUTPUT ENA L		16,06		MIC06 T0 PARITY ENA H		04,07,06	
MIC06 T0 PARITY ENA H		04,07,06		MIC06 X0 SELECT H		07,06,01,02	
MIC06 X0 SELECT H		07,06,01,02		MIC06 X00 IH USE L		07,06	
MIC06 X00 IH USE L		07,06		MIC06 X01 IH USE L		07,06	
MIC06 X01 IH USE L		07,06		MIC07 ACV H		04,07	
MIC07 ACV H		04,07		MIC07 ADD REG ENA L		11,05,07,01,02,10	
MIC07 ADD REG ENA L		11,05,07,01,02,10		MIC07 CORR DATA INT L		04,07	
MIC07 CORR DATA INT L		04,07		MIC07 ENA CMI L		07,01,02	
MIC07 ENA CMI L		07,01,02		MIC07 ENC UTRAP 0 L		07	
MIC07 ENC UTRAP 0 L		07		MIC07 ENC UTRAP 1 L		07	
MIC07 ENC UTRAP 1 L		07		MIC07 ENC UTRAP 2 L		07	
MIC07 ENC UTRAP 2 L		07		MIC07 FORCE MA 09 H		07,03	
MIC07 FORCE MA 09 H		07,03		MIC07 GEH DEST INH L		04,07	
MIC07 GEH DEST INH L		04,07		MIC07 GRANT STALL L		10,06,07	
MIC07 GRANT STALL L		10,06,07		MIC07 INHIBIT CMI H		07	
MIC07 INHIBIT CMI H		07		MIC07 PROC INIT H		04,07	
MIC07 PROC INIT H		04,07		MIC07 PTE CHK OR PROBE H		07	
MIC07 PTE CHK OR PROBE H		07		MIC07 SNAPSHOT CMI L		06,07,01,02	
MIC07 SNAPSHOT CMI L		06,07,01,02		MIC07 STATUS 0 H		07	
MIC07 STATUS 0 H		07		MIC07 STATUS 1 H		07	
MIC07 STATUS 1 H		07		MIC07 STATUS VALID L		04,07,06	
MIC07 STATUS VALID L		04,07,06		MIC07 UTRAP L		10,04,07,06	
MIC07 UTRAP L		10,04,07,06		MIC07 WR BUS ERR INT L		04,07	
MIC07 WR BUS ERR INT L		04,07		MIC07 WRITE VECT OCC L		06,07	
MIC07 WRITE VECT OCC L		06,07		MIC08 CA TAG 12 H		12,11,08	

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D								
	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)		
	MIC00 CA TAG 18 H	11,12,00	MIC11 B PAD 08 H	10,09,11	MIC14 T1 TAG 26 H	14,16,17		
	MIC00 CA TAG 19 H	11,12,00	MIC11 B PAD 09 H	10,09,11	MIC14 T1 TAG 27 H	14,16,17		
	MIC00 CA TAG 20 H	11,12,00	MIC11 B PAD 10 L	10,09,11	MIC14 T1 TAG 28 H	14,16,17		
	MIC00 CA TAG 21 H	11,12,00	MIC11 B PAD 11 L	10,09,11	MIC14 T1 TAG 29 H	14,16,17		
	MIC00 CA TAG 22 H	11,12,00	MIC11 CA WRITE ENA L	10,09,11,00	MIC14 T1 TAG 30 H	14,16,17		
	MIC00 CA TAG 23 H	11,12,00	MIC11 ENA CACHE H	09,10,11	MIC14 T1 TAG PAR OUT L	14,17		
	MIC00 CA TAG PAR OUT H	12,00	MIC11 ENA CACHE L	13,09,10,11	MIC14 T1 VALID H	14,16,17		
	MIC00 CA VALID H	11,12,00	MIC11 MI 0 CLK H	11,16	MIC15 T0 PAR OUT 0 H	15,16		
	MIC09 CA 16 H	09	MIC12 CA TAG PAR ERR H	12,06	MIC15 T0 PAR OUT 1 H	15,16		
	MIC09 CA 17 H	09	MIC12 CA TAG PAR IN H	12,08	MIC15 T0 PAR OUT 2 H	15,16		
	MIC09 CA 18 H	09						
	MIC09 CA 19 H	09	MIC13 CA 00 PAR IN H	13,10	MIC15 T0 T0 AC 0 H	15,16		
	MIC09 CA 20 H	09	MIC13 CA 00 PAR L	13	MIC15 T0 T0 AC 1 H	15,16		
	MIC09 CA 21 H	09	MIC13 CA 00 PERR H	13	MIC15 T0 T0 AC 2 H	15,16		
	MIC09 CA 22 H	09	MIC13 CA 01 PAR IN H	13,10	MIC15 T0 T0 AC 3 H	15,16		
	MIC09 CA 23 H	09	MIC13 CA 01 PAR L	13	MIC15 T0 TB DATA 09 H	16,15		
	MIC09 CA 24 H	09	MIC13 CA 01 PERR H	13	MIC15 T0 TB DATA 10 H	16,15		
	MIC09 CA 25 H	09	MIC13 CA 02 PAR IN H	09,13	MIC15 T0 TB DATA 11 H	16,15		
	MIC09 CA 26 H	09	MIC13 CA 02 PAR L	13	MIC15 T0 TB DATA 12 H	16,15		
	MIC09 CA 27 H	09	MIC13 CA 02 PERR H	13	MIC15 T0 TB DATA 13 H	16,15		
			MIC13 CA 03 PAR IN H	13,09	MIC15 T0 TB DATA 14 H	16,15		
	MIC09 CA 28 H	09						
	MIC09 CA 29 H	09	MIC13 CA B3 PAR L	13	MIC15 T0 TB DATA 15 H	16,15		
	MIC09 CA 30 H	09	MIC13 CA B3 PERR H	13	MIC15 T0 TB DATA 16 H	15,16		
	MIC09 CA 31 H	09	MIC13 CA DATA PAR ERR L	13,06	MIC15 T0 TB DATA 17 H	16,15		
	MIC09 CA B2 PAR OUT H	13,09	MIC14 T0 TAG 16 H	14,17,16	MIC15 T0 TB DATA 18 H	16,15		
	MIC09 CA B3 PAR OUT H	13,09	MIC14 T0 TAG 17 H	14,17,16	MIC15 T0 TB DATA 19 H	16,15		
	MIC10 CA 00 H	10	MIC14 T0 TAG 18 H	14,17,16	MIC15 T0 TB DATA 20 H	15,16		
	MIC10 CA 01 H	10	MIC14 T0 TAG 19 H	14,17,16	MIC15 T0 TB DATA 21 H	16,15		
	MIC10 CA 02 H	10	MIC14 T0 TAG 20 H	14,17,16	MIC15 T0 TB DATA 22 H	16,15		
	MIC10 CA 03 H	10	MIC14 T0 TAG 21 H	14,17,16	MIC15 T0 TB DATA 23 H	16,15		
			MIC14 T0 TAG 22 H	14,17,16	MIC15 T0 TB M 0IT H	15,16		
	MIC10 CA 04 H	10						
	MIC10 CA 05 H	10	MIC14 T0 TAG 23 H	14,17,16	MIC15 T1 PAR OUT 0 H	15,16		
	MIC10 CA 06 H	10	MIC14 T0 TAG 24 H	14,17,16	MIC15 T1 PAR OUT 1 H	15,16		
	MIC10 CA 07 H	10	MIC14 T0 TAG 25 H	14,16,17	MIC15 T1 PAR OUT 2 H	15,16		
	MIC10 CA 08 H	10	MIC14 T0 TAG 26 H	14,16,17	MIC15 T1 T0 AC 0 H	15,16		
	MIC10 CA 09 H	10	MIC14 T0 TAG 27 H	14,16,17	MIC15 T1 T0 AC 1 H	15,16		
	MIC10 CA 10 H	10	MIC14 T0 TAG 28 H	14,16,17	MIC15 T1 T0 AC 2 H	15,16		
	MIC10 CA 11 H	10	MIC14 T0 TAG 29 H	14,16,17	MIC15 T1 T0 AC 3 H	15,16		
	MIC10 CA 12 H	10	MIC14 T0 TAG 30 H	14,16,17	MIC15 T1 T0 DATA 09 H	16,15		
	MIC10 CA 13 H	10	MIC14 T0 TAG PAR OUT L	14,17	MIC15 T1 TB DATA 10 H	16,15		
			MIC14 T0 VALID H	14,16,17	MIC15 T1 TB DATA 11 H	16,15		
	MIC10 CA 14 H	10						
	MIC10 CA 15 H	10	MIC14 T1 TAG 16 H	14,17,16	MIC15 T1 TB DATA 12 H	15,16		
	MIC10 CA B0 PAR OUT H	13,10	MIC14 T1 TAG 17 H	14,17,16	MIC15 T1 TB DATA 13 H	16,15		
	MIC10 CA B1 PAR OUT H	13,10	MIC14 T1 TAG 18 H	14,17,16	MIC15 T1 T0 DATA 14 H	16,15		
	MIC11 B PAD 02 H	10,09,11	MIC14 T1 TAG 19 H	14,17,16	MIC15 T1 TB DATA 15 H	16,15		
	MIC11 B PAD 03 H	10,09,11	MIC14 T1 TAG 20 H	14,17,16	MIC15 T1 TB DATA 16 H	15,16		
	MIC11 B PAD 04 H	10,09,11	MIC14 T1 TAG 21 H	14,17,16	MIC15 T1 TB DATA 17 H	16,15		
	MIC11 B PAD 05 H	10,09,11	MIC14 T1 TAG 22 H	14,17,16	MIC15 T1 TB DATA 18 H	16,15		
	MIC11 B PAD 06 H	10,09,11	MIC14 T1 TAG 23 H	14,17,16	MIC15 T1 TB DATA 19 H	16,15		
	MIC11 B PAD 07 H	10,09,11	MIC14 T1 TAG 24 H	14,17,16	MIC15 T1 T0 DATA 20 H	15,16		
			MIC14 T1 TAG 25 H	14,16,17	MIC15 T1 T0 DATA 21 H	16,15		

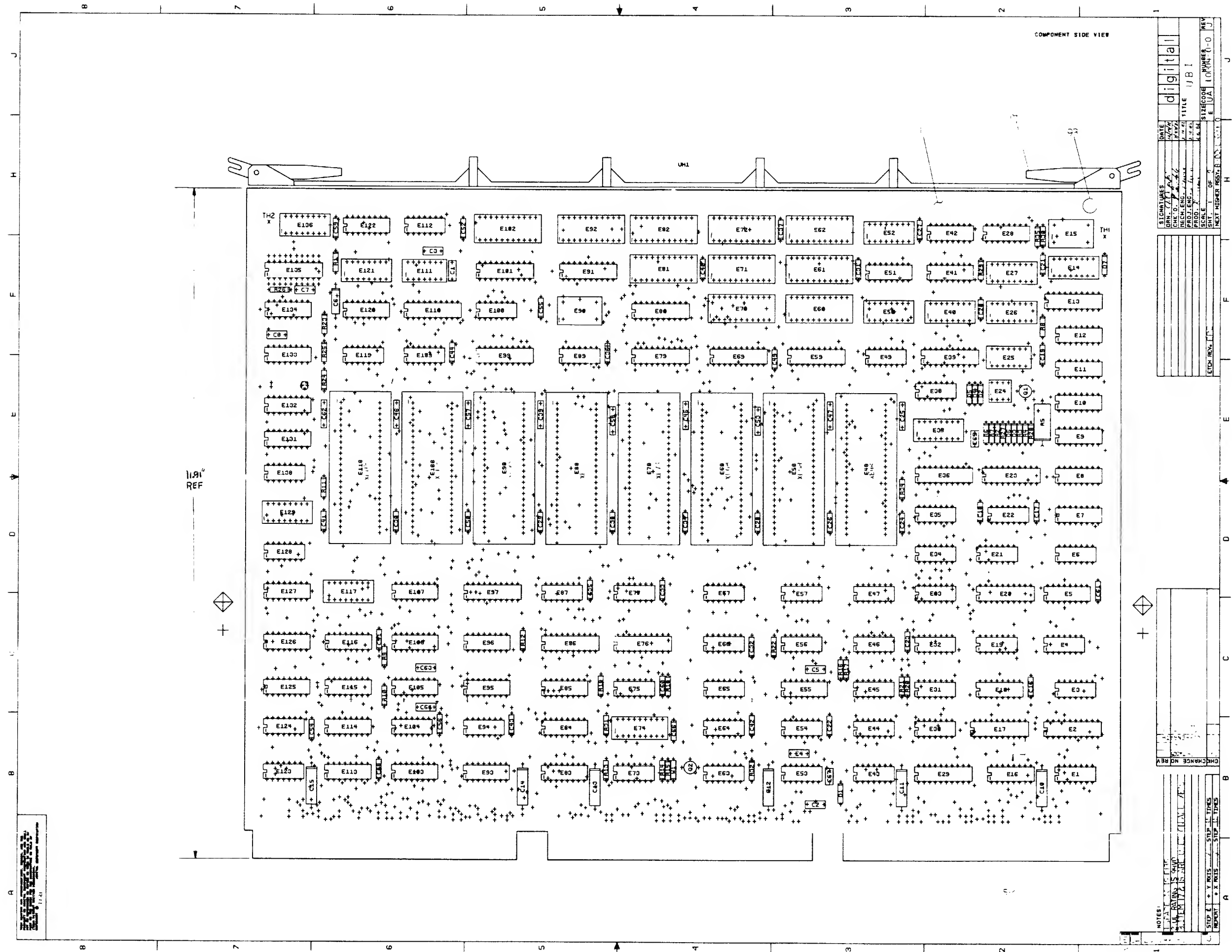
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						3002	3215		



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REWORK INSTRUCTIONS  
WIRE ADDS ECO'S  
5-2 E16-15 TO E17-10

REVISION HISTORY		
DATE	ECO NUMBER	REV

REV 1004

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DOCUMENT NUMBER		
REV	CODE	NUMBER
E	JA	10004-0-0
SCALE		SHEET 2 OF 2

REV 1004  
E JA 10004-0-0  
SHEET 2 OF 2

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
1	1	E-MD-5013827-0-0	5013827-00	CIRCUIT BOARD (PCB)	1	
2	2		1000019-00	150.0 MMF 100V 5%200PPM MICA	1	C8
3	3		1000023-00	330.0 MMF 100V 5%200PPM MICA	4	C1-C4
4	4		1000024-00	470.0 MMF 100V 5%200PPM MICA	1	C5
5	5		1000043-00	1000.0 MMF 250V 20% Y5F DISC	2	C63, C64
6	6	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	6	C10-C15
7	7		1009964-00	.68 MFD 100V 10% S.TANT	1	C6
8	8		1010978-40	.22 MFD 50V 10% CER	1	C65
9	9		1012784-00	.047 MFD 50V +80-20% CER	48	C16-C62, C66
10	10		1010978-24	.01 MFD 50V 10% CER	2	C9, C67
11	11		1104860-00	1N 746A VZ= 3.3 5%	1	D1
12	12		1105736-00	1N 4004 PIV=400 I= 1A DO41 SP	1	D2-D5
13	13		1105871-01	1/4M3.0A21 - 3.0 1% .25W N	1	D6
14	14		1210711-02	/REPLACED BY 12-16988-02	1	
15	15		1211164-04	*** THIS ITEM IS NOT USED ***	1	
16	16		1215924-00	SKT, IC 48PIN DIP GOLD PLATE	8	XE48, XE58, XE68, XE78, XE88, XE98, XE108, XE118
17	17		1215935-00	GASKET, THERMAL .50"X.80"	8	
18	18		1215936-00	HEAT SINK, FORCED CONVECTION	8	
19	19		1300005-04	R NETWORK 15-470 5.0 % 16PIN	1	E37
20	20		1300005-07	R NETWORK 15-4.7K 5.0 % 16PIN	1	E40
21	21		1300316-00	470.0 .25 W 5.0 % CC	1	R20
22	22		1300365-00	1.0 K .25 W 5.0 % CC	14	R1, R8, R12-R19, R22, R23, R30, R31
23	23		1300398-00	1.80 K .25 W 5.0 % CC	2	R4, R28
24	24		1300432-00	3.0 K .25 W 5.0 % CC	1	R11
25	25		1300447-00	4.70 K .25 W 5.0 % CC	3	R2, R3
26	26		1302394-00	30.0 K .25 W 5.0 % CC	1	R23
27	27		1305346-00	27.0 K .25 W 5.0 % CC	1	R25
28	28		1301322-00	190.0 .25 W 5.0 % CC	1	R21
29	29		1301423-00	*** THIS ITEM IS NOT USED ***	1	

REVISION HISTORY			BASIC PART NO: L0004		DRN: K.FRIEDGEN		DATE: 04-MAY-79		D I G I T A L			
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: E.T.GERRY		DATE: 04-MAY-79		TITLE		PARTS LIST	
---	INITIAL	C	SECTION VARIATION INDEX		DES.ENG: S.SMITH <td colspan="2">DATE: 4-MAY-79<td colspan="4">DOCUMENT NUMBER</td></td>		DATE: 4-MAY-79 <td colspan="4">DOCUMENT NUMBER</td>		DOCUMENT NUMBER			
TK	L0004-TW002	D	[A]	00	RESP.ENG.: S.SMITH <td colspan="2">DATE: 04-MAY-79<td colspan="2">SIZE CODE<td colspan="2">NUMBER REV</td></td></td>		DATE: 04-MAY-79 <td colspan="2">SIZE CODE<td colspan="2">NUMBER REV</td></td>		SIZE CODE <td colspan="2">NUMBER REV</td>		NUMBER REV	
D L	L0004-TW003	F	[B]		MFG.ENG.: VANCE PARKER <td colspan="2">DATE: 8-FEB-80</td> <td>K</td> <td>PL</td> <td>L0004-0-DBP</td> <td>F</td>		DATE: 8-FEB-80		K	PL	L0004-0-DBP	F
LL	TW005		[C]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME:		EDIT #	
			[D]		E-UA-L0004-0-0		#B-DD-L0004-0		Z1256F.PLS		20	
			[E]									
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AUTOMATED BY PRTLST.3L(31)

## P A R T S L I S T

SHEET A2 OF A3

LINE ITEM DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION  
00

REFERENCE DESIGNATOR

30	30	1301571-00	68.0 1.0 W 5.0 % CC	1	R5
30	30	1304838-00	43.0 K .25 W 5.0 % CC	1	R24
30	30	1302514-00	39.0 K .25 W 5.0 % CC	1	R26
30	30	1305125-00	383.0 .25 W 1.0 % RN55D-F10	1	R9, R10
30	30	1312628-00	R NETWORK 14-176.5 14-375 16PIN	2	E93, E114
30	30	1312628-01	R NETWORK 14-176.5 11-375 16PIN	2	E103, E113
30	30	1509649-01	3762 PNP 4W SI 40 35	1	Q1
30	30	1811660-23	OSCILLATOR, XTAL 5.5296 MHZ	1	E90
30	30	1813951-00	OSCILLATOR, XTAL 1.0 KHZ	1	E15
30	30	1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1	E94, E123
30	30	1910322-00	DEC 1488L DRIVER LINE, QUAD, EI	1	E111
30	30	1910323-00	DEC 1489L RECEIVER LINE, QUAD	1	E53
30	30	1910436-00	DEC 74123 ONE SHOT-DUAL, RETRIG	1	E133, E134
30	30	1910532-00	74500 NAND GATE-QUAD 2IN	4	E30, E66, E77, E100
30	30	1910533-00	74503 NAND GATE-QUAD 2IN, 0	1	E45
30	30	1910534-00	74504 INVERTER GATE-HEX 11	3	E21, E31, E87
30	30	1910535-00	74505 INVERTER GATE-HEX 1	1	E22
30	30	1910536-00	74510 NAND GATE-TRIPLE 3IN	1	E19, E65, E89
30	30	1910539-00	74520 NAND GATE-DUAL 4INPU	3	E75, E109
30	30	1910544-00	74574 FF-D DUAL, EDGE TRIGG	5	E4, E32, E44, E67, E119
30	30	1910550-00	745174 FF-D HEX	1	E127
30	30	1910957-00	745175 FF-D QUAD COMMON CLO	3	E5, E55, E132
30	30	1911469-00	DEC 8640 RECEIVER, BUS, QUAD, U	2	E104, E124
30	30	1911573-00	745280 PARITY GEN/CHKR, 9BIT	1	E3
30	30	1911579-00	8641 TRANSCEIVER, BUS, QUA	11	E33-E85, E95, E96, E105, E106, E115
30	30	1911641-00	SN 745257 MUX, QUAD 2 TO 1	4	E116, E125, E126
30	30	1911675-00	745138 DECODER/DEMUX 3-8 LIN	1	E107, E120, E122, E131
30	30	1911712-00	74551 AND-OR GATE-INVERT 0	1	E13
30	30	1912388-00	74502 NOR GATE-QUAD 2IN, PO	1	E44
30	30	1912389-00	74508 AND GATE-QUAD 2IN, PO	2	E47, E57, E64, E130
30	30	1912646-00	LS253 MUX 1 OF 4 (DUAL)	4	E35, E46
30	30	1912799-00	LS00 NAND-GATE-QUAD 2IN, P	2	E36, E41, E42, E51
30	30	1912810-00	LS20 NAND GATE-DUAL 4IN	1	E73, E128
30	30	1912812-00	LS22 NAND GATE-DUAL 4IN, P	1	E34
30	30	1912815-00	LS30 NAND GATE-SINGLE 8IN	1	E43
30	30	1912827-00	LS83 ADDERS-4BIT	4	E117, E121, E129, E135
30	30	1912863-00	LS273 FF-D OCTAL W/CLEAR	1	E39
30	30	1913340-00	74532 OR GATE-QUAD 2IN	1	E38
30	30	1913493-00	745241 OCTAL BUFFER, TRI-STA	1	E76
30	30	1913671-00	745374 FF-D OCTAL TRISTATE	7	E2, E13, E17, E20, E23, E29, E36
30	30	1913839-00	LS165 SHIFT REG., 8BIT	1	E16
30	30	1914085-00	745260 NOR GATE-DUAL POS	1	E1
30	30	1914156-00	LM 393 VOLT. COMPARTOR DUAL	1	E24
30	30	1914451-00	74LS393 COUNTER, BINARY, 4BIT	3	E6, E63, E112
30	30	1914558-00	SN 745132N NAND GATE QUAD 2IN P	1	E56
30	30	1914685-00	DC 611B BIPOLAR, LS, 400-GATE	2	E108, E118
30	30	1914692-00	DC 618C BIPOLAR, LS, 400-GATE	4	E58, E68, E79, E88

CONT

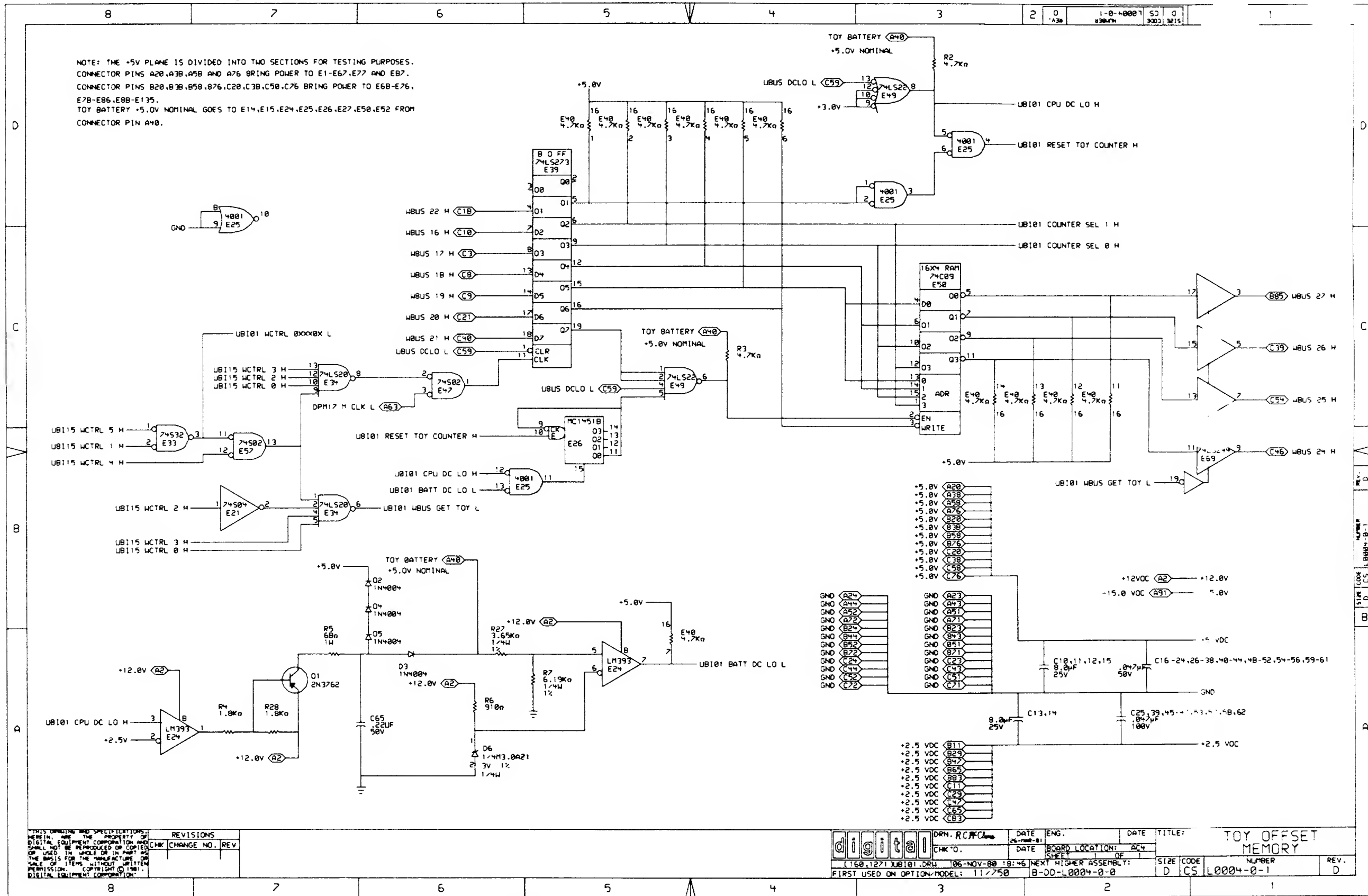
D	I	G	I	T	A	L	TITLE	U.B.I.	SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
											K	PL	L0004-0-DBP	F

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION	REFERENCE DESIGNATOR
77	77		1914693-00	DC 619D BIPOLAR,LS,400-GATE	1	E48
78	78		1914704-00	DC 630B BIPOLAR,LS,400-GATE	1	E98
79	79		1915193-00	LS244 DRIVER,LINE,OCTAL,T	9	E59,E69,E79,E80,E86,E97,E99, E101,E110
80	80		1915697-00	RAM 256X4 TRI-STATE	10	E60-E62,E70-E72,E81,E82,E92,E102
81	81		2112623-02	DUAL BAUD RATE GEN/PROG DIVIDER	1	E74
82	82		2113603-00	4001UBNOR GATE-QUAD 2IN CM	1	E25
83	83		2113647-00	4513B COUNTER,DUAL UP BCD	1	E26
84	84		2113653-00	74C89 RAM 64BIT CMOS TRIST	1	E50
85	85		2114462-00	4040B COUNTER/DIVIDER,BINA	3	E14,E27,E52
86	86		23524A2-00	A2-05	1	E77
87	87		23525A2-00	A2-05	1	E8
88	88		23526A2-00	A2-05	1	E9
89	89		23527A2-00	A2-05	1	E10
90	90		23528A2-00	A2-05	1	E11
91	91		23529A2-00	A2-05	1	E12
92	92		23000A2-05	*** THIS ITEM IS NOT USED ***	-	
93	93		9000024-01	EYELET,ROLL FLANGE .12100X .192	12	
94	94		1002476-00	510.0 MMF 100V 5%200PPM MICA	1	C7
95	95		1304835-00	/REPLACED BY 13-05337-00	1	R27
96	96		1305374-00	910.0 .25 W 5.0 % CC	1	R6
97	97		1314187-00	5.19 K .25 W 1.0 % RN55D-F10	1	R7
98	98		1503121-00	2N 2369 NPN 350MW SI N	1	Q2
99	99		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	1	W1
100	100		1302379-00	75.0 .25 W 5.0 % CC	2	R32,R34
101	101		1910542-00	74S64 A-J-I GATE 4-2-3-2	1	E38
102	102		1913670-00	74S373 LATCH 8BIT TRASP TR	1	E91
103	103		1300202-00	47.0 .25 W 5.0 % CC	1	R35
104	104		1112585-00	5953 VZ= 5.2 .40W DO	1	D7
105	105		9007200-00	TRANSIPADS #10134	1	XQ2

106 NOTE: SPARE I.C. LOCATIONS ARE:E130,E135  
107 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12034-01

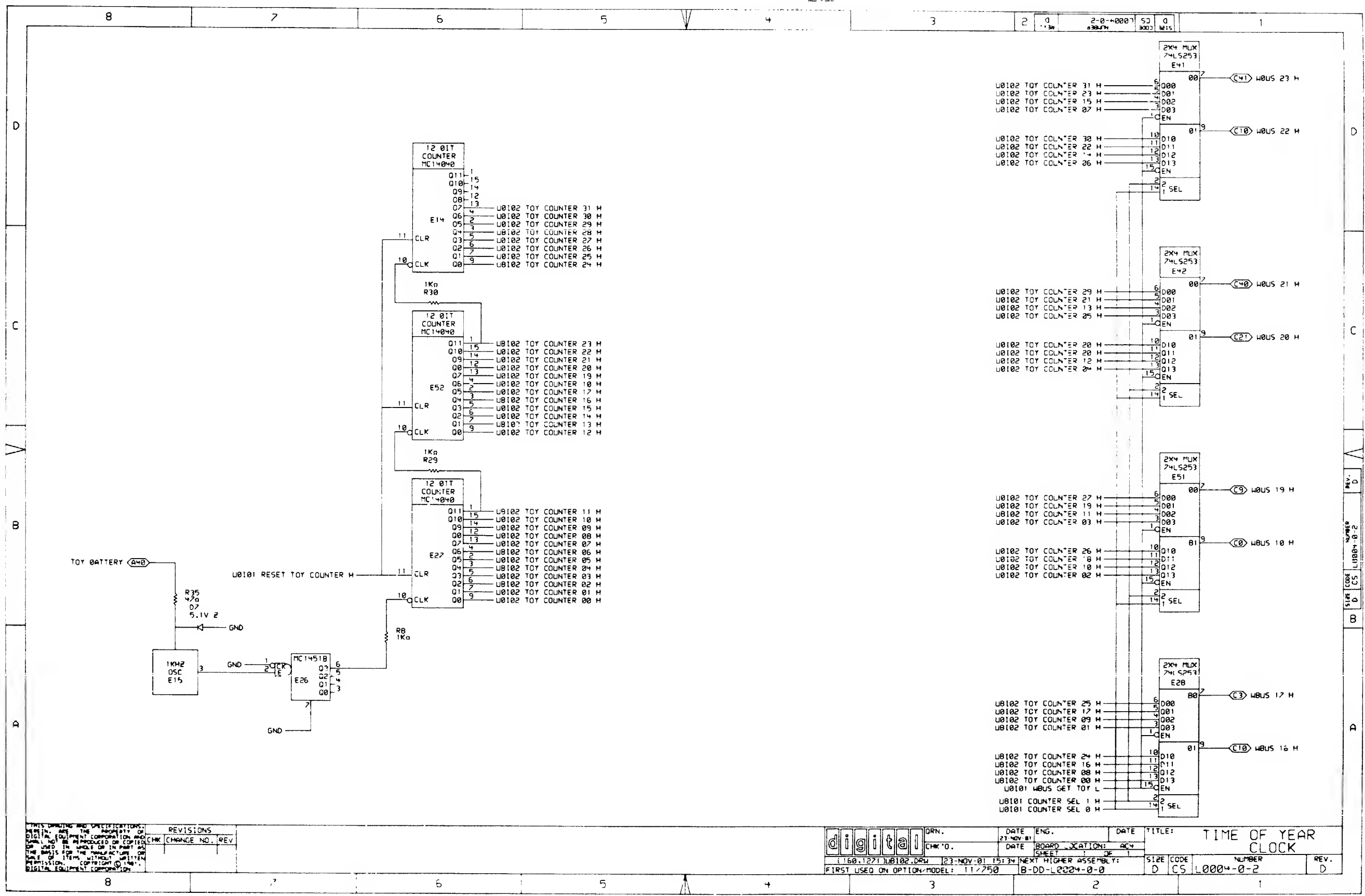
D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							U.B.I.		K	PL	L0004-0-DBP	F

NOTE: THE +5V PLANE IS DIVIDED INTO TWO SECTIONS FOR TESTING PURPOSES.  
CONNECTOR PINS A20,A30,A50 AND A76 BRING POWER TO E1-E67,E77 AND E87.  
CONNECTOR PINS B20,B30,B50,B76,C20,C30,C50,C76 BRING POWER TO E68-E76,  
E78-E86,E88-E135.  
TOY BATTERY +5.0V NOMINAL GOES TO E14,E15,E24,E25,E26,E27,E50,E52 FROM  
CONNECTOR PIN A40.



REVISIONS	CHK	CHANGE NO.	REV
1			

digital	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	26-MAR-81			TOY OFFSET MEMORY
(160,127) UB101.DRW	106-NOV-80	8:46	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION MODEL: 117750	B-DD-L0004-0-0			D	CS
				L0004-0-1	REV. D



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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN.	DATE	ENG.	DATE	TITLE: TIME OF YEAR CLOCK		
	CHK'D.	DATE	BOARD LOCATION: ACV	DATE	SIZE CODE	NUMBER	REV.
160,1271 UB102.DRW 23-NOV-01 15:34 NEXT HIGHER ASSEMBLY: B-DD-L0004-0-0					D	CS L0004-0-2	D
FIRST USED ON OPTION/MODEL: 11/750							





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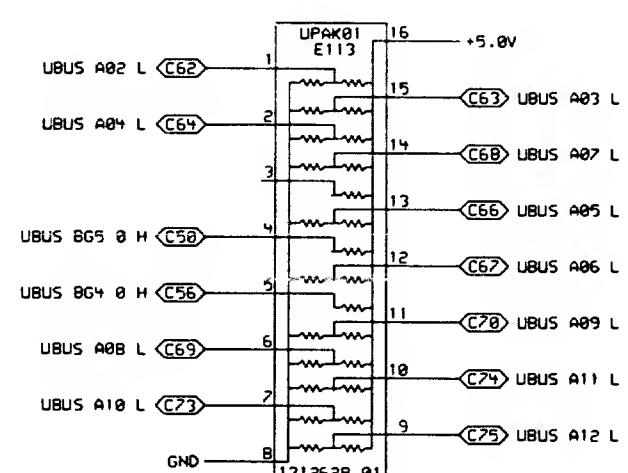
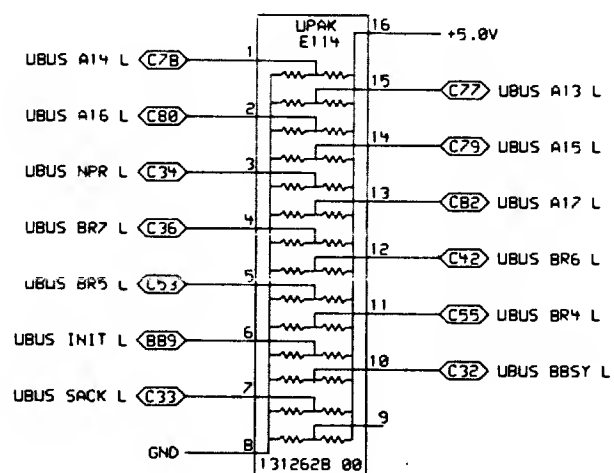
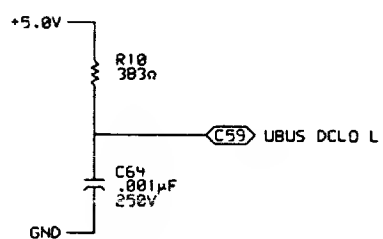
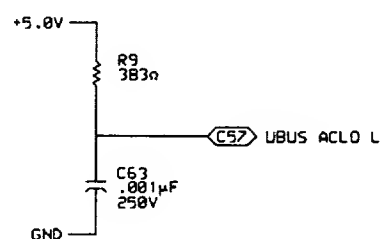
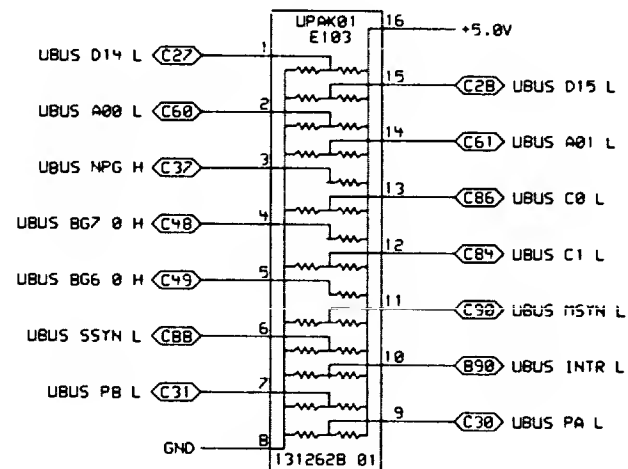
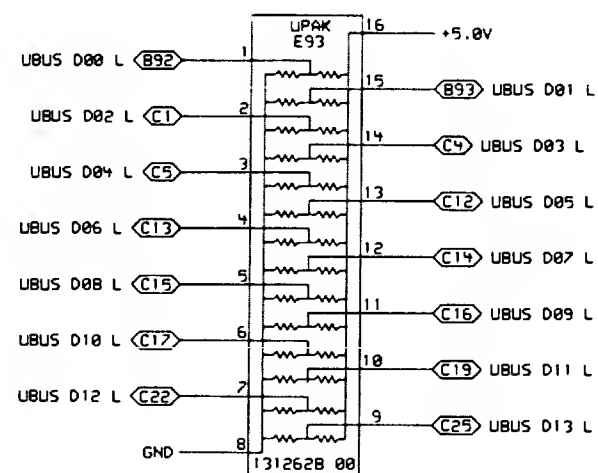
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REVISIONS	
CHK	CHANGE NO. REV

digital

DRN. R.C.F. / Cline

DATE

ENG.

DATE

21-MAR-80

DATE

AC4

DATE

BOARD LOCATION:

AC4

SHEET

OF

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TITLE:

UNIBUS

RESISTOR PAKS

SIZE

CODE

NUMBER

REV.

C

(160,1271)UB104.DRW 19-MAR-80 18:37 NEXT HIGHER ASSEMBLY:  
FIRST USED ON OPTION-MODEL: 117750

8-DD-L0003-0-0

D CS

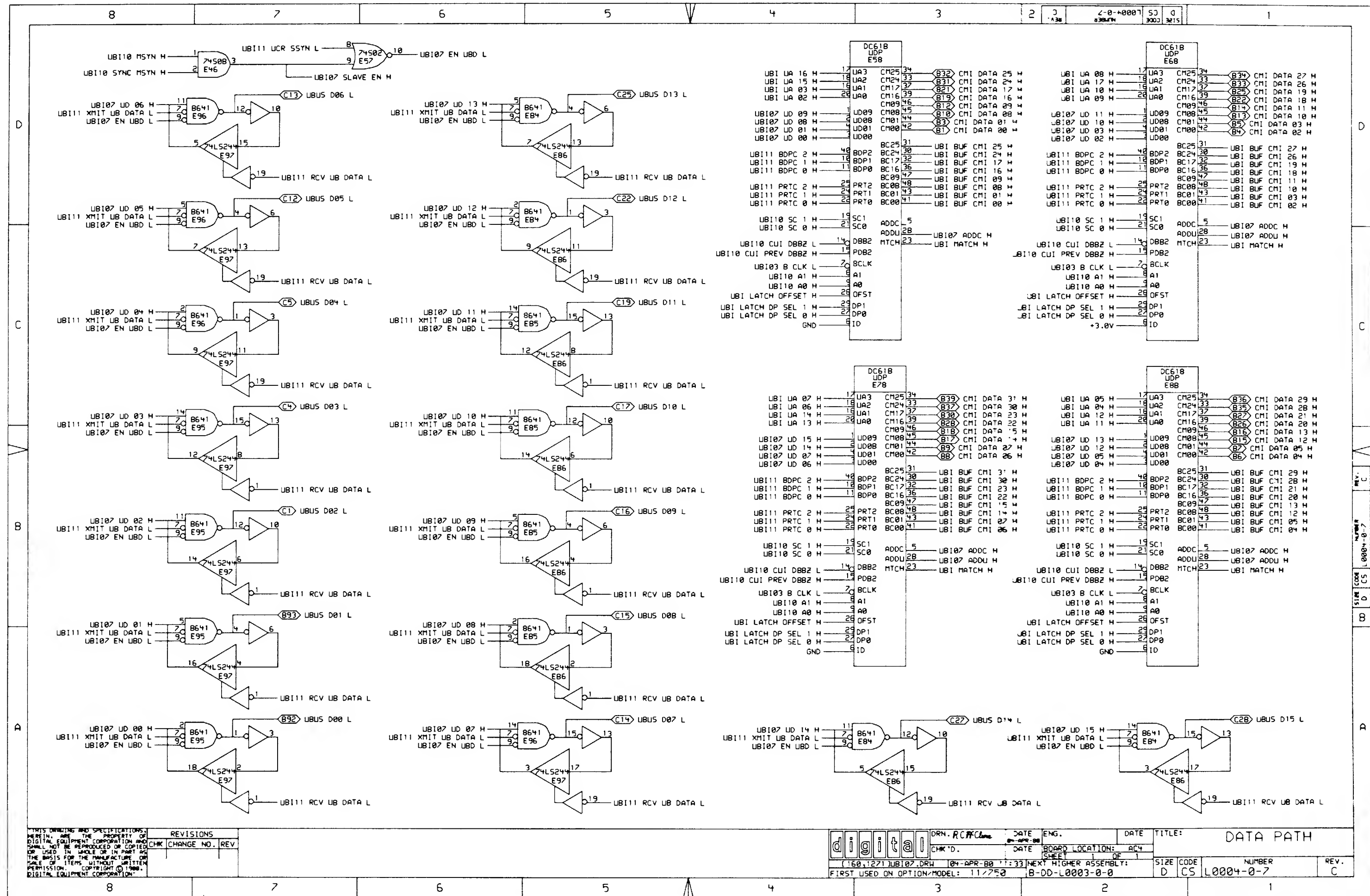
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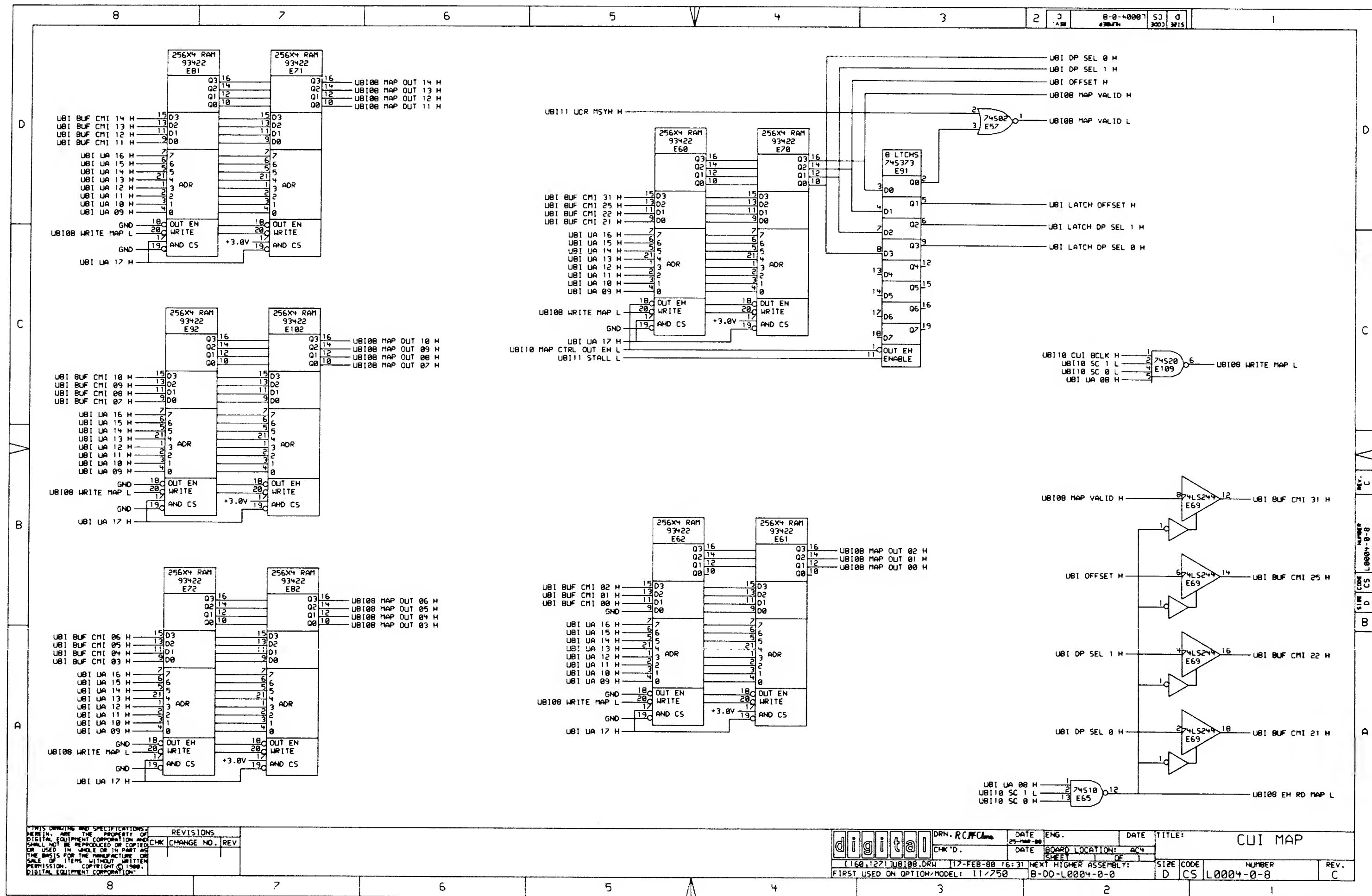
REV.

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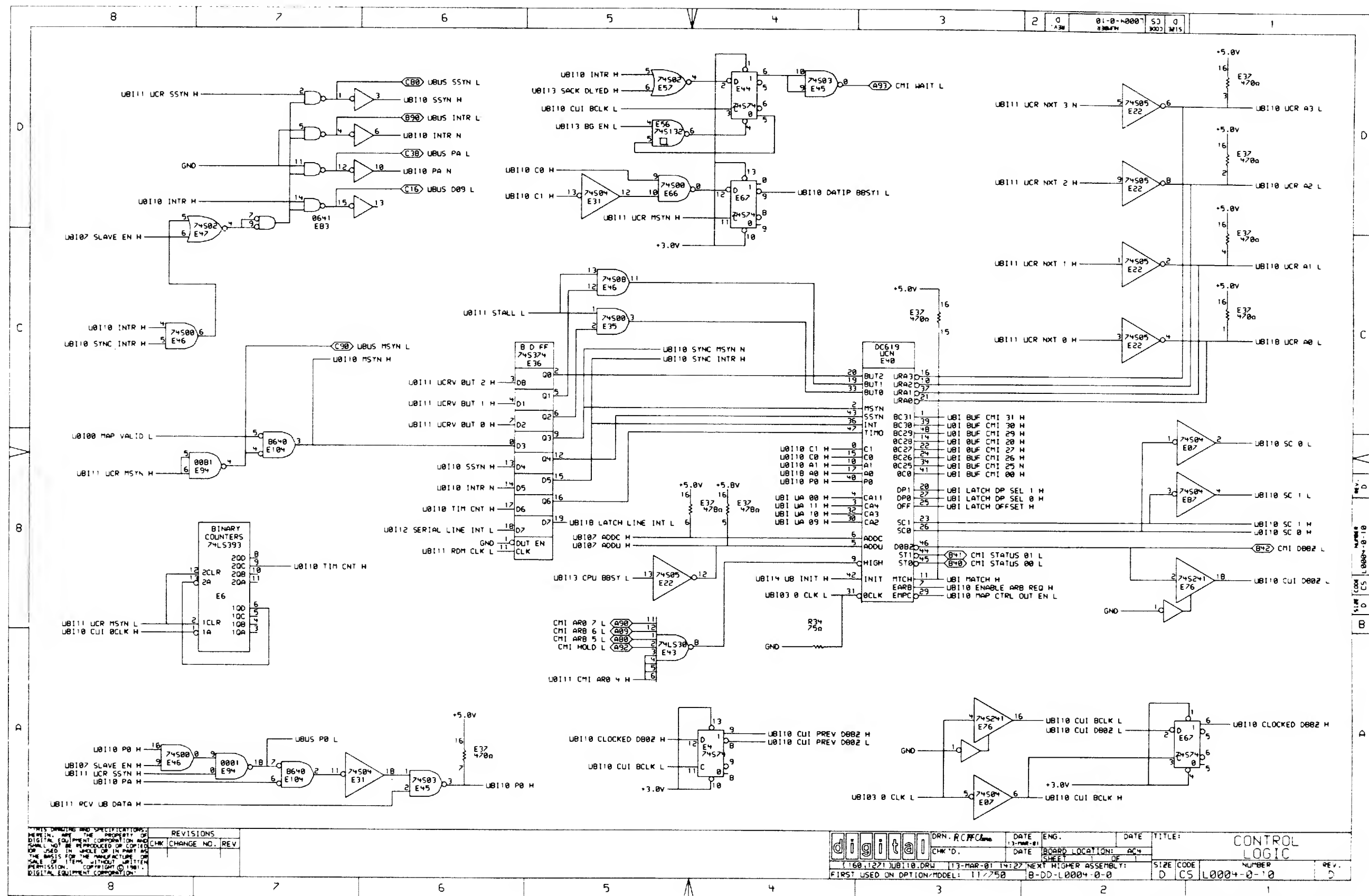








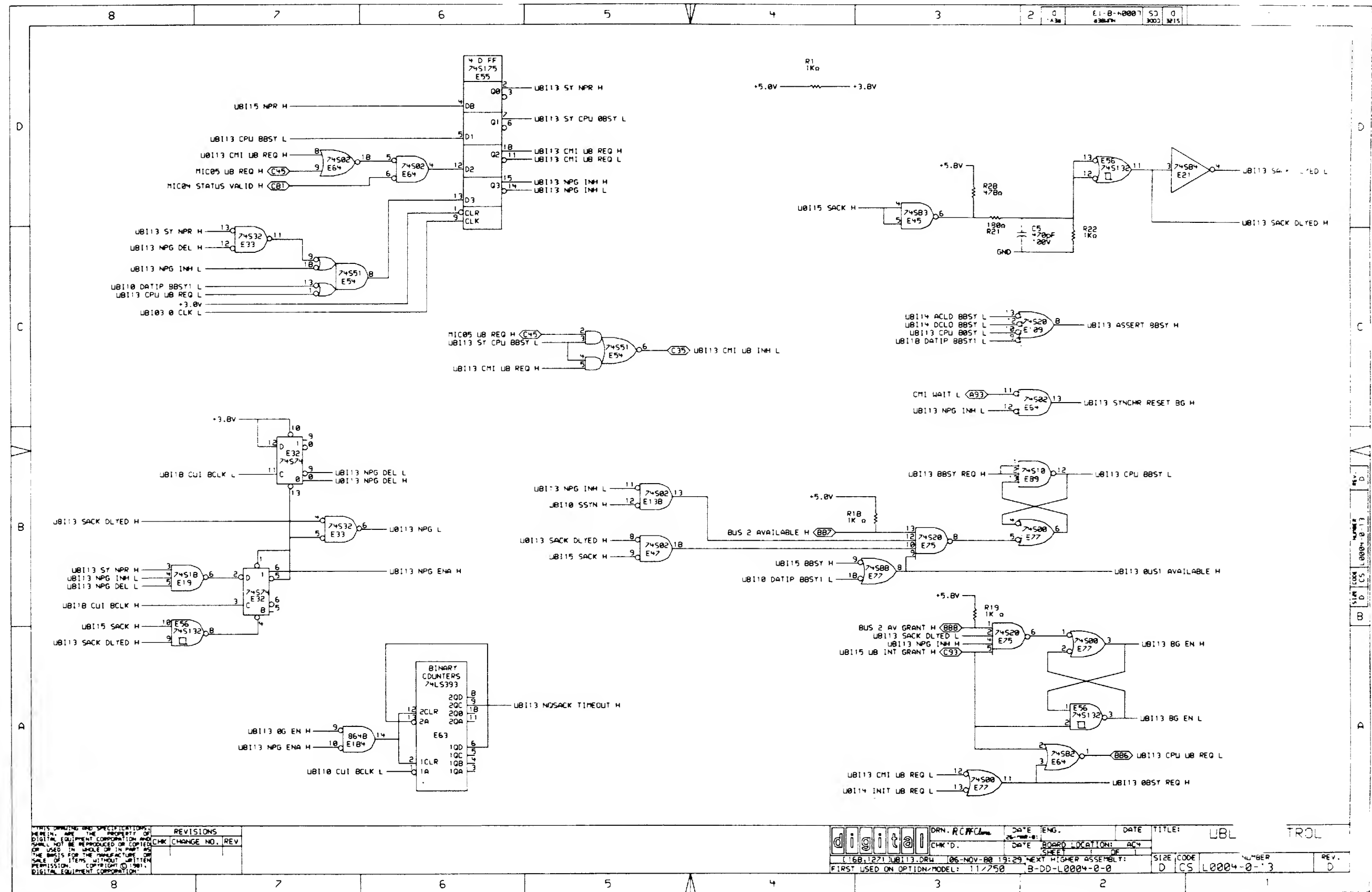
















SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)	SIGNAL NAME	PAGE NUMBER(S)
BUS 2 AV GRANT H	13	CS ADDR 12 H	3	MIC07 PTE CHK OR PROBE H	15
BUS 2 AVAILABLE H	13	CS BUS 0 H	03,14	MIC07 UTRAP L	03,15
CMI AR0 4 L	11	CS BUS 1 H	03,14	MIC07 WR BUS ERR INT L	15
CMI AR0 5 L	10	CS BUS 2 H	14,03	MIC18 INTERRUPT INH H	03
CMI AR0 6 L	10	CS BUS 3 H	14,03	MICRO VECTOR 0 H	15
CMI AR0 7 L	10	CS BUS 4 H	14,03	MICRO VECTOR 1 H	15
CMI DATA 00 H	07	CS BUT 0 H	03	MICRO VECTOR 2 H	15
CMI DATA 01 H	07	CS BUT 1 H	03	PB INIT L	15
CMI DATA 02 H	07	CS BUT 2 H	03	RD INT INH H	12
CMI DATA 03 H	07	CS BUT 3 H	03	RDM V CLOCK H	15
				RDM V LOAD H	15
CMI DATA 04 H	07	CS BUT 4 H	03	ROM PRESENT L	12
CMI DATA 05 H	07	CS BUT 5 H	03	SY ID 0 H	15
CMI DATA 06 H	07	CS FPA 0 H	03	SYS ID 1 H	15
CMI DATA 07 H	07	CS FPA 1 H	03	SYS ID 2 H	15
CMI DATA 08 H	07	CS FPA 2 H	03	SYS ID 3 H	15
CMI DATA 09 H	07	CS FPA 3 H	03	SYS ID 4 H	15
CMI DATA 10 H	07	CS HNEXT PAR H	15	SYS ID 5 H	15
CMI DATA 11 H	07	CS LIT 0 H	03	SYS ID 6 H	15
CMI DATA 12 H	07	CS LIT 1 H	03	SYS ID 7 H	15
CMI DATA 13 H	07	CS MISC CTL 0 H	03	TOY BATTERY	01
				UBI BUF CMI 00 H	10,07,09,08
CMI DATA 14 H	07	CS MISC CTL 1 H	03	UBI BUF CMI 01 H	07,09,08
CMI DATA 15 H	07	CS MISC CTL 2 H	03	UBI BUF CMI 02 H	09,08,07,06,05
CMI DATA 16 H	07	CS MISC CTL 3 H	03	UBI BUF CMI 03 H	07,08,09,06,05
CMI DATA 17 H	07	CS MISC CTL 4 H	03	UBI BUF CMI 04 H	08,09,07,06,05
CMI DATA 18 H	07	CS PAR 1 H	3	UBI BUF CMI 05 H	08,09,07,06,05
CMI DATA 19 H	07	CS WCTRL 0 H	15	UBI BUF CMI 06 H	08,07,09,06,05
CMI DATA 20 H	07	CS WCTRL 1 H	15	UBI BUF CMI 07 H	07,08,09,06,05
CMI DATA 21 H	07	CS WCTRL 2 H	15	UBI BUF CMI 08 H	07,08,09,06,05
CMI DATA 22 H	07	CS WCTRL 3 H	15	UBI BUF CMI 09 H	07,09,08,05
CMI DATA 23 H	07	CS WCTRL 4 H	15		
				UBI BUF CMI 10 H	09,07,08,05
CMI DATA 24 H	07	CS WCTRL 5 H	15	UBI BUF CMI 11 H	09,07,08,05
CMI DATA 25 H	07	DPH13 TIMER INT L	15	UBI BUF CMI 12 H	08,09,07,05
CMI DATA 26 H	07	DPH14 UVCTR BRANCH H	15	UBI BUF CMI 13 H	08,09,07,05
CMI DATA 27 H	07	DPH17 0CLK L	03	UBI BUF CMI 14 H	08,07,09,05
CMI DATA 28 H	07	DPH17 D CLK ENABLE H	15,12	UBI BUF CMI 15 H	07,09,05
CMI DATA 29 H	07	DPH17 DO SRVC L	15	UBI BUF CMI 16 H	07,09,05
CMI DATA 30 H	07	DPH17 IMSTR FETCH H	12,13	UBI BUF CMI 17 H	07,09,05
CMI DATA 31 H	07	DPH17 M CLK ENABLE H	15	UBI BUF CMI 18 H	07,09
CMI DBBZ L	10	DPH17 M CLK L	03,01,12	UBI BUF CMI 19 H	07,09
CMI HOLD L	11,10	DPH17 PHASE 1 H	15		
				UBI BUF CMI 20 H	09,07
CMI STATUS 00 L	10	EIA CON SI L	12	UBI BUF CMI 21 H	08,09,07
CMI STATUS 01 L	10	EIA CON SD L	12	UBI BUF CMI 22 H	08,07,09
CMI WAIT L	10,13	EIA TU SI L	12	UBI BUF CMI 23 H	07,09
COM BAUD OSC H	12	EIA TU SD L	12	UBI BUF CMI 24 H	1
COM BAUD OSC IH H	12	FORCE CON FAST L	12	UBI BUF CMI 25 H	10,07,08
COM BR A L	12	FRNT PNL LOCK H	12	UBI BUF CMI 26 H	10,07
COM BR B L	12	MIC04 STATUS VALID H	13	UBI BUF CMI 27 H	10,07
COM BR C L	12	MIC04 V OUT H	15	UBI BUF CMI 28 H	10,07
COM BR D L	12	MIC05 UB REQ H	13	UBI BUF CMI 29 H	10,07
CS ADDR 02 H	3	MIC07 CORR DATA INT L	15		

NOTES:  
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

REV. D  
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<b>digital</b>	DRN. 05	DATE ENG. 26-MAR-81	DATE	TITLE: UBI FORWARD REFERENCE
	CHK'D.	DATE BOARD LOCATION: ACV	SHEET DE 1	
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FIRST USED ON OPTION MODEL: 11/250	18-DD-L0004-0-0			D CS L0004-0-16

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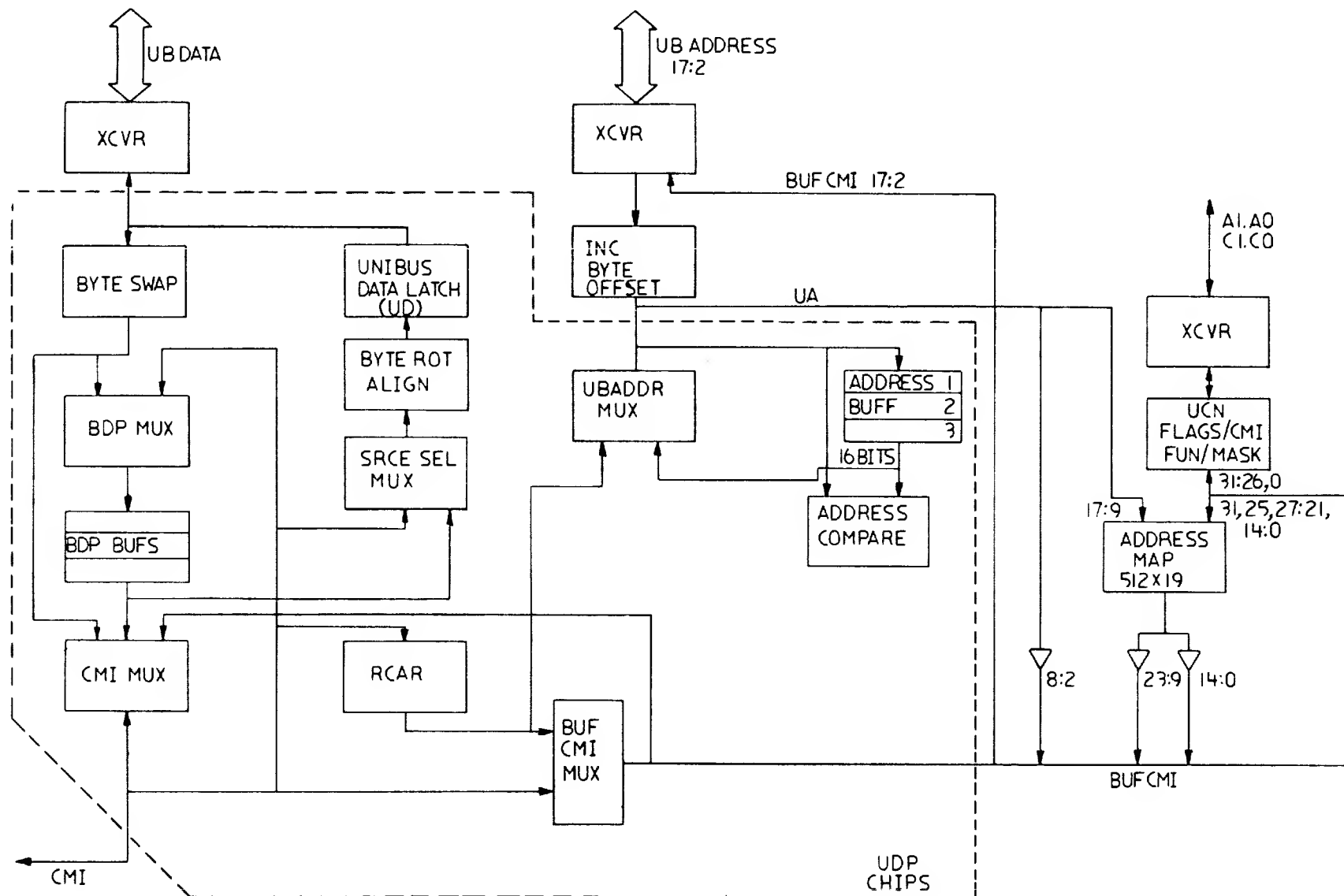






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colspan="2">01,02,15,12</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">UBUS A12 L</td><td colspan="2">04,05</td><td colspan="2">UBUS 22 H</td><td colspan="2">01,02,15,12</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">UBUS A13 L</td><td colspan="2">04,05</td><td colspan="2">UBUS 23 H</td><td colspan="2">02,15,12</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">UBUS A14 L</td><td colspan="2">04,05</td><td colspan="2">UBUS 24 H</td><td colspan="2">01,15,12</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">UBUS A15 L</td><td colspan="2">04,05</td><td colspan="2">UBUS 25 H</td><td colspan="2">01,15,12</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">UBUS A16 L</td><td colspan="2">04,05</td><td colspan="2">UBUS 26 H</td><td colspan="2">01,15</td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2">UBUS A17 L</td><td colspan="2">04,05</td><td colspan="2">UBUS 27 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BDPC			
2	1	0	
0	0	0	IDLE
0	0	1	BDP ← CMI UD ← CMI
0	1	1	UD ← CMI FOR WRAP AROUND
1	0	0	BDP ← UNIBUS BYTE 0
1	0	1	BDP ← UNIBUS (2 BYTES)
1	1	0	BDP ← UNIBUS (1 BYTES)

PRTC			
2	1	0	
0	0	0	IDLE
0	0	1	UNIBUS DEVICE DOING DATA
0	1	0	PUT BUF CMI ON CMI FOR ADDRESS
0	1	1	PUT ADDRESS REG IN UDP ON UA (PURGE)
1	0	0	UNIBUS DEVICE DOING DATA
1	0	1	CPU DOING UNIBUS WRITE
1	1	1	CPU DOING UNIBUS READ

SC		
1	0	
0	0	WRITE MAP/CSR
0	1	READ MAP/CSR
1	0	IDLE

THIRD ANGLE PROJECTION		DESCRIPTION		DWG./PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES					
ANGLES ± 30°		CLASSE OF ACCURACY		NOMINAL DIMENSION RANGE INCHES	
SURFACE QUALITY		(CHECK ONE)		OVER 0 TO 0.2	OVER 0.2 TO 1.2
IN		MEDIUM		OVER 1.2 TO 4.0	OVER 4.0 TO 12.0
MICROINCHES		PREFERRED		OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
QUANTITY & VARIATION		DRN. I. M. C. 1/2/80		FIRST USED ON	
REMOVE BURRS AND BREAK SHARP CORNERS		CHK'D L. KERN 3/21/80		11750	
DO NOT SCALE DWG		ENG. S. SMITH 3/21/80		TITLE	
MATERIAL		PROJ. ENG. D. CARR 3/21/80		UNIBUS INTERFACE	
FINISH		PROD. W. B. 3/21/80		NEXT HIGHER ASSY.	
		100040-0		SIZE CODE	
		SCALE		D BD	
		SHEET 1 OF 1		NUMBER	
				100040-20	
				REV.	
				C	

Table of Contents

1 COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79  
33 FIELD DEFINITIONS  
137 FIRST FORK BREAKOUT  
231 CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION  
293 DATA THROUGH BUFFERED DATA PATH  
348 BDP DATA'S  
454 THIS SECTION HANDLES DATA'S TO THE DDP  
563 THIS PAGE IS WHERE WE COME FOR DATA'S THROUGH THE DIRECT DATA PATH  
618 PURGE CODE  
650 POWER UP CODE

UBI .MCR (160,5507) Micro-2.1 1B(40) 8152133 18-Feb-1980  
UBI .MIC (160,5507) COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79

.TCC \*COMET UNIBUS INTERFACE MICROCODE REV 015 12/19/79"  
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DC FIXED DEFINITION OF UA.CTRL.PCV AND RVC.INCR WERE SWAPPED  
FIXED DDP AND BDP DATA CODE TO HOLD BYTE 0 OF DATA IN UD  
LATCH FOR OFFSET CASE.  
ONLY E8,E7 CHANGE  
DC FIXED SEQUENCE AT BEGINNING OF READ SO AS TO NOT  
CAUSE UDP'S TO DRIVE UA BUS. E8,E7 CHANGE  
PR CHANGED DEFAULT OF UA.CTRL TO \*2\* TO GO ALONG WITH CHANGE  
FOR REV 001.  
DC ADDED CONSTRAINED WORD IN DDP.DATO WRAP CODE  
TO ALLOW UBI TO HOLD DATA ON BUS PROPERLY  
ALL ROMS  
DC FIXED RDP DATA CODE TO HOLD BYTE 0 ON WRAP-CHANGE  
TO URDATA FIELD TO KEEP HI-Z. E9 CHANGES  
DC FIXED DATCH BDP NO WRITE TO ASSERT SSYN BEFORE  
CHECKING TO SEE IF ITS THERE. E12,E11 CHANGE  
DC CHANGED PROMPT CONTROL FILE TO BLAST BDPC FIELD LOW TRUE  
ONLY E7 CHANGES  
DC ADDED APB FOR CMI DURING WRAP AROUND READS AND WRITES  
TO KEEP CMI DURING BOTH SETS  
DC CHANGED PROMPT CONTROL TIO BLAST BUT<1> LOW TRUE  
ONLY E10 CHANGES  
CHANGED MAIN.20, ALL ROMS  
DC CHANGED PROMPT TO BLAST BUT<0> LOW TRUE  
E10 CHANGES  
CHANGED RDP.DATI.50, DDP.40 AND DDP.47 TO BUT ON  
SSYN INSTEAD OF MSYN  
DC NUMEROUS CHANGES TO FIX UNALIGNED AND PB PROBLEMS  
DC FIX TO CPU.RO TO PREVENT IT FROM LEAVING  
GARBAGE ON THE CMI. E8,E9 CHANGE

```

133 .TOC "FIELD DEFINITIONS"
134 .RTOI
135 .HEXADECIMAL
136 ,
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141 ,
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143 ,
144 ,
145 ,
146 ,
147 BUFCMI=<23123>, .DEFAULT=0
148     ADDR#1
149     HI-Z=0
150
151     NEXT=<23116>, .NEXTADDRESS
152
153 BOPC=<45113>, .DEFAULT=0
154     DATI#1
155     DATAW#3
156     DATOW#4
157     DATO#5
158     PATO#6
159
160 PRTC=<42116>, .DEFAULT=0
161     DATI#1
162     UR_ADDR#2
163     PURGF.ADDR#3
164     PATO#4
165     CPU.WRT#5
166     CPU.PD#7
167
168 UA_CTRL=<4019>, .DEFAULT=2
169     XJT#0
170     HI-Z#1
171     RCV.INCR#3
172     RCV#2
173
174 MSGN=<4717>, .DEFAULT=0
175     ASSERT#1
176
177

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178 SSYN/=<616>, .DEFAULT=0
179 ASSEPT=1
180
181 UBADATA/=<514>, .DEFAULT=2
182 PCV=2
183 DRIVE.UO=1
184 DRIVE.UO,NDPB=3
185 HI-Z=0
186
187 CMI.ARB/=<113>, .DEFAULT=0
188 REQUEST=1
189
190 RUT/=<210>, .DEFAULT=0
191
192 EMPTY=1
193 ARB=2
194 SET.FLAG=3
195 UB.STATUS=4
196 CLK.FLAGS=5
197 CMI.STATUS=6
198 CMI.STATUS=7
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1115	REG.WRT?	"CMI.ARB/REQUEST,PRTC/UB.ADDR,BUT/ARB,BUFCHI/ADDR"
1116	REQ.RD?	"CMI.ARB/REQUEST,PRTC/UB.ADDR,RUT/ARE,UBDATA/HI-Z,BUFCHI/ADDR"
1117	REQ.PUR?	"CMI.ARB/REQUEST,PRTC/PURGE.ADDR,UA.CTRL/HI-Z,BUT/ARB,BUFCHI/ADDR"
1118	EMPTY?	"BUT/EMPTY"
1119	FIRST.FORK?	"BUT/FIRST.FORK,NEXT/MAIN.LODP"
1120	CMI.STAT?	"BUT/CMI.STATUS"
1121	UB.STAT?	"BUT/UP.STATUS"
1122		
1123	SSYN	"SSYN/ASSERT"
1124	MSYN	"MSYN/ASSEPT"
1125	INCR	"UA.CTRL/RCV.INCR"
1126	REQ	"CMI.ARB/REQUEST"
1127		
1128	DP_CMI	"BDPC/DATI,PRTC/DATI,UBDATA/HI-Z"
1129	DP_CMI.W	"BDPC/DATI,PRTC/DATI,UBDATA/HI-Z,BUFCHI/ADDR"
1130	UB_CMI.WRT	"PRTC/CPU.WRT,UBDATA/DRIVE.UO,UA.CTRL/XMIT"
1131	UB_CMI.WRT.NOPR	"PRTC/CPU.WRT,UBDATA/DRIVE.UO,NOPB,UA.CTRL/XMIT"
1132	UB_CMI.ADDR	"PRTC/CPU.RD,UA.CTRL/XMIT"
1133	UB_RD_DP	"PRTC/DATI,UBDATA/DRIVE.UO"
1134	REQ.XTRA?	"CMI.ARB/REQUEST,PRTC/UB.ADDR,RUT/SET.FLAG,UA.CTRL/RCV.INCR,BUFCHI/ADDR"
1135	HDLD.B0	"BDPC/DATI,UBDATA/HI-Z"
1136		

1137	.TOC "FIRST FORK BREAKOUT"	
1138	MAIN.LDOP1	THIS IS THE TOP OF FIRST FORK
1139		
1140		
1141	RUT/CLK.FLAGS,	BDP DATDB; CMI WRITE
1142	BDPC/DAT0B,	
1143	NEXT/BDP.DATO	
1144		
1145		
1146	RUT/CLK.FLAGS,	BDP DAT0; CMI WRITE
1147	BDPC/DATD,	IPUT DATA IN BUFFER
1148		
1149	NEXT/BDP.DATO	
1150		
1151	BDPC/DAT0B,	BDP DAT0B,BUFFER NDT FULL
1152	RUT/CLK.FLAGS,	
1153	NEXT/BDP.DATD.20	
1154		
1155		
1156	BDPC/DAT0,	BDP DAT0,BUFFER NOT FULL
1157	RUT/CLK.FLAGS,	FOR DFFSET CAUSING WRAPAROUND
1158	NEXT/MAIN.20	
1159		
1160		
1161	BDPC/DATI.W,	BDP DATI,LDNGWRD WRAP
1162	REQ.RD?,INCR,	
1163	NEXT/BDP.DATI.30	1:1RST WRD IS IN THE BUFFER
1164		
1165		
1166	PRTC/DATI,BDPC/DATI.W,	BDP DATI,DATA AVAILABLE IN
1167	UBDATA/HI-Z,REQ,	IBUFFER*****
1168	NEXT/BDP.DATI.45	
1169		
1170		
1171	BDPC/DATD,	BDP DAT0B,OFFSET PUTS BYTE
1172	INCR,BUT/SET.FLAG,	IN NEXT LDNGWRD
1173	NEXT/BDP.DATO.20	
1174		
1175		
1176	REQ.RD?	BDP DATI,BUFFER EMPTY
1177	NEXT/BDP.DATI.10	
1178		

THIS PAGE HOLDS THE SECOND EIGHT PLACES WHERE THE FIRST FORK GOES TO

```
1179      11000-----;
1180      PRTC/CPU.WRT,      ;CPU DOING WRITE TO UNIBUS
1181      UBDATA/HI-2,      ;GET READY TO ASSERT STUFF
1182      UA.CTRL/HI-2,      ;ON UNIBUS
1183      UB.STAT?,          ;CHECK SSYN REMOVED FROM UNIBUS
1184      NEXT/CPU.WRT      ;GO TIME DESKEW INTERVAL
1185
1186      11001-----;
1187      PRTC/CPU.RD,        ;CPU DOING READ FROM UNIBUS
1188      UA.CTRL/HI-2,      ;SEE COMMENTS ABOVE
1189      UB.STAT?,          ;
1190      NEXT/CPU.RD
1191
1192      11010-----;
1193      REQ.WRT?,          ;DDP DATO(R)
1194      NEXT/DDP.DATO
1195
1196      11011-----;
1197      REQ.RD?,           ;DDP DATI
1198      NEXT/DDP.DATI
1199
1200      11020-----;
1201      EMPT?,             ;PURGE, CHECK FOR EMPTY
1202      NEXT/PURGE
1203
1204      11021-----;
1205      EMPT?,             ;PURGE, CHECK FOR EMPTY
1206      NEXT/PURGE
1207
1208      11100-----;
1209      REQ.XTRA?,         ;DDP DATO WRAP TO NEXT LONGWORD
1210      NEXT/DDP.DATO,20  ;ALSO HERE FOR INTERRUPTS
1211
1212      IDLE;
1213
1214      11111-----;
1215      FIRST.FORK?       ;NOTHING GOING ON, KEEP TRYING
1216
1217      11112-----;
1218      HERE TO SEE IF ;0011 WAS NO WRITE OR OFFSET
1219      BUT/CMI.STATUS    ;NOW THAT UDP ADDR IS THERE, TRY AGAIN
1220
1221      11113-----;
1222      WRAP AROUND, WRITE NEEDED
1223
1224      11114-----;
1225      REQ.WRT?,         ;WRAP AROUND, WRITE NEEDED
1226      NEXT/ROP.DATO,05
1227
1228      11115-----;
1229      SSYN,NEXT/DDP.45  ;NO WRITE NEEDED
1230
1231
```

.TOC \*CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION\*

```
1231      CPU.WRT;
1232
1233      110-----;
1234      UB.CMI.WRT,        ;BRANCH COMES HERE IF SSYN NOT ASSERTED
1235      NEXT/CPU.WRT,10    ;ASSERT ADDRESS AND DATA ON UNIBUS
1236
1237      11-----;
1238      UB.CMI.WRT,        ;HERE IF SSYN LEFT ASSERTED FROM
1239      UB.STAT?,          ;LAST UNIBUS TRANSACTION
1240
1241      CPU.WRT,10;
1242
1243      11-----;
1244      UB.CMI.WRT,NOPB    ;HEATING UP TIME FOR ADDR/DATA
1245
1246      110-----;
1247      MSYN,UB.STAT?,     ;ASSERT MSYN AND WAIT FOR SSYN
1248      UB.CMI.WRT,NOPB,   ;
1249      NEXT/CPU.WRT,20
1250
1251      111-----;
1252      UB.CMI.WRT,NOPB    ;MSYN REMOVED,BECAUSE SSYN ARRIVED
1253
1254      CPU.WRT,25;
1255
1256      11-----;
1257      UA.CTRL/HI-2,      ;PREVENT TRISTATE OVERLAP
1258      NEXT/IDLE
1259
1260
1261
```

! UBI .MCR [160,5507] Micro-2.1 1B(40) 9152133 18-Feb-1980  
! UBI .MIC [160,5507] CPU READS AND WRITES TO THE UNIBUS ARE HANDLED IN THIS SECTION

! THIS SECTION FOR CPU READS TO UNIBUS

1262  
1263  
1264  
1265  
1266  
1267  
1268  
1269  
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1292

U 014, 291C,20  
UB-CMI,ADDR,  
NEXT/CPU.RD.10  
ISSYN REMOVED FROM LAST TRANSACTION  
! ASSERT ADDRESS AND BEGIN DESKEW TIME

U 015, 141C,24  
UB-CMI,ADDR,  
UR,STAT7,  
NEXT/CPU.RD  
ISSYN STILL ASSERTED, DON'T COUNT  
! DESKEW TIME YET

U 029, 3A1C,20  
CPU.RD.10  
UR-CMI,ADDR  
! EAT 125 FOR DESKEW

U 03A, 3A1C,A4  
CPU.RD.20  
UB-CMI,ADDR,  
MSYN,UB,STAT7,  
NEXT/CPU.RD.20  
! ASSERT MSYN AND WAIT FOR SSYN

U 03B, 3B1C,A0  
UR-CMI,ADDR,MSYN  
! KEEP MSYN SO SLAVE HOLDS DATA

U 038, 2B14,00  
PRTC/CPU,RT,  
UPDATA/HI-Z,UA,CTRL/XMIT,  
NEXT/CPU.WRT.25  
! KEEP ADDRESS ON UNIBUS

! UBI .MCR [160,5507] Micro-2.1 1B(40) 9152133 18-Feb-1980  
! UBI .MIC [160,5507] DATA THROUGH BUFFERED DATA PATH

.TOC "DATA THROUGH BUFFERED DATA PATH"  
! DATA(0) THROUGH BDP THAT NEEDS TO DO A CMI WRITE  
BDP.DATO.05

U 048, 9812,26  
PRTC/DATO,CMI,STAT7,  
RUF-CMI/ADDR,  
NEXT/BDP.DATO.10  
ISSYN DISAPPEARED JUST AS WE WON THE BUS  
! GO THROUGH WITH IT ANYWAY, WE'RE ALREADY ON BUS

U 049, 0F02,20  
NEXT/IDLE  
! LOST MSYN, ABORT

U 04A, 9812,26  
PRTC/DATO,CMI,STAT7,  
BUP-CMI/ADDR,  
NEXT/BDP.DATO.10  
! BUS WON, ASSERT DATA NEXT CYCLE  
! AND CHECK FOR LONGWORD WRAPPEDBZ

U 04B, C80A,2A  
REQ.WRT7,  
NEXT/BDP.DATO.05  
! WAITING TO WIN THE CMI

U 018, 1812,26  
BDP.DATO.10  
! THE NEXT FOUR ENTRIES OCCUR FOR THE OFFSET CASE WHERE THE DATA WRAPS  
PRTC/DATO,CMI,STAT7,  
NEXT/BDP.DATO.10  
! DBL, STILL HELD ON BUS, KEEP WAITING

U 019, 1812,26  
PRTC/DATO,CMI,STAT7,  
NEXT/BDP.DATO.10  
! DBBZ STILL HELD ON BUS, KEEP WAITING

U 01A, 6F02,20  
INCR,BOPC/DATON,  
BUT/SET,FLAG,  
NEXT/BDP.DATO.20  
! DBBZ HAS GONE AWAY, PUT BYTE IN  
! BOP REGS AND SET FLAGS

U 01B, 1F03,23  
! THESE FOUR ARE FOR NO WRAP AROUND  
PRTC/DATO,CMI,STAT7,  
NEXT/BDP.DATO.10  
! NO WRAP AROUND, DBBZ STILL ASSERTED  
! SO KEEP THE DATA ON THE BUS

U 01C, 1812,26  
PRTC/DATO,CMI,STAT7,  
NEXT/BDP.DATO.10  
! NO WRAP AROUND, DBBZ STILL ASSERTED  
! SO KEEP THE DATA ON THE BUS

U 01E, 6F02,20  
NEXT/DDP.50  
! INX4

U 01F, 6B02,60  
BOP.DATO.20  
SSYN,NEXT/DDP.45  
! DBBZ WENT AWAY, ASSERT SSYN

```
1348 .TOC "BDP DATI'S"
1349 !HERE FOR BDP DATI'S THAT NEED CMI ACTION
1350 #00
1351 BDP,DATI.10;
1352 !00-----;
1353 CMI,STAT?, !GOT THE BUS, JUST AS WE LOST MSYN
1354 BUFCMI/ADDR,
1355 NEXT/BDP,DATI.20
1356 !01-----;
1357 NEXT/IDLE !LOST MSYN, ABORT
1358 !10-----;
1359 CMI,STAT?, !GOT THE BUS ,GET READY FOR DATA
1360 BUFCMI/ADDR,
1361 NEXT/BDP,DATI.20
1362 !11-----;
1363 REQ,RD?, !WF GOT HERE CAUSE WE DIDN'T WIN THE BUS
1364 NEXT/BDP,DATI.10
1365 #002
1366 BDP,DATI.20;
1367 !THESE FOUR ARE FOR THE DATA WRAP AROUND CASE
1368 !000-----;
1369 DP,CMI,CMI,STAT?,REQ, !DBBZ STILL ASSERTED, KEEP WAITING
1370 NEXT/BDP,DATI.20
1371 !001-----;
1372 DP,CMI,CMI,STAT?,REQ, !DBBZ STILL ASSERTED, KEEP WAITING
1373 NEXT/BDP,DATI.20
1374 !002-----;
1375 DP,CMI,CMI,STAT?,REQ, !DBBZ STILL ASSERTED, KEEP WAITING
1376 NEXT/BDP,DATI.20
1377 !010-----;
1378 NEXT/BDP.50 !DBBZ GONE, NXM STATUS RETURNED
1379 !111-----;
1380 ROPC/DATI, !DATA IN BUFFER, NOW MOVE TO UD
1381 UD,DATA/HT-Z,REQ,
1382 NEXT/BDP,DATI.35
1383 !THESE FOUR ENTRIES ARE FOR NO WRAP-AROUND
1384 !100-----;
1385 DP,CMI,CMI,STAT?, !DBBZ STILL ASSERTED, KEEP WAITING
1386 NEXT/BDP,DATI.20,REQ !*****REMOVE REQ WITH UCN-C
1387 !001-----;
1388 DP,CMI,CMI,STAT?, !DBBZ STILL ASSERTED, KEEP WAITING
1389 NEXT/BDP,DATI.20,REQ !*****REMOVE REQ WITH UCN-C
1390 !110-----;
1391 NEXT/BDP.50 !DBBZ GONE, NXM STATUS RETURNED
1392 !111-----;
1393 UB,RD,DP,SSYN, !DBBZ'S GONE, WE GOT THE DATA
1394 NEXT/BDP,DATI.55,REQ !*****REMOVE REQ WITH UCN-C
1395 !000-----;
1396 BDP,DATI.35;
1397 !11-----;
1398 REQ,RD?,INCR,ROPD/DATI, !GET THE BUS FOR THE SECOND
1399 NEXT/BDP,DATI.30
1400 #100
1401 BDP,DATI.40;
1402 !100-----;
1403 DP,CMI,CMI,STAT?,INCR, !DBBZ STILL ASSERTED, KEEP WAITING
1404 NEXT/BDP,DATI.40,REQ !*****REMOVE REQ WITH UCN-C
1405 !101-----;
1406 DP,CMI,CMI,STAT?,INCR, !DBBZ STILL ASSERTED, KEEP WAITING
1407 NEXT/BDP,DATI.40,REQ !*****REMOVE REQ WITH UCN-C
1408 !110-----;
1409 NEXT/BDP.50 !DBBZ GONE, NXM STATUS RETURNED
1410 !111-----;
1411 UB,RD,DP,SSYN, !DBBZ'S GONE, WE GOT THE DATA
1412 NEXT/BDP,DATI.55,REQ !*****REMOVE REQ WITH UCN-C
1413 !000-----;
1414 BDP,DATI.50;
1415 !10-----;
1416 FIRST,FORK? !ND MSYN, REMOVE DATA AND SSYN
1417 !11-----;
1418 UB,RD,DP,
1419 SSYN,UP,STAT?, !WAIT FOR MSYN TO GO AWAY
1420 NEXT/BDP,DATI.50,REQ !*****REMOVE REQ WITH UCN-C
1421 !100-----;
1422 BDP,DATI.55;
1423 !11-----;
1424 !111-----;
1425 !111-----;
1426 !111-----;
1427 !111-----;
1428 !111-----;
1429 !111-----;
1430 !111-----;
1431 !111-----;
1432 !111-----;
1433 !111-----;
1434 !111-----;
1435 !111-----;
1436 !111-----;
1437 !111-----;
1438 !111-----;
1439 !111-----;
1440 !111-----;
1441 !111-----;
1442 !111-----;
1443 !111-----;
1444 !111-----;
1445 !111-----;
1446 !111-----;
1447 !111-----;
1448 !111-----;
1449 !111-----;
1450 !111-----;
1451 !111-----;
1452 !111-----;
1453 !111-----;
```

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1403 !100-----;
1404 !100-----;
1405 !100-----;
1406 !100-----;
1407 !100-----;
1408 !100-----;
1409 !100-----;
1410 !100-----;
1411 !100-----;
1412 !100-----;
1413 !100-----;
1414 !100-----;
1415 !100-----;
1416 !100-----;
1417 !100-----;
1418 !100-----;
1419 !100-----;
1420 !100-----;
1421 !100-----;
1422 !100-----;
1423 !100-----;
1424 !100-----;
1425 !100-----;
1426 !100-----;
1427 !100-----;
1428 !100-----;
1429 !100-----;
1430 !100-----;
1431 !100-----;
1432 !100-----;
1433 !100-----;
1434 !100-----;
1435 !100-----;
1436 !100-----;
1437 !100-----;
1438 !100-----;
1439 !100-----;
1440 !100-----;
1441 !100-----;
1442 !100-----;
1443 !100-----;
1444 !100-----;
1445 !100-----;
1446 !100-----;
1447 !100-----;
1448 !100-----;
1449 !100-----;
1450 !100-----;
1451 !100-----;
1452 !100-----;
1453 !100-----;
```



K-MP-L0004-0-21-C

UBI .MCR (160,5507] Micro-2.1 18(40) 8:52133 18-Feb-1980  
UBI .MIC (160,5507] THIS SECTION HANDLES DATO'S TO THE DDP

.TOC "THIS SECTION HANDLES DATO'S TO THE DDP"

DDP.DATO1

PRTC/DATO,BUFCMI/ADDR, IWE GOT IT  
CMI.STAT?,  
NEXT/DDP.DATO.10

U 05C, B012,26

NEXT/IOLF

U 05D, 3F02,20

PRTC/DATO,BUFCMI/ADDR, IWE GOT IT  
CMI.STAT?,  
NEXT/DDP.DATO.10

U 05E, B012,26

REQ.WRT?,  
NEXT/DDP.DATO

U 05F, DC0A,2A

DDP.DATO.10:

THE FOUR CASES ARE FOR THE WRAPAROUND SITUATION

PRTC/DATO,CMI.STAT?,  
REQ.NEXT/DDP.DATO.10

U 030, 3012,2E

PRTC/DATO,CMI.STAT?,  
REQ.NEXT/DDP.DATO.10

U 031, 3012,2E

PRTC/DATO,CMI.STAT?,  
REQ.NEXT/DDP.DATO.10

U 032, 6F02,20

REQ.XTRA?,  
NEXT/DDP.DATO.20

U 033, E008,2B

THESE CASES ARE FOR NO WRAP

PRTC/DATO,CMI.STAT?,  
NEXT/DDP.DATO.10

U 034, 3012,26

PRTC/DATO,CMI.STAT?,  
NEXT/DDP.DATO.10

U 035, 3012,26

SSYN,NEXT/DDP.45

U 036, 6F02,20

SSYN,NEXT/DDP.45

U 037, 6R02,60

1507

UBI .MCR (160,5507] Micro-2.1 18(40) 8:52133 18-Feb-1980

UBI .MIC (160,5507] THIS SECTION HANDLES DATO'S TO THE DDP

THIS PAGE CONTINUES THE DDP DATO WRAP CASE CODE,  
AND ALSO HAS THE WAITING FOR MSYN TO GO AWAY STUFF

DDP.DATO.20:

PRTC/DATO,BUT/SET.FLAG, IBUS MON  
INCR,BUFCMI/ADDR,  
NEXT/DDP.DATO.25

U 060, E713,23

NEXT/IOLF

U 061, 0F02,20

PRTC/DATO,BUT/SET.FLAG, IBUS MON  
INCR,BUFCMI/ADDR,  
NEXT/DDP.DATO.25

U 062, E713,23

REQ.XTRA?,  
NEXT/DDP.DATO.20

U 063, E008,2B

PRTC/DATO,CMI.STAT?  
THIS IS CONSTRAINED AS TARGET OF SET.FLAG

U 067, 3C12,26

PRTC/DATO,CMI.STAT?,  
NEXT/DDP.DATO.30

U 03C, 3C12,26

PRTC/DATO,CMI.STAT?,  
NEXT/DDP.DATO.30

U 03D, 3C12,26

SSYN,NEXT/DDP.45

U 03E, 6F02,20

SSYN,NEXT/DDP.45

U 03F, 6R02,60

FIRST.FORK?  
NO MSYN, REMOVE DATA AND SSYN

U 06A, 0002,27

SSYN,UB.STAT?,  
NEXT/DDP.40

U 06B, 6A02,64

DDP.47:

FIRST.FORK?  
NO MSYN, REMOVE DATA AND SSYN

U 06E, 0002,27

UB.STAT?,NEXT/DDP.47

U 06F, 6F02,24

1562

```
1563 .TOC "THIS PAGE IS WHERE WE COME FOR DATI'S THROUGH THE DIRECT DATA PATH"
1564 ,
1565 EQU
1566 DDP,DATI:
1567     1100-----;
1568     CMI,STAT7,      !GOT THE BUS, WAIT FOR DATA
1569     BUFCMI/ADDR,
1570     NEXT/DDP,DATI.10
1571
1572     101-----;
1573     NEXT/IDLE      !LOST MSYN
1574
1575     110-----;
1576     CMI,STAT7,      !GOT THE BUS, WAIT FOR DATA
1577     BUFCMI/ADDR,
1578     NEXT/DDP,DATI.10
1579
1580     111-----;
1581     REQ,RD7,        !TRY TO GET THAT BUS
1582     NEXT/DDP,DATI
1583
1584     1100-----;
1585     DDP,DATI.10;
1586     !THESE FOUR ARE FOR THE WRAP CASE
1587     1000-----;
1588     OP_CMI,W,CMI,STAT7,REQ, !WAITING FOR DATA
1589     NEXT/DDP,DATI.10
1590
1591     1001-----;
1592     DP_CMI,W,CMI,STAT7,REQ, !WAITING FOR DATA
1593     NEXT/DDP,DATI.10
1594
1595     1010-----;
1596     NEXT/DDP.50      !NXM
1597
1598     1011-----;
1599     REQ,RD7,INCR,BDPC/DATI, !WE GOT THE FIRST, NOW DO THE SECOND
1600     NEXT/BDP,DATI.30
1601
1602     1100-----;
1603     DP_CMI,W,CMI,STAT7,      !WAITING FOR DATA, NO WRAP-AROUND
1604     NEXT/DDP,DATI.10,REQ    !*****REMOVE REQ WITH UCN-C
1605
1606     1101-----;
1607     DP_CMI,W,CMI,STAT7,      !WAITING FOR DATA, NO WRAP-AROUND
1608     NEXT/DDP,DATI.10,REQ    !*****REMOVE REQ WITH UCN-C
1609
1610     1110-----;
1611     NEXT/DDP.50      !NXM, WAIT THE MSYN OUT
1612
1613     1111-----;
1614     UN,RD_OP,        !DBUZ WENT AWAY
1615     SSYN,            !GIVE THE UBUS DATA, AND ISSUE SSYN
1616     NEXT/BDP,DATI.55,REQ    !*****REMOVE REQ WITH UCN-C
1617
```

```
1618 .TOC "PURGE CODE"
1619 !THIS PAGE HANDLES PURGES
1620
1621     110
1622     PURGE;
1623
1624     110-----;
1625     REQ,PUR7,        !NOT EMPTY IF WE GET HERE
1626     NEXT/PURGE.10
1627
1628     111-----;
1629     NEXT/IDLE      !IF PUREG WAS EMPTY WE CLEARED IT
1630
1631     110
1632     PURGE.10;
1633     110-----;
1634     PRTC/DATO,UA,CTRL/BI-Z,
1635     BUFCMI/ADDR,
1636     NEXT/PURGE.20
1637
1638     111-----;
1639     REQ,PUR7,
1640     NEXT/PURGE.10
1641
1642     1101
1643     PURGE.20;
1644     1101-----;
1645     PRTC/DATO,CMI,STAT7,    !DO WRITE AND WAIT FOR NO DBUZ
1646     NEXT/PURGE.20
1647
1648     1111-----;
1649     NEXT/IDLE      !ALL DONE
```

UBI .MCR [160,5507] M1C10-2.1 1B(40)  
UBI .M1C [160,5507] POWER UP CODE

8152133 18-Feb-1980

1650 .TDC "POWER UP CODE"  
1651 .REGION /80,OFF  
1652

U 080, 0F02,20 1653 NEXT/IDLE  
U 081, 0F02,20 1654 NEXT/IDLE  
U 082, 0F02,20 1655 NEXT/IDLE  
U 083, 0F02,20 1656 NEXT/IDLE  
U 084, 0F02,20 1657 NEXT/IDLE  
U 085, 0F02,20 1658 NEXT/IDLE  
U 086, 0F02,20 1659 NEXT/IDLE  
U 087, 0F02,20 1660 NEXT/IDLE  
U 088, 0F02,20 1661 NEXT/IDLE  
U 089, 0F02,20 1662 NEXT/IDLE  
U 08A, 0F02,20 1663 NEXT/IDLE  
U 08B, 0F02,20 1664 NEXT/IDLE  
U 08C, 0F02,20 1665 NEXT/IDLE  
U 08D, 0F02,20 1666 NEXT/IDLE  
U 08E, 0F02,20 1667 NEXT/IDLE  
U 08F, 0F02,20 1668 NEXT/IDLE  
U 090, 0F02,20 1669 NEXT/IDLE  
U 091, 0F02,20 1670 NEXT/IDLE  
U 092, 0F02,20 1671 NEXT/IDLE  
U 093, 0F02,20 1672 NEXT/IDLE  
U 094, 0F02,20 1673 NEXT/IDLE  
U 095, 0F02,20 1674 NEXT/IDLE  
U 096, 0F02,20 1675 NEXT/IDLE  
U 097, 0F02,20 1676 NEXT/IDLE  
U 098, 0F02,20 1677 NEXT/IDLE  
U 099, 0F02,20 1678 NEXT/IDLE  
U 09A, 0F02,20 1679 NEXT/IDLE  
U 09B, 0F02,20 1680 NEXT/IDLE  
U 09C, 0F02,20 1681 NEXT/IDLE  
U 09D, 0F02,20 1682 NEXT/IDLE  
U 09E, 0F02,20 1683 NEXT/IDLE  
U 09F, 0F02,20 1684 NEXT/IDLE  
1685

UBI .MCR [160,5507] M1C10-2.1 1B(40)  
UBI .M1C [160,5507] POWER UP CODE

8152133 18-Feb-1980

U 0A0, 0F02,20 1686 NEXT/IDLE  
U 0A1, 0F02,20 1687 NEXT/IDLE  
U 0A2, 0F02,20 1688 NEXT/IDLE  
U 0A3, 0F02,20 1689 NEXT/IDLE  
U 0A4, 0F02,20 1690 NEXT/IDLE  
U 0A5, 0F02,20 1691 NEXT/IDLE  
U 0A6, 0F02,20 1692 NEXT/IDLE  
U 0A7, 0F02,20 1693 NEXT/IDLE  
U 0A8, 0F02,20 1694 NEXT/IDLE  
U 0A9, 0F02,20 1695 NEXT/IDLE  
U 0AA, 0F02,20 1696 NEXT/IDLE  
U 0AB, 0F02,20 1697 NEXT/IDLE  
U 0AC, 0F02,20 1698 NEXT/IDLE  
U 0AD, 0F02,20 1699 NEXT/IDLE  
U 0AE, 0F02,20 1700 NEXT/IDLE  
U 0AF, 0F02,20 1701 NEXT/IDLE  
U 0B0, 0F02,20 1702 NEXT/IDLE  
U 0B1, 0F02,20 1703 NEXT/IDLE  
U 0B2, 0F02,20 1704 NEXT/IDLE  
U 0B3, 0F02,20 1705 NEXT/IDLE  
U 0B4, 0F02,20 1706 NEXT/IDLE  
U 0B5, 0F02,20 1707 NEXT/IDLE  
U 0B6, 0F02,20 1708 NEXT/IDLE  
U 0B7, 0F02,20 1709 NEXT/IDLE  
U 0B8, 0F02,20 1710 NEXT/IDLE  
U 0B9, 0F02,20 1711 NEXT/IDLE  
U 0BA, 0F02,20 1712 NEXT/IDLE  
U 0BB, 0F02,20 1713 NEXT/IDLE  
U 0BC, 0F02,20 1714 NEXT/IDLE  
U 0BD, 0F02,20 1715 NEXT/IDLE  
U 0BE, 0F02,20 1716 NEXT/IDLE  
U 0BF, 0F02,20 1717 NEXT/IDLE  
1718

8:52:33 18-Feb-1989

```

I UBI      .MCR [160,5507] Micro-2.1 IB(40)
; UBI      .MIC [160,5507] POWER UP CODE

U 0C0, 0F02,20 1719      NEXT/IDLE
U 0C1, 0F02,20 1720      NEXT/IDLF
U 0C2, 0F02,20 1721      NEXT/IDLE
U 0C3, 0F02,20 1722      NEXT/IDLE
U 0C4, 0F02,20 1723      NEXT/IDLE
U 0C5, 0F02,20 1724      NEXT/IDLE
U 0C6, 0F02,20 1725      NEXT/IDLE
U 0C7, 0F02,20 1726      NEXT/IDLE
U 0C8, 0F02,20 1727      NEXT/IDLE
U 0C9, 0F02,20 1728      NEXT/IDLE
U 0CA, 0F02,20 1729      NEXT/IDLE
U 0CB, 0F02,20 1730      NEXT/IDLE
U 0CC, 0F02,20 1731      NEXT/IDLE
U 0CD, 0F02,20 1732      NEXT/IDLE
U 0CE, 0F02,20 1733      NEXT/IDLE
U 0CF, 0F02,20 1734      NEXT/IDLE
U 0D0, 0F02,20 1735      NEXT/IDLE
U 0D1, 0F02,20 1736      NEXT/IDLE
U 0D2, 0F02,20 1737      NEXT/IDLE
U 0D3, 0F02,20 1738      NEXT/IDLE
U 0D4, 0F02,20 1739      NEXT/IDLE
U 0D5, 0F02,20 1742      NEXT/IDLE
U 0D6, 0F02,20 1741      NEXT/IDLE
U 0D7, 0F02,20 1742      NEXT/IDLF
U 0D8, 0F02,20 1743      NEXT/IDLE
U 0D9, 0F02,20 1744      NEXT/IDLE
U 0DA, 0F02,20 1745      NEXT/IDLE
U 0DB, 0F02,20 1746      NEXT/IDLE
U 0DC, 0F02,20 1747      NEXT/IDLE
U 0DD, 0F02,20 1748      NEXT/IDLF
U 0DE, 0F02,20 1749      NEXT/IDLE
U 0DF, 0F02,20 1750      NEXT/IDLE
                                1751
```

8:52:33 18-Feb-1989

```

I UBI      .MCR [160,5507] Micro-2.1 IB(40)
; UBI      .MIC [160,5507] POWER UP CODE

U 0E0, 0F02,20 1752      NEXT/IDLE
U 0E1, 0F02,20 1753      NEXT/IDLE
U 0E2, 0F02,20 1754      NEXT/IDLE
U 0E3, 0F02,20 1755      NEXT/IDLE
U 0E4, 0F02,20 1756      NEXT/IDLE
U 0E5, 0F02,20 1757      NEXT/IDLE
U 0E6, 0F02,20 1758      NEXT/IDLE
U 0E7, 0F02,20 1759      NEXT/IDLE
U 0E8, 0F02,20 1760      NEXT/IDLE
U 0E9, 0F02,20 1761      NEXT/IDLE
U 0EA, 0F02,20 1762      NEXT/IDLE
U 0EB, 0F02,20 1763      NEXT/IDLE
U 0EC, 0F02,20 1764      NEXT/IDLE
U 0ED, 0F02,20 1765      NEXT/IDLE
U 0EE, 0F02,20 1766      NEXT/IDLE
U 0EF, 0F02,20 1767      NEXT/IDLE
U 0F0, 0F02,20 1768      NEXT/IDLF
U 0F1, 0F02,20 1769      NEXT/IDLE
U 0F2, 0F02,20 1770      NEXT/IDLE
U 0F3, 0F02,20 1771      NEXT/IDLE
U 0F4, 0F02,20 1772      NEXT/IDLE
U 0F5, 0F02,20 1773      NEXT/IDLF
U 0F6, 0F02,20 1774      NEXT/IDLE
U 0F7, 0F02,20 1775      NEXT/IDLE
U 0F8, 0F02,20 1776      NEXT/IDLE
U 0F9, 0F02,20 1777      NEXT/IDLE
U 0FA, 0F02,20 1778      NEXT/IDLF
U 0FB, 0F02,20 1779      NEXT/IDLE
U 0FC, 0F02,20 1780      NEXT/IDLE
U 0FD, 0F02,20 1781      NEXT/IDLE
U 0FE, 0F02,20 1782      NEXT/IDLE
U 0FF, 0F02,20 1783      NEXT/IDLE
                                1784
```





Location / Line Number Index

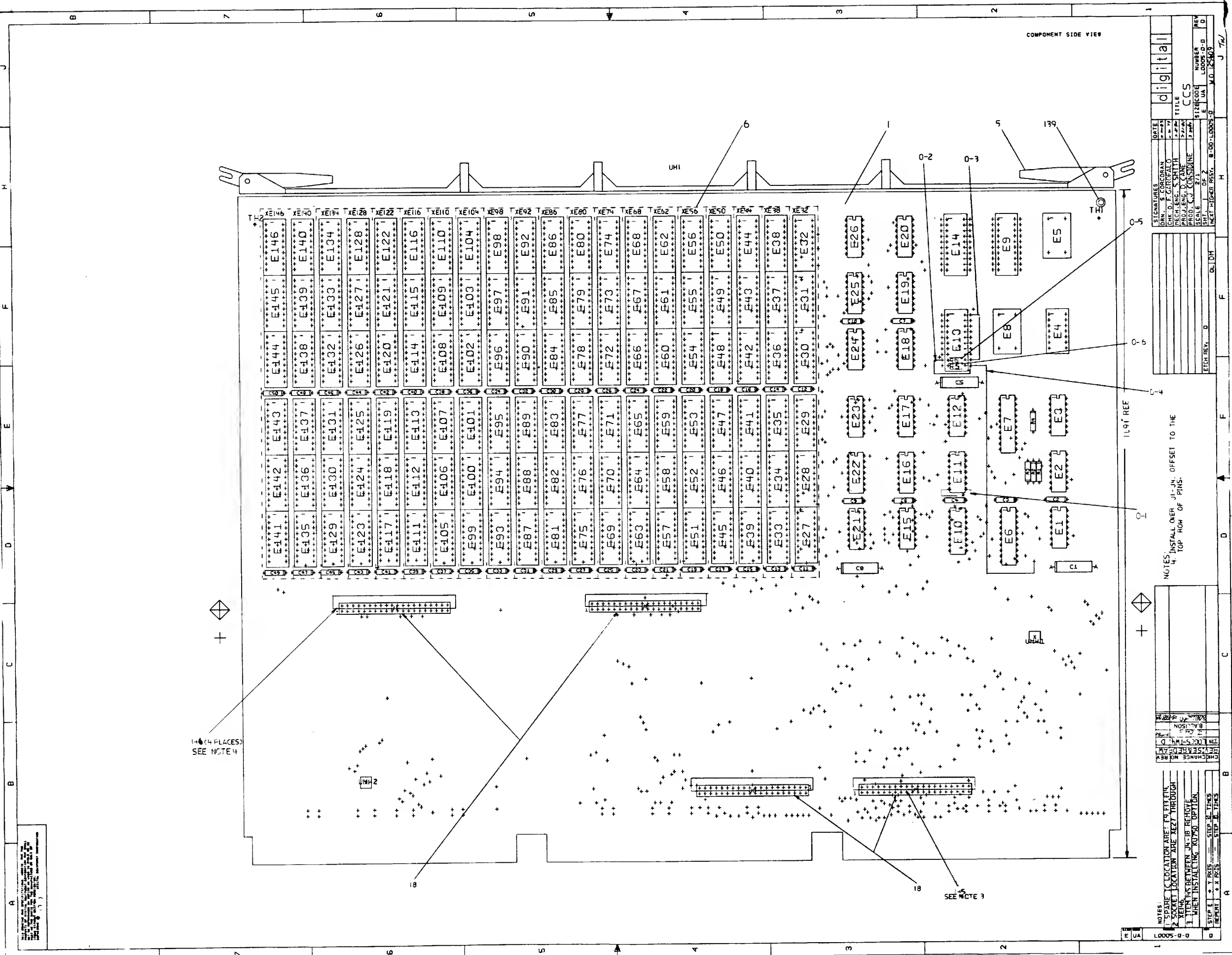
U 000	143#	148#	153#	158#	163#	168#	173#	177#
U 008	186#	192#	196#	200#	204#	208#	212#	216#
U 010	236#	241#	221#	226#	267#	272#	245	229#
U 018	318#	322#	325#	330#	335#	339#	342#	346#
U 020	373#	377#	380#	385#	390#	394#	397#	401#
U 028	260#	276	252#	255#	428#	432#	435#	440#
U 030	480#	484#	487#	491#	496#	500#	503#	506#
U 038	291	283#	286#	286#	536#	540#	543#	546#
U 040	589#	593#	596#	600#	604#	608#	611#	616#
U 048	300#	303#	308#	312#	409#	412#	417#	422#
U 050	355#	358#	363#	367#	460#	463#	468#	472#
U 058			445#	451#				
U 060	515#	518#	523#	527#			558#	561#
U 068			550#	554#			625#	
U 070	570#	573#	578#	582#				628#
U 078			635#	639#				
U 080	653	654	655#	656	657	658	659	660
U 088	661	662	663	664	665	666	667	668
U 090	669	670#	671	672	673	674	675	676
U 098	677	678	679	680#	681	682	683	684
U 0A0	686	687	688	689	690	691	692	693
U 0A8	694	695	696	697	698	699	700#	701
U 0B0	702	703	704	705	706	707	708	709
U 0B8	710	711	712	713	714	715	716	717
U 0C0	719	720	721	722	723	724	725	726
U 0C8	727	728	729	730#	731	732	733	734
U 0D0	735	736	737	738	739	740	741	742
U 0D8	743	744	745	746	747	748	749	750
U 0EA	752	753	754	755	756	757	758	759
U 0E8	760	761	762	763	764	765	766	767
U 0F0	768	769	770#	771	772	773	774	775
U 0F8	776	777	778	779	780	781	782	783

Error Summary

Memory No.	Microwords	High Addr
U	236	255
Total number of microwords used:		
	236	255
Highest address(decimal):		
	0	0
Pass 1 warnings detected:		
	0	0
Pass 1 errors detected:		
	0	0
Pass 2 warnings detected:		
	0	0
Pass 2 errors detected:		
	0	0







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1-4 (4 PLACES)  
SEE NOTE 4

SEE NOTE 3

NOTES:  
1. WIRE LOCATION ARE: 1-9 PINS  
2. SOCKET LOCATION ARE: 1-18 THROUGH  
3. ITEMS BETWEEN 1-18 REMOVE  
WHEN INSTALLING K0750 OPTION  
4. STEP 1 - 1-9 PINS  
5. STEP 2 - 1-18 PINS

NOTES:  
4. INSTALL OVER J1-J4, OFFSET TO THE  
TOP ROW OF PINS.

SIGNATURES	DATE	TITLE	SIZE CODE	NUMBER	REV
DRN. S. CORCORAN	1-7-72	digital	CCS	10005-0-0	D
CHK. D. J. GILBERT	1-7-72				
APP. J. E. GILBERT	1-7-72				
PROJ. ENG. D. GILBERT	1-7-72				
PROJ. C. J. CONSIDINE	1-7-72				
SCALE 2/1	OF 2				
SMT.					
TEST DIRECT REPLY	8-00-10005-0				
ETCH REV.					
DLT DM					

REWORK INSTRUCTIONS

WIRE ADDS SIDE 1:  
O-1 FROM E11-7 TO E11-8  
O-2 FROM E13-9 TO E13-11  
O-3 FROM E13-12 TO E13-20  
O-4 FROM E13-11 TO RIGHT OF E6-11

COMPONENT ADDS SIDE 1:  
C-5 ADD R6 FROM E13-9 TO E13-12  
C-6 ADD R5 FROM E13-10 TO E13-11

REWORK INSTRUCTIONS

CCS L0005-0-0



AUTOMATED BY PRTLST.3L(31)

## P A R T S L I S T

SHEET A2 OF A4

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
31	31		23833F1-00	F1-01	1	E39
32	32		23834F1-00	F1-01	1	E40
33	33		23835F1-00	F1-01	1	E41
34	34		23836F1-00	F1-01	1	E42
35	35		23837F1-00	F1-01	1	E43
36	36		23838F1-00	F1-01	1	E44
37	37		23839F1-00	F1-01	1	E45
38	38		23840F1-00	F1-01	1	E46
39	39		23841F1-00	F1-01	1	E47
40	40		23842F1-00	F1-01	1	E48
41	41		23718F1-00	F1-01	1	E49
42	42		23843F1-00	F1-01	1	E50
43	43		23504F1-00	F1-01	1	E51
44	44		23505F1-00	F1-01	1	E52
45	45		23506F1-00	F1-01	1	E53
46	46		23507F1-00	F1-01	1	E54
47	47		23719F1-00	F1-01	1	E55
48	48		23844F1-00	F1-01	1	E56
49	49		23845F1-00	F1-01	1	E57
50	50		23511F1-00	F1-01	1	E58
51	51		23720F1-00	F1-01	1	E59
52	52		23846F1-00	F1-01	1	E60
53	53		23721F1-00	F1-01	1	E61
54	54		23847F1-00	F1-01	1	E62
55	55		23848F1-00	F1-01	1	E63
56	56		23849F1-00	F1-01	1	E64
57	57		23786F1-00	F1-01	1	E65
58	58		23850F1-00	F1-01	1	E66
59	59		23724F1-00	F1-01	1	E67
60	60		23851F1-00	F1-01	1	E68
61	61		23852F1-00	F1-01	1	E69
62	62		23778F1-00	F1-01	1	E70
63	63		23726F1-00	F1-01	1	E71
64	64		23853F1-00	F1-01	1	E72
65	65		23727F1-00	F1-01	1	E73
66	66		23854F1-00	F1-01	1	E74
67	67		23855F1-00	F1-01	1	E75
68	68		23779F1-00	F1-01	1	E76
69	69		23856F1-00	F1-01	1	E77
70	70		23857F1-00	F1-01	1	E78
71	71		23732F1-00	F1-01	1	E79
72	72		23858F1-00	F1-01	1	E80
73	73		23859F1-00	F1-01	1	E81
74	74		23535F1-00	F1-01	1	E82
75	75		23733F1-00	F1-01	1	E83
76	76		23860F1-00	F1-01	1	E84
77	77		23734F1-00	F1-01	1	E85
78	78		23861F1-00	F1-01	1	E86

D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							CCS		K	FL	L0005-0-DBP	F

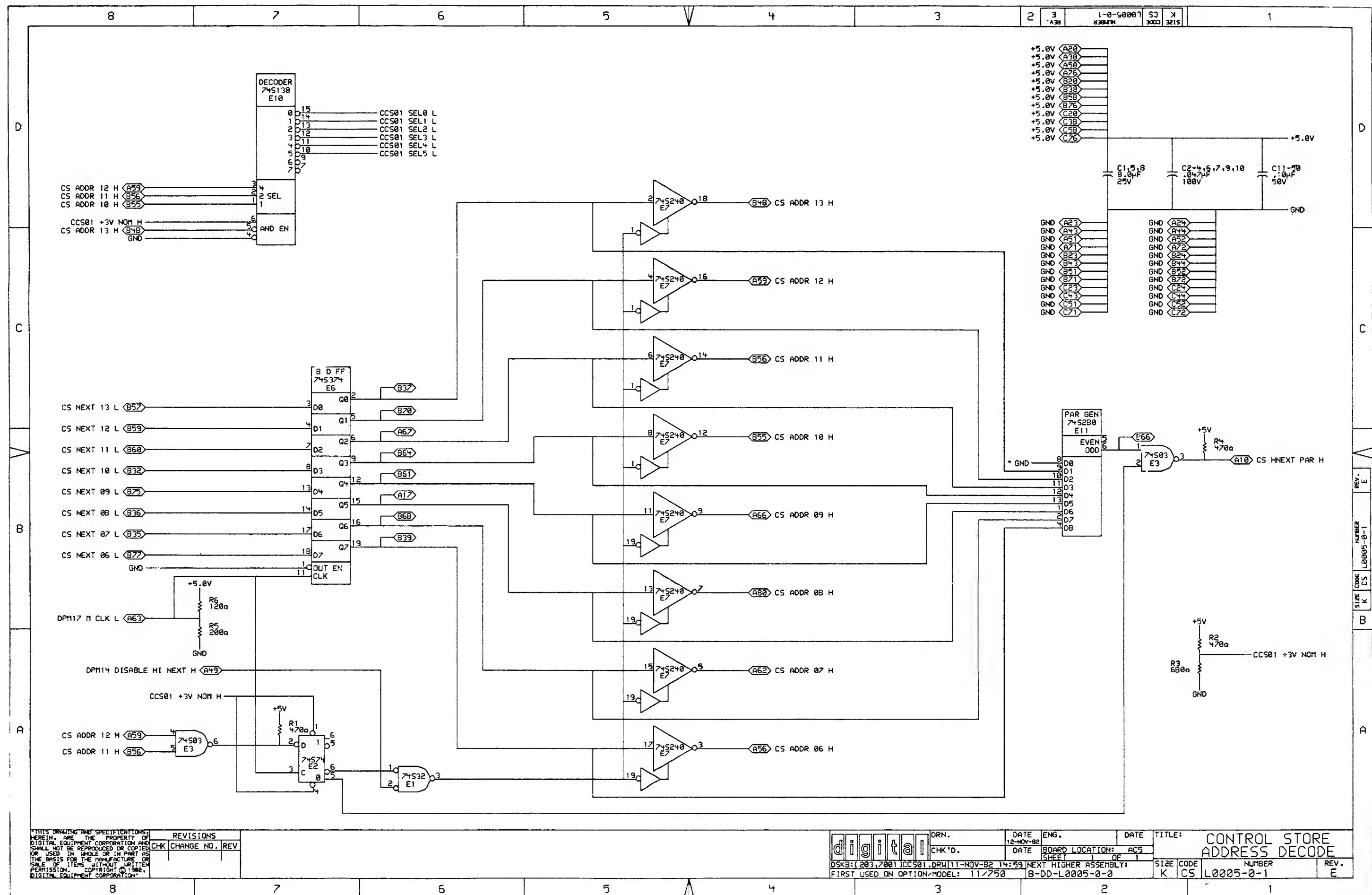
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80	80		23863F1-00	F1-01	1	E88
81	81		23864F1-00	F1-01	1	E89
82	82		23865F1-00	F1-01	1	E90
83	83		23866F1-00	F1-01	1	E91
84	84		23867F1-00	F1-01	1	E92
85	85		23868F1-00	F1-01	1	E93
86	86		23869F1-00	F1-01	1	E94
87	87		23740F1-00	F1-01	1	E95
88	88		23870F1-00	F1-01	1	E96
89	89		23741F1-00	F1-01	1	E97
90	90		23871F1-00	F1-01	1	E98
91	91		23872F1-00	F1-01	1	E99
92	92		23553F1-00	F1-01	1	E100
93	93		23743F1-00	F1-01	1	E101
94	94		23873F1-00	F1-01	1	E102
95	95		23744F1-00	F1-01	1	E103
96	96		23874F1-00	F1-01	1	E104
97	97		23875F1-00	F1-01	1	E105
98	98		23559F1-00	F1-01	1	E106
99	99		23745F1-00	F1-01	1	E107
100	100		23876F1-00	F1-01	1	E108
101	101		23746F1-00	F1-01	1	E109
102	102		23877F1-00	F1-01	1	E110
103	103		23878F1-00	F1-01	1	E111
104	104		23565F1-00	F1-01	1	E112
105	105		23747F1-00	F1-01	1	E113
106	106		23879F1-00	F1-01	1	E114
107	107		23748F1-00	F1-01	1	E115
108	108		23880F1-00	F1-01	1	E116
109	109		23881F1-00	F1-01	1	E117
110	110		23571F1-00	F1-01	1	E118
111	111		23882F1-00	F1-01	1	E119
112	112		23883F1-00	F1-01	1	E120
113	113		23750F1-00	F1-01	1	E121
114	114		23884F1-00	F1-01	1	E122
115	115		23885F1-00	F1-01	1	E123
116	116		23577F1-00	F1-01	1	E124
117	117		23752F1-00	F1-01	1	E125
118	118		23886F1-00	F1-01	1	E126
119	119		23753F1-00	F1-01	1	E127
120	120		23887F1-00	F1-01	1	E128
121	121		23888F1-00	F1-01	1	E129
122	122		23754F1-00	F1-01	1	E130
123	123		23755F1-00	F1-01	1	E131
124	124		23889F1-00	F1-01	1	E132
125	125		23757F1-00	F1-01	1	E133
126	126		23890F1-00	F1-01	1	E134

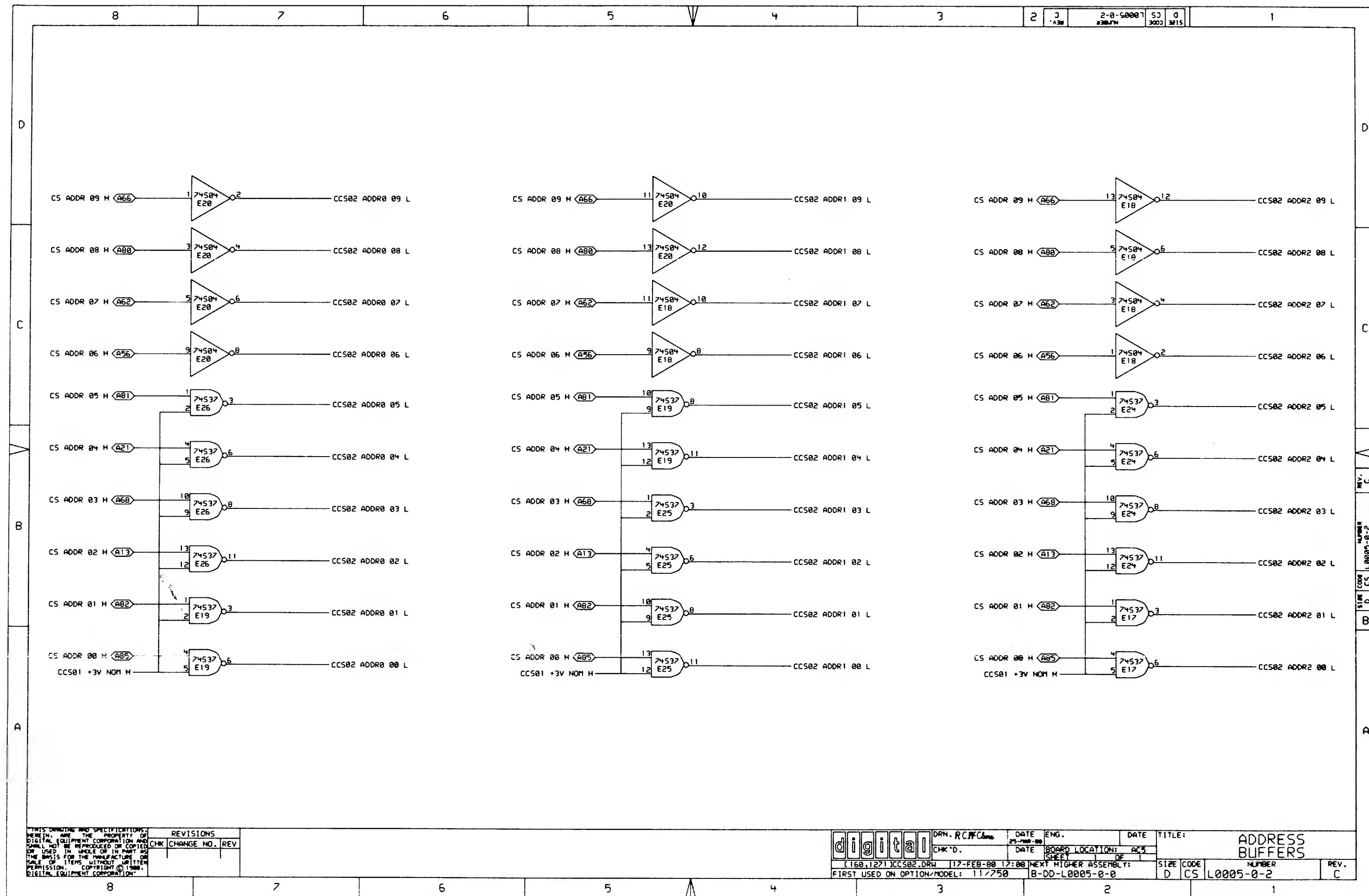
I D I G I T A L				TITLE		SECTION A OF A		SIZE	CODE	DOCUMENT NUMBER	REV
				CCS				K	PL	L0005-0-DBP	F

LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION		REFERENCE DESIGNATOR
					00		
127	127		23891F1-00	F1-01	1		E135
128	128		23892F1-00	F101	1		E136
129	129		23760F1-00	F1-01	1		E137
130	130		23893F1-00	F1-01	1		E138
131	131		23762F1-00	F1-01	1		E139
132	132		23894F1-00	F1-01	1		E140
133	133		23895F1-00	F1-01	1		E141
134	134		23896F1-00	F1-01	1		E142
135	135		23897F1-00	F1-01	1		E143
136	136		23898F1-00	F1-01	1		E144
137	137		23899F1-00	F1-01	1		E145
138	138		23900F1-00	F1-01	1		E146
139	139		9000024-01	EYELET,ROLL FLANGE .1210DX .192	12		
140	140		1912830-00	LS90 COUNTER,ASYNCH UP,DE	1		E4
141	141		1811660-01	OSCILLATOR, XTAL 10.000 MHZ	1		E5
142	142		1811660-29	OSCILLATOR, XTAL 18.750 MHZ	1		E8
143	143		1300247-00	120.0 .25 W 5.0 % CC	1		R5
144	144		1311522-00	200.0 .25 W 5.0 % CC	1		R6
145	145		1214314-00	CONN 2POS JUMPER	1		

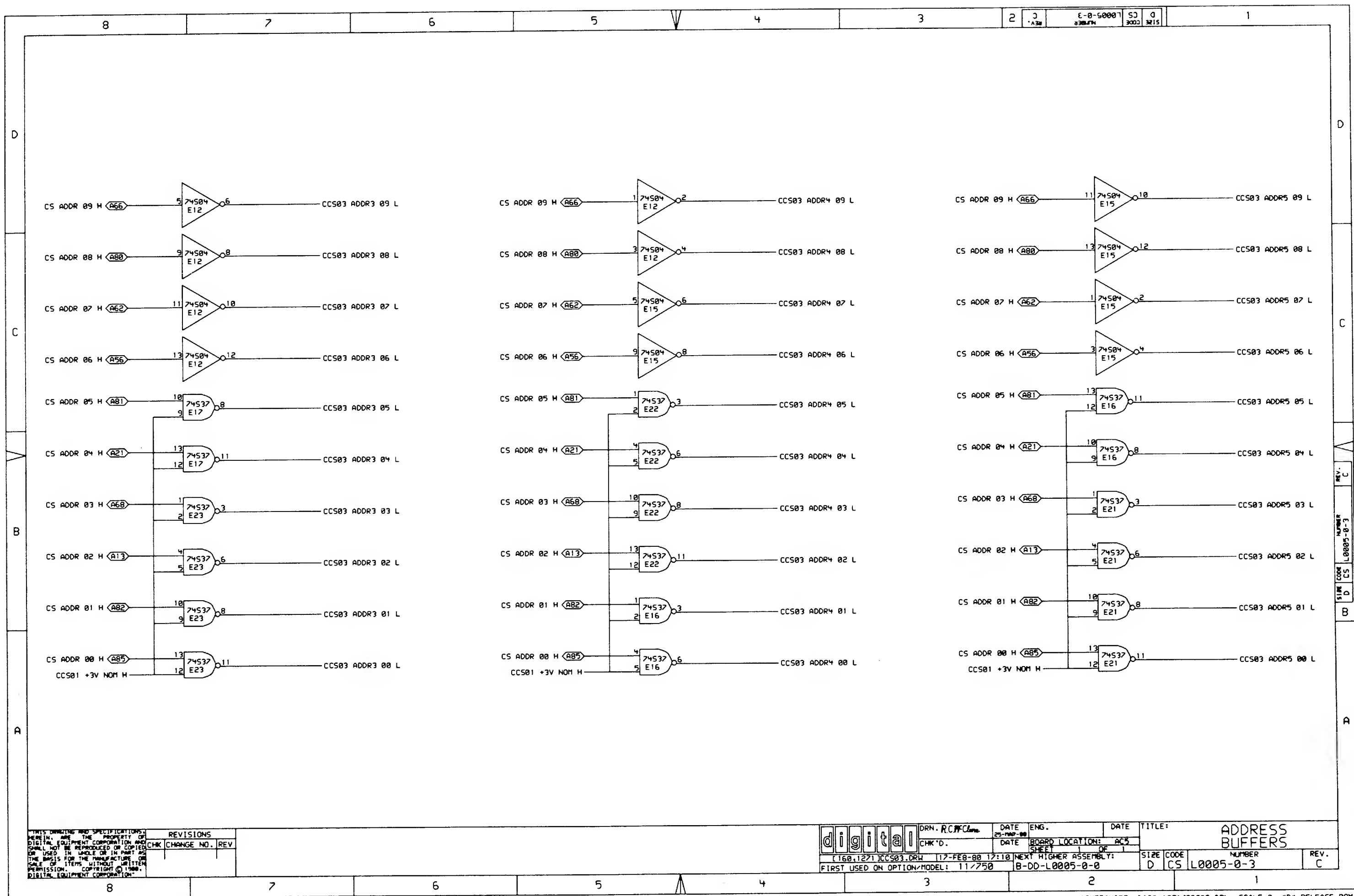
146 NOTE: ITEM #18 IS USED ON J1-J4.  
147 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

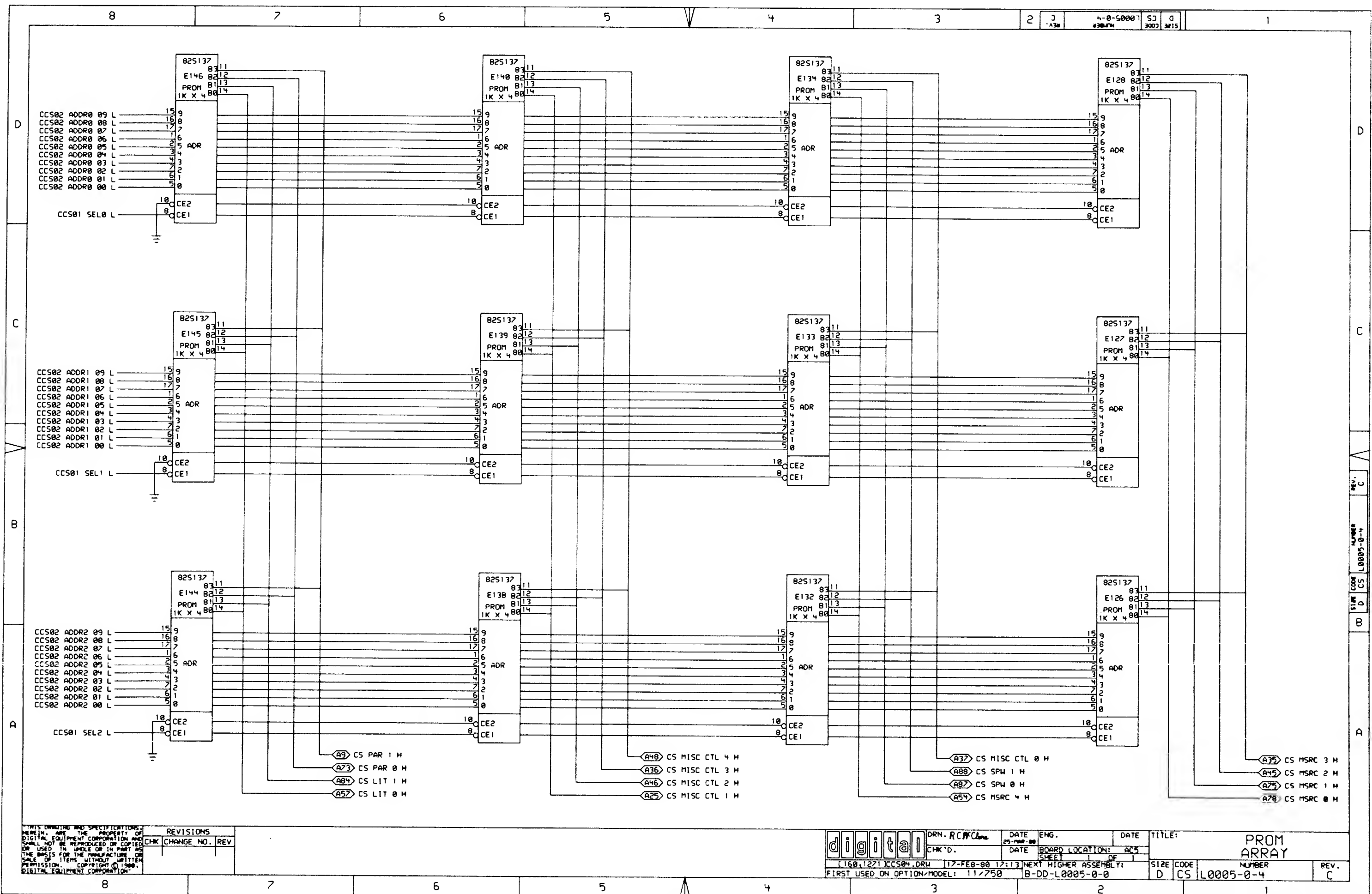
D	I	G	I	T	A	L	TITLE	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
							CCS		K	PL	L0005-0-DBP	F











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REVISIONS		
CHK	CHANGE NO.	REV



DRN. RCM/Chm  
CHK'D.

DATE 25-MAR-88  
DATE 17-FEB-88

ENG.  
BOARD LOCATION: ACS

DATE  
SHEET 1 OF 1

TITLE:  
PROM ARRAY

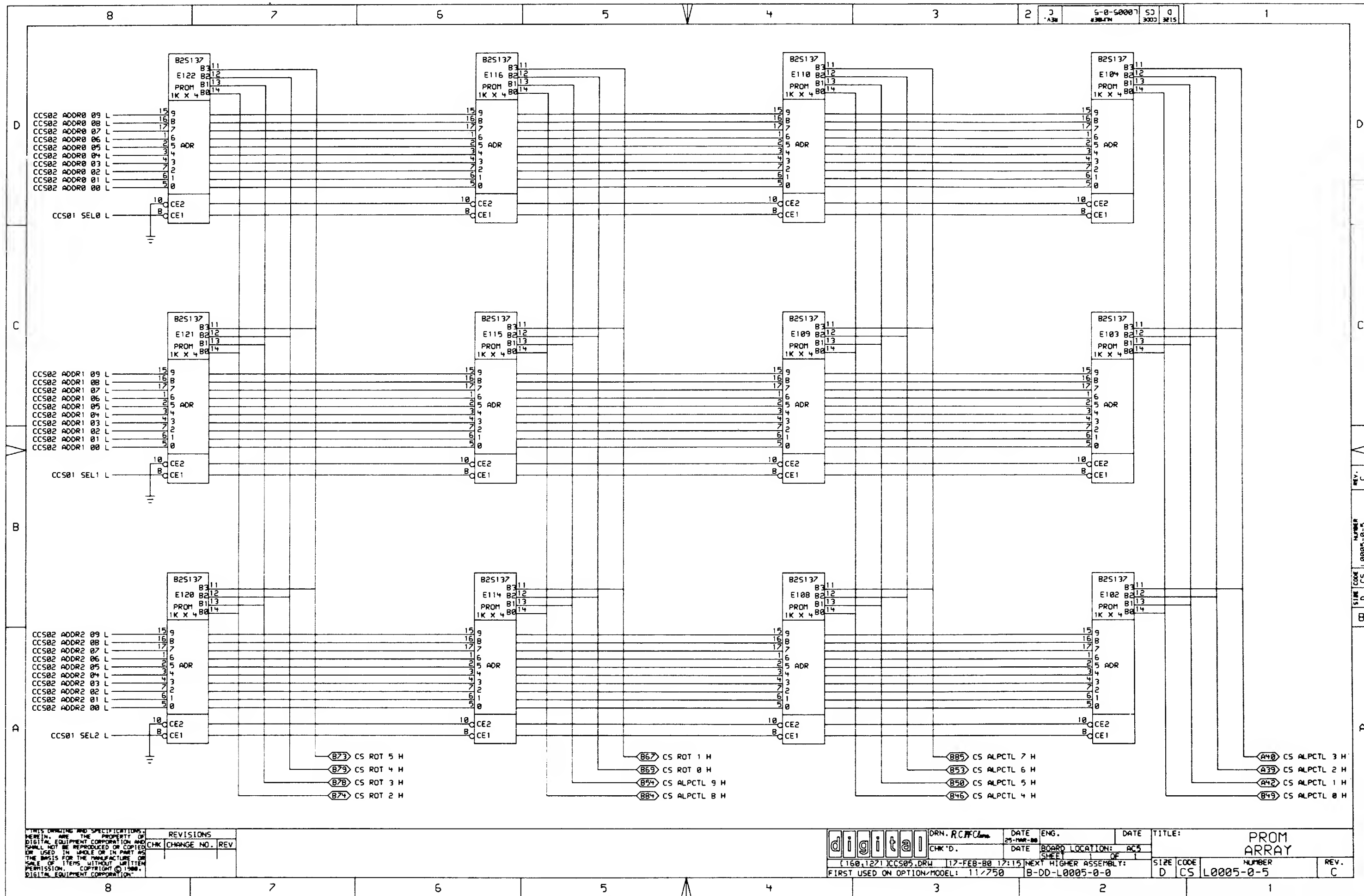
[160,127] CCS04.DRW 17-FEB-88 17:13 NEXT HIGHER ASSEMBLY:  
FIRST USED ON OPTION MODEL: 11/750

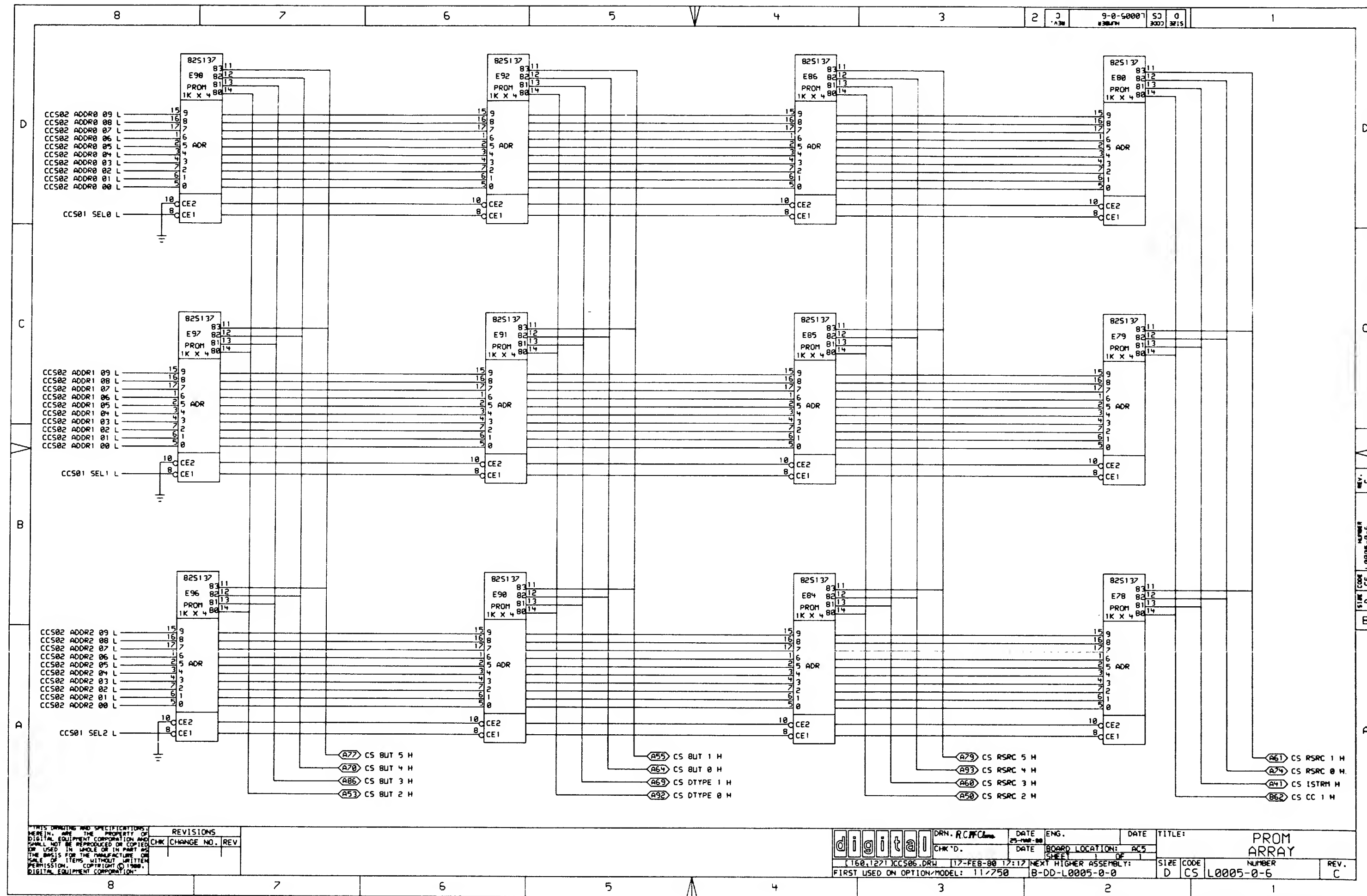
B-DD-L0005-0-0

SIZE CODE D CS

NUMBER L0005-0-4

REV. C

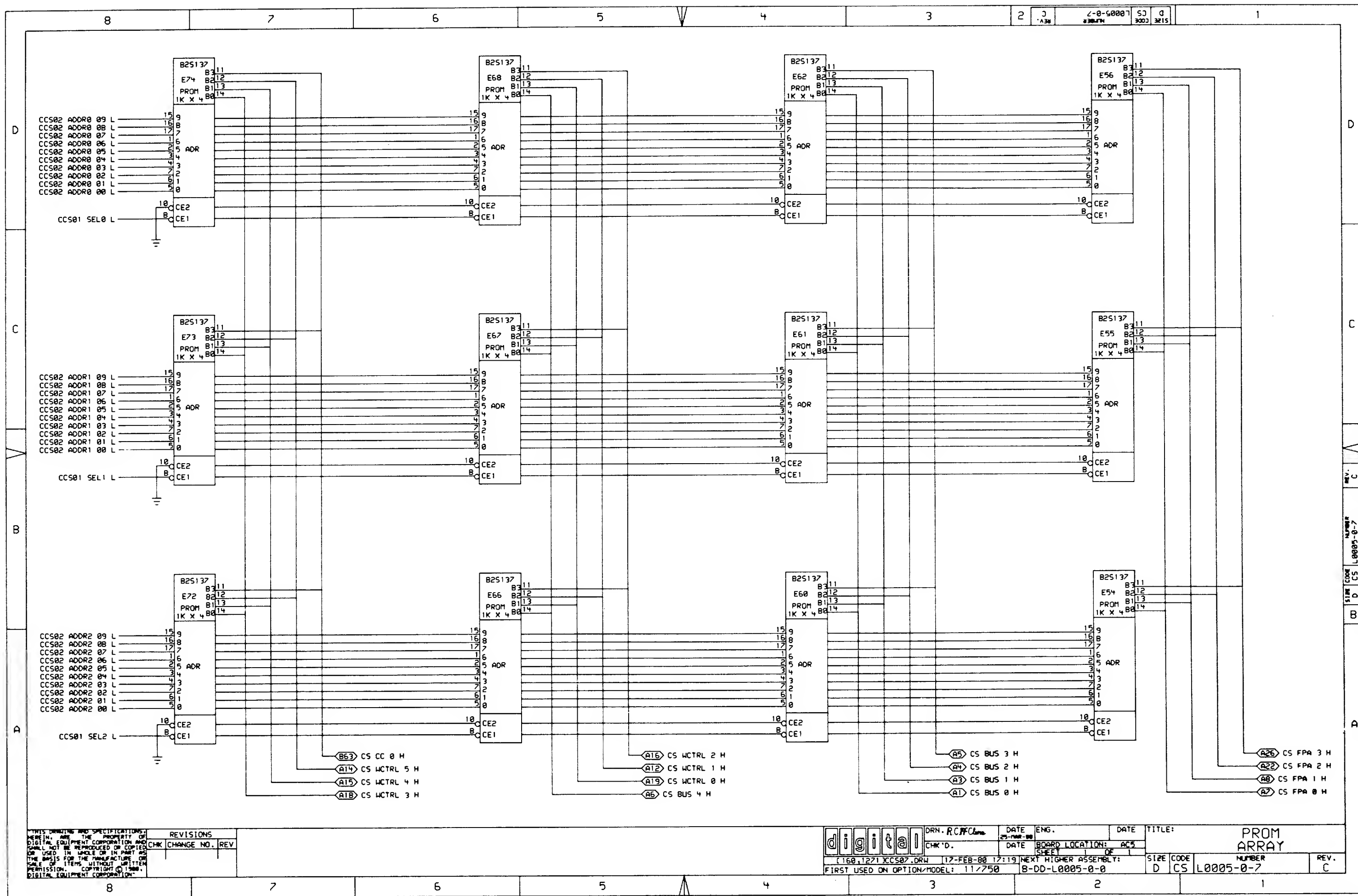


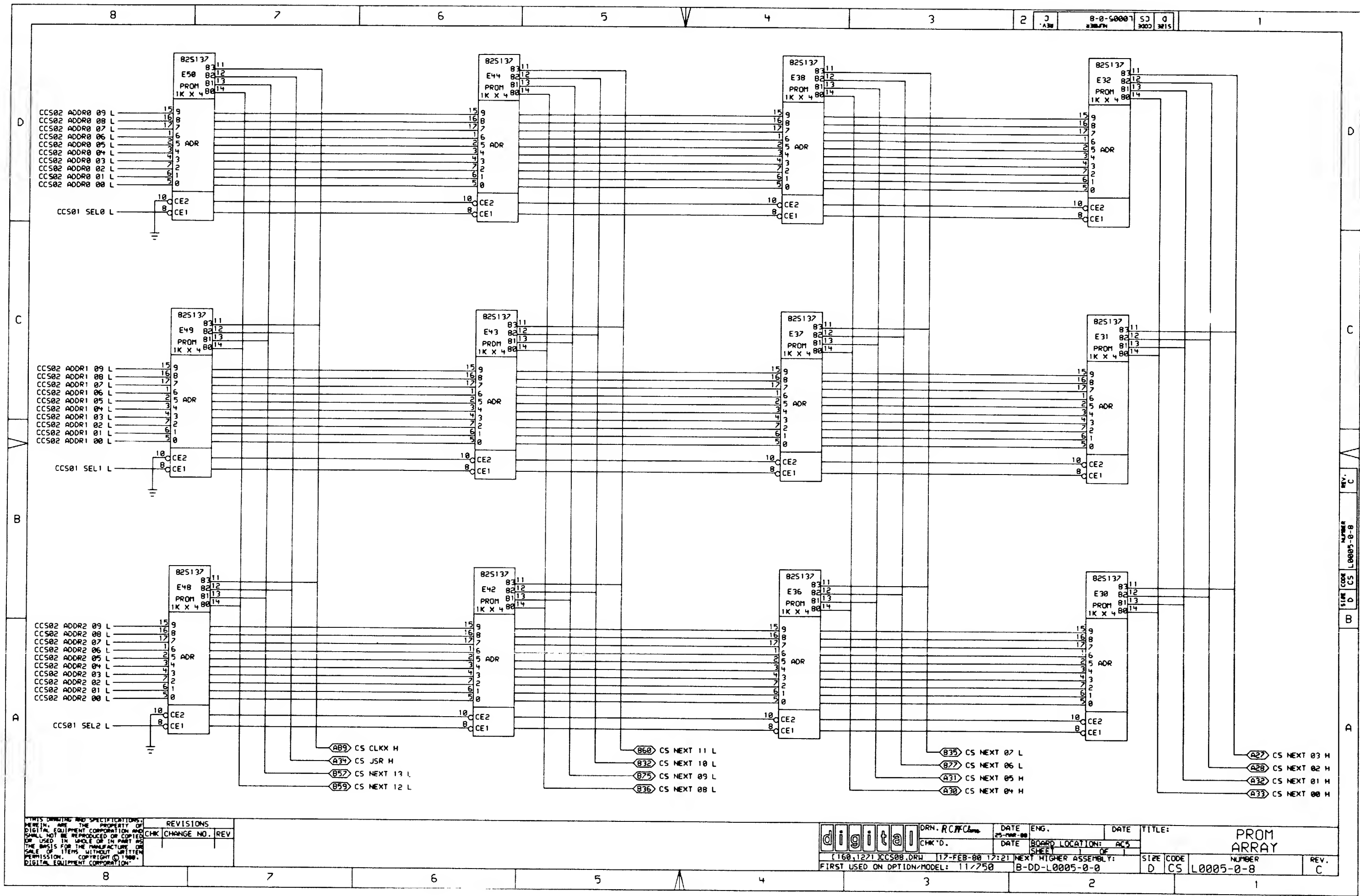


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REVISIONS		
CHK	CHANGE NO.	REV

<b>digital</b>	DRN. R.C.F.C.	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:	AC5	PROM ARRAY
[160,127] CCS06.DRW		17-FEB-80 17:17	NEXT HIGHER ASSEMBLY:	SIZE CODE	NUMBER
FIRST USED ON OPTION/MODEL: 117750		B-DD-L0005-0-0	D	CS	L0005-0-6
REV. C					

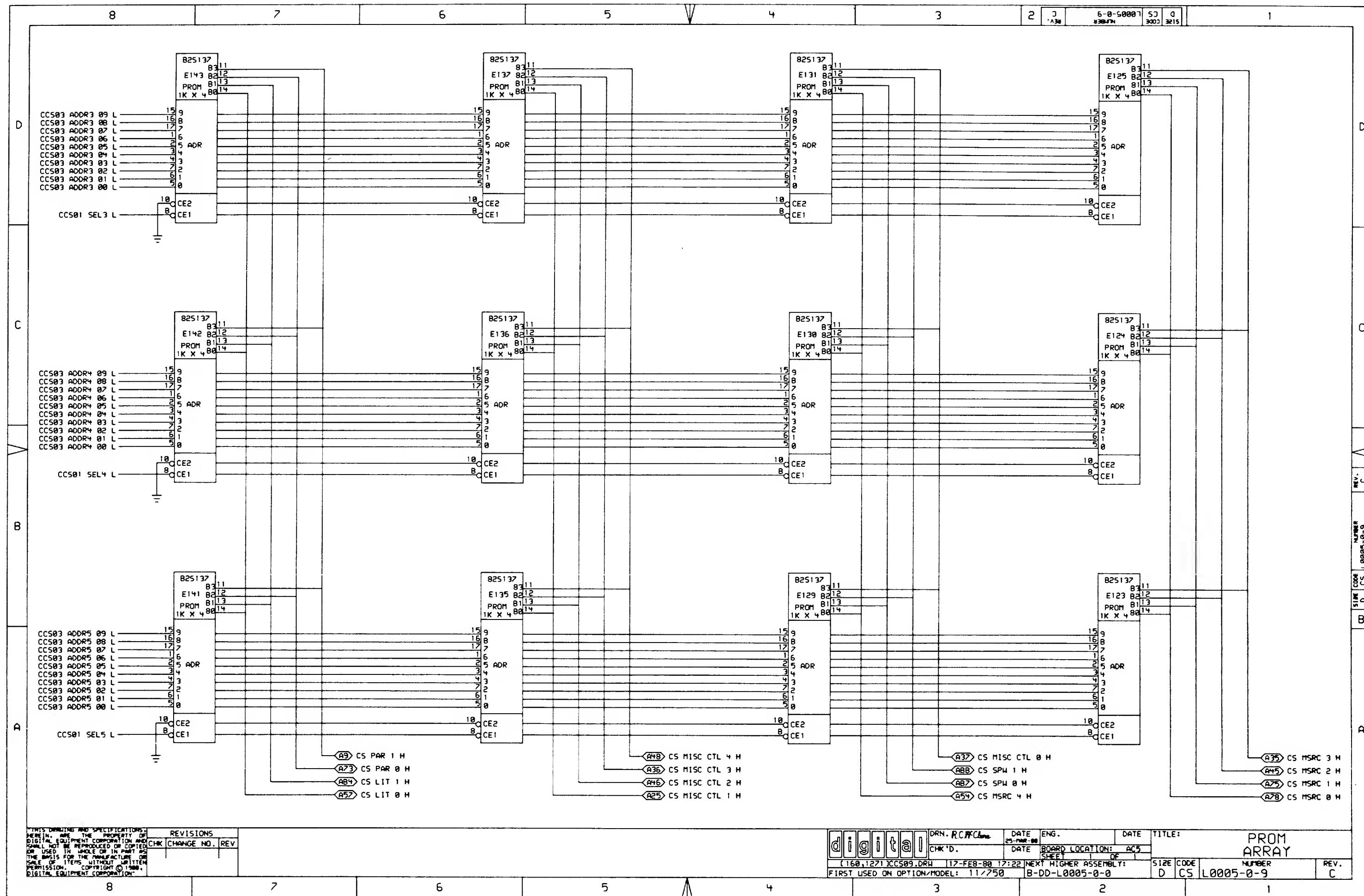




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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. RCM/Chms	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:	AC'S	
FIRST USED ON OPTION/MODEL: 11/7250		B-DD-L0005-0-0		SIZE CODE	NUMBER
				D CS	L0005-0-8
				REV.	C

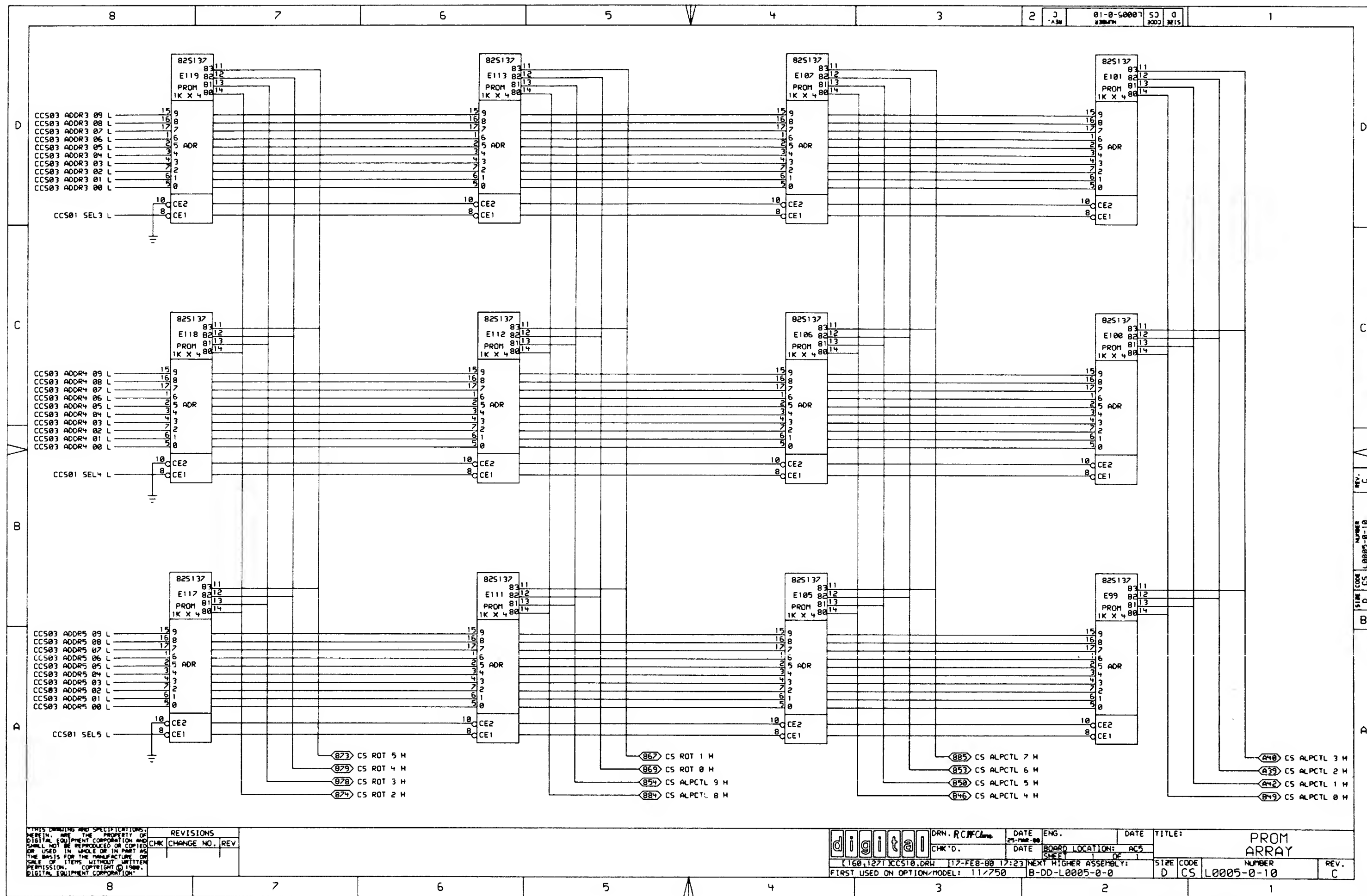


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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R.C.F. Chm	DATE	ENG.	DATE	TITLE:
	CHK'D.	DATE	BOARD LOCATION:	OF	
C160,1271 JCC509.DRW		17-FEB-80 17:22	NEXT HIGHER ASSEMBLY:	SIZE	CODE
FIRST USED ON OPTION/MODEL: 11/750		B-DD-L0005-0-0		D	CS
				NUMBER	REV.
				L0005-0-9	C





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REVISIONS	
CHK	CHANGE NO. REV

digital

DRN. R.C.M.C.

DATE 25-MAR-80

ENG.

DATE 25-MAR-80

BOARD LOCATION: AC5

SHEET 1 OF 1

TITLE:

PROM

ARRAY

SIZE CODE

NUMBER

REV.

160,1271 JCS10.DPL

117-FEB-80 12:23

NEXT HIGHER ASSEMBLY:

SIZE CODE

D

CS

L0005-0-10

NUMBER

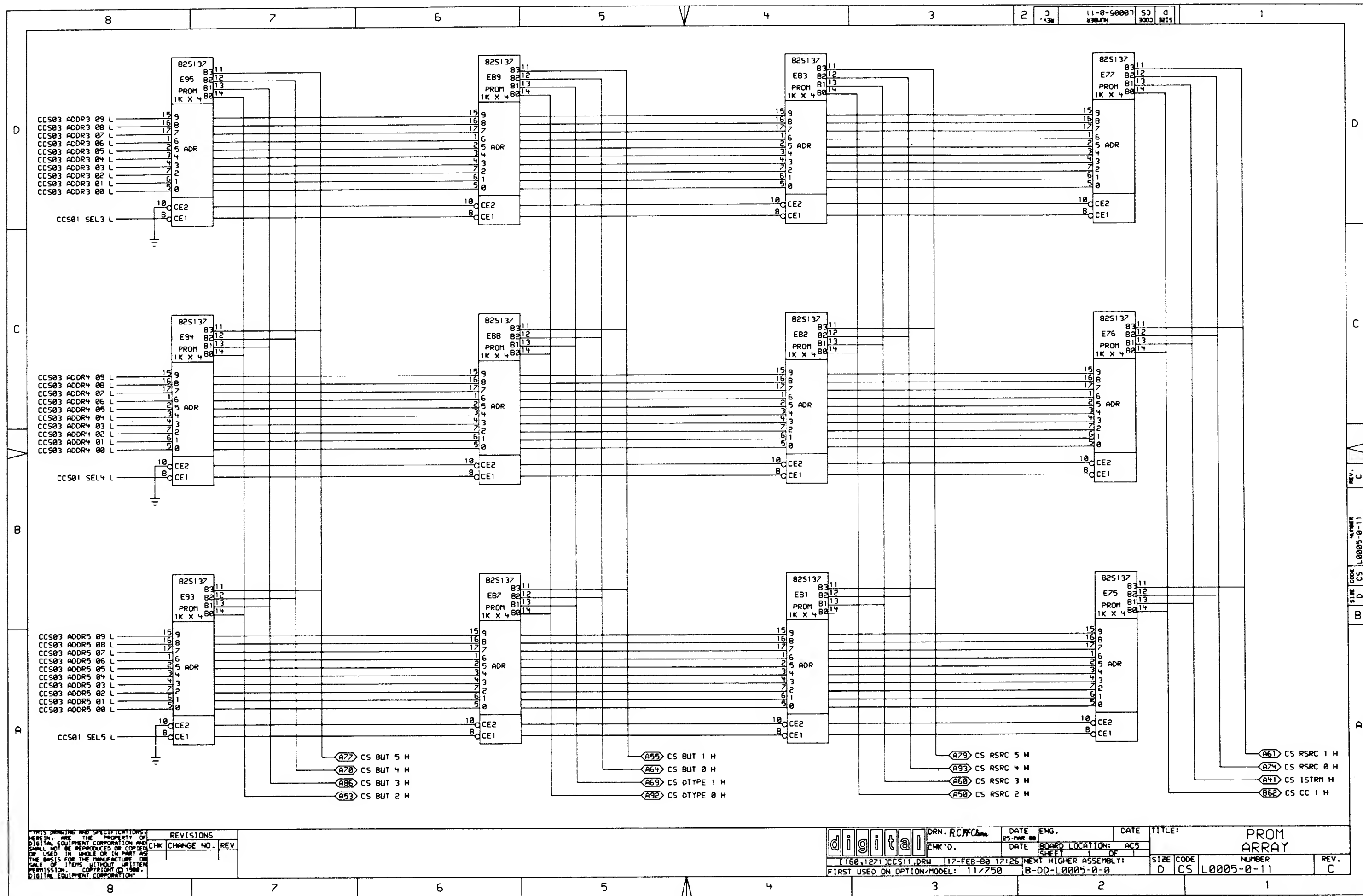
REV.

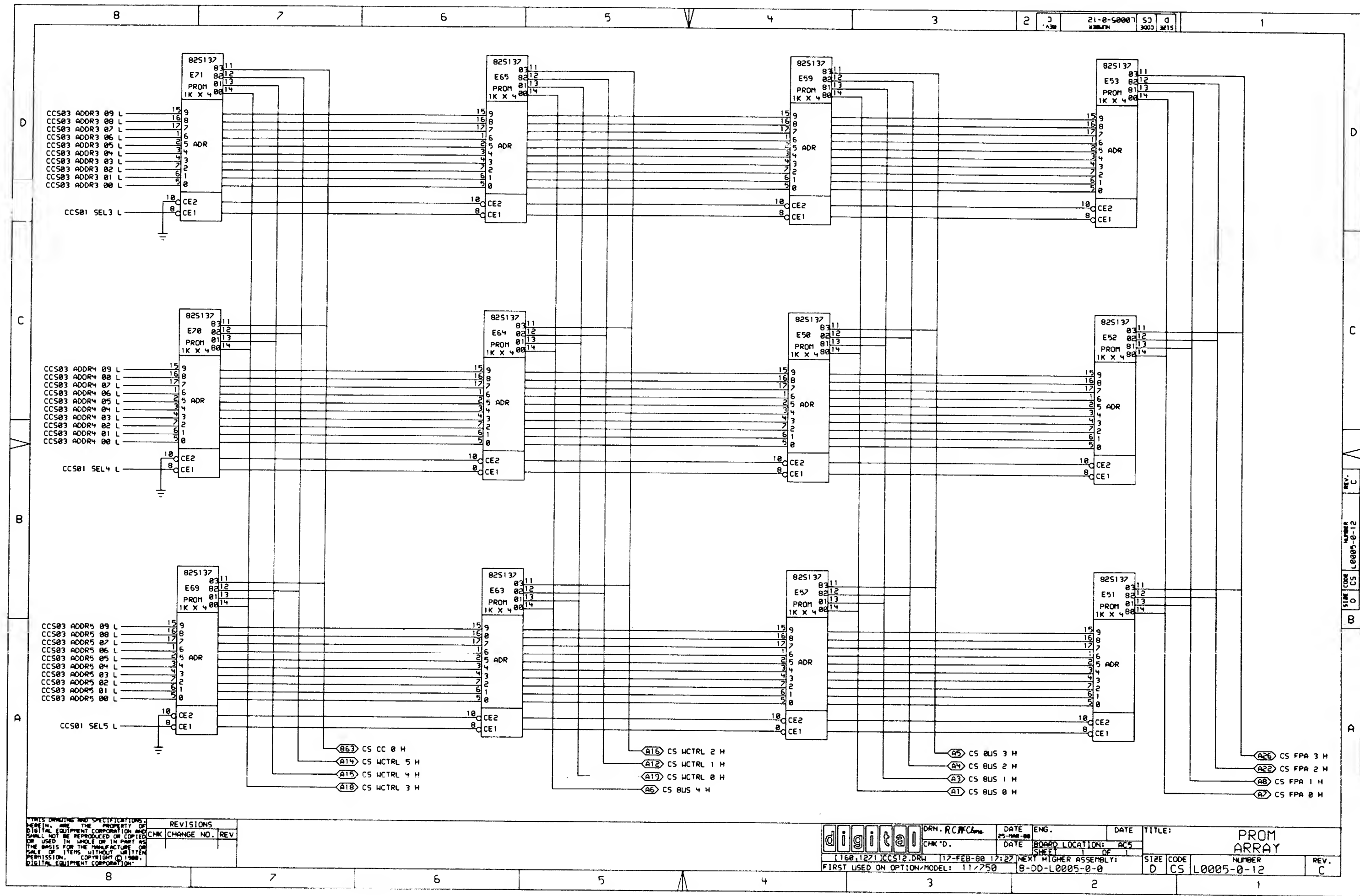
C

FIRST USED ON OPTION/MODEL: 117/250

B-DD-L0005-0-10

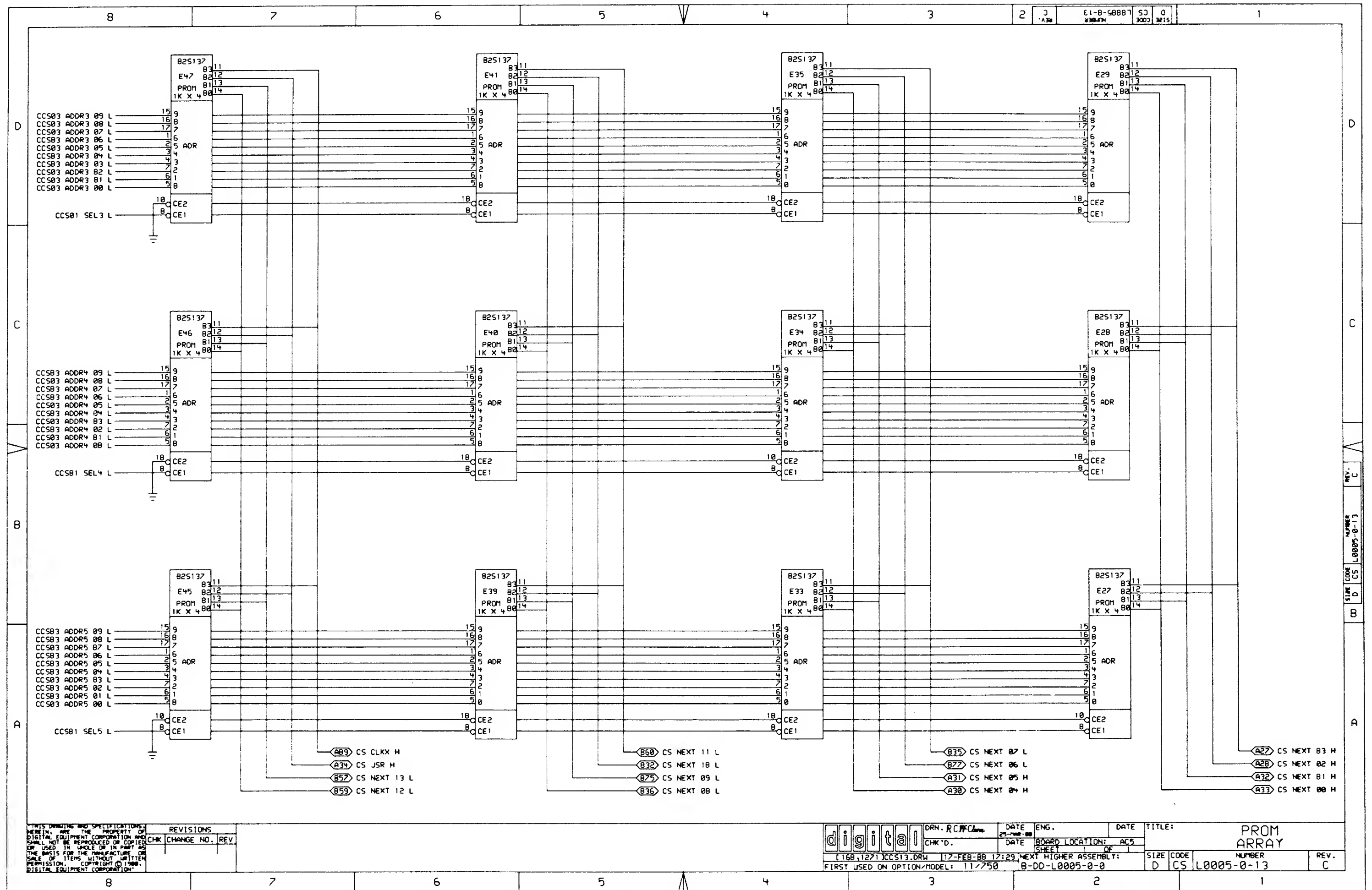


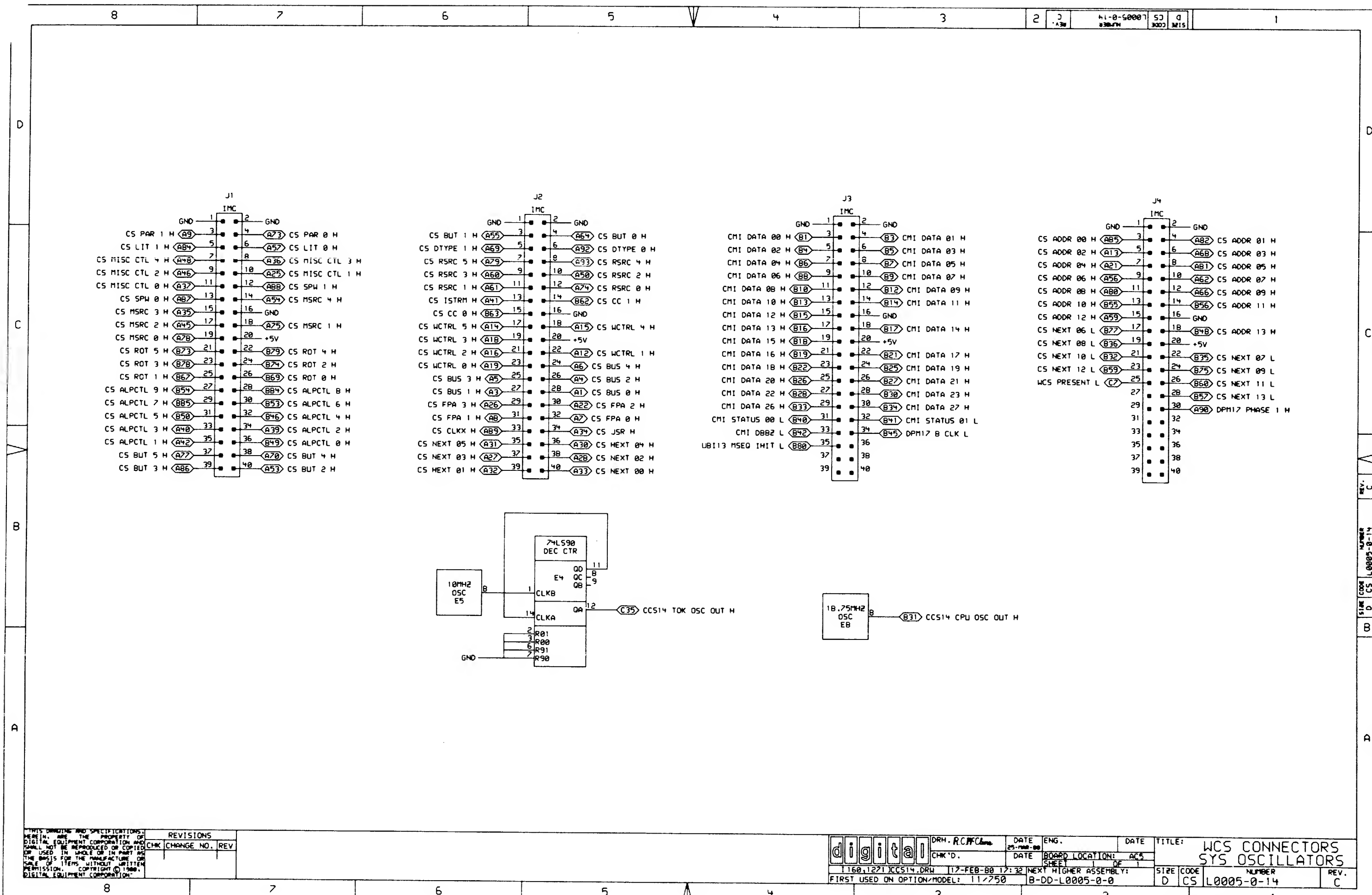




REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R.C. McNamee	DATE 25-FEB-80	ENG.	DATE	TITLE: PROM ARRAY
	CHK'D.	DATE	BOARD LOCATION: AC5	SHEET 1 OF 1	NUMBER
1160.1271 JCS12.DRW		17-FEB-80 17:27	NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0		SIZE CODE D CS L0005-0-12
FIRST USED ON OPTION/MODEL: 11/750				REV. C	





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REVISIONS	
CHK	CHANGE NO. REV

digital	DRW. R.C.McCormack	DATE 25-MAR-80	ENG.	DATE	TITLE: WCS CONNECTORS SYS OSCILLATORS
	CHK'D.	DATE	BOARD LOCATION: ACS	SHEET 1 OF 1	
1160,1271 CCS14.DRW		12-FEB-80 12:32	NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0	SIZE CODE D CS	NUMBER L0005-0-14
FIRST USED ON OPTION/MODEL: 11/750				REV. C	

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NOTES:  
1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.

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REVISIONS		
CHK	CHANGE NO.	REV

digital  
[160,1271] CCS15.DRW  
FIRST USED ON OPTION/MODEL: 11/750

DRN. 027000  
CHK'D.  
DATE 12-FEB-88 12:35  
NEXT HIGHER ASSEMBLY: B-DD-L0005-0-0

ENG.  
DATE  
BOARD LOCATION: AC5  
SHEET 1 OF 1

TITLE: CCS  
FORWARD REFERENCE

SIZE	CODE	NUMBER	REV.
D	CS	L0005-0-15	C

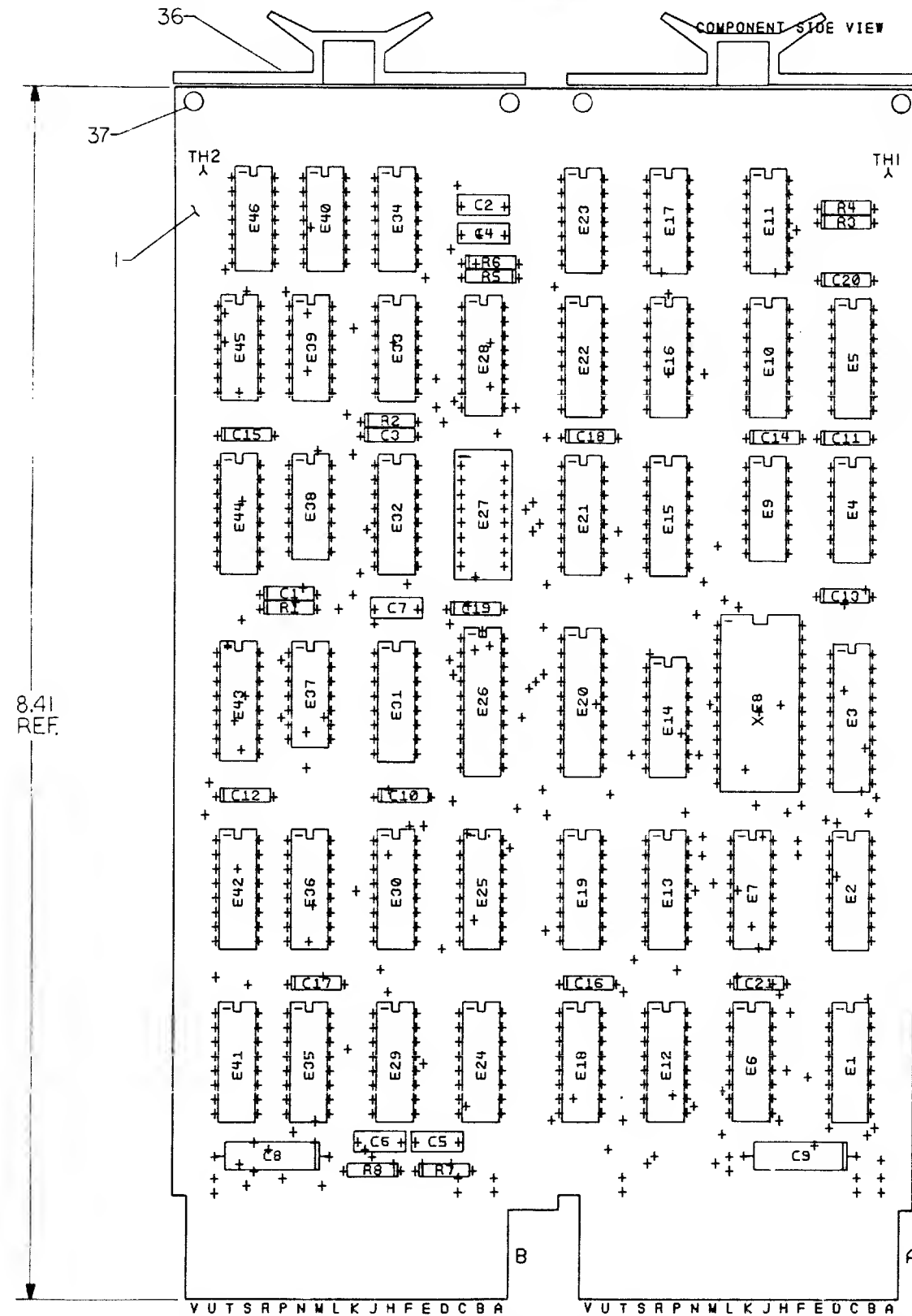
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D						D	
C						C	
B						B	
A						A	
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)	
CS MISC CTL 3 H		09,04,14					
CS MISC CTL 4 H		09,04,14					
CS MSRC 0 H		09,04,14					
CS MSRC 1 H		09,04,14					
CS MSRC 2 H		09,04,14					
CS MSRC 3 H		09,04,14					
CS MSRC 4 H		09,04,14					
CS NEXT 00 H		13,08,14					
CS NEXT 01 H		13,08,14					
CS NEXT 02 H		13,08,14					
CS NEXT 03 H		13,08,14					
CS NEXT 04 H		13,08,14					
CS NEXT 05 H		13,08,14					
CS NEXT 06 L		01,13,08,14					
CS NEXT 07 L		01,13,08,14					
CS NEXT 08 L		01,13,08,14					
CS NEXT 09 L		01,13,08,14					
CS NEXT 10 L		01,13,08,14					
CS NEXT 11 L		01,13,08,14					
CS NEXT 12 L		01,13,08,14					
CS NEXT 13 L		01,13,08,14					
CS PAR 0 H		09,04,14					
CS PAR 1 H		09,04,14					
CS ROT 0 H		10,05,14					
CS ROT 1 H		10,05,14					
CS ROT 2 H		10,05,14					
CS ROT 3 H		10,05,14					
CS ROT 4 H		10,05,14					
CS ROT 5 H		10,05,14					
CS RSRC 0 H		11,06,14					
CS RSRC 1 H		11,06,14					
CS RSRC 2 H		11,06,14					
CS RSRC 3 H		11,06,14					
CS RSRC 4 H		11,06,14					
CS RSRC 5 H		11,06,14					
CS SPW 0 H		09,04,14					
CS SPW 1 H		09,04,14					
CS WCTRL 0 H		12,07,14					
CS WCTRL 1 H		12,07,14					
CS WCTRL 2 H		12,07,14					
CS WCTRL 3 H		12,07,14					
CS WCTRL 4 H		12,07,14					
CS WCTRL 5 H		12,07,14					
DPM14 DISABLE HI NEXT H		01					
DPM17 B CLK L		14					
DPM17 M CLK L		01					
DPM17 PHASE 1 H		14					
UBI13 MSEQ IMIT L		14					
WCS PRESENT L		14					
NOTES: 1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.							
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REVISIONS CHK CHANGE NO. REV							
digital DRN. 057000 DATE 25-MAR-88 ENG. DATE TITLE: CCS FORWARD REFERENCE CHK'D. DATE BOARD LOCATION: ACS [160,1271]CCS16.DRW 17-FEB-88 17:36 NEXT HIGHER ASSEMBLY: SIZE CODE NUMBER REV. FIRST USED ON OPTION/MODEL: 11/750 B-DD-L0005-0-0 D CS L0005-0-16 C							
8	7	6	5	4	3	2	1



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8 7 6 5 4 3 2 1  
DUA M9313-0-0 A

COMPONENT SIDE VIEW



8.41  
REF.

NOTES:

STEP	Y AXIS	STEP	TIMES
REPEAT	X AXIS	STEP	TIMES

CHG CHANGE NO REV

ETCH REV.	B
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SIGNATURES		DATE	digital	TITLE	UET
DRN	7-27-79	7-27-79			
CHK'D	E.T. GERRY	7-31-79			
MECH. ENG.	7-21-80	7-21-80			
PROJ. ENG.	7-21-80	7-21-80			
PROD.	7-20-80	7-20-80			
SCALE	2:1				
SHT.	1	DF 3			
NEXT HIGHER ASSY. B-DD-M9313-0					

1 MS#1701248

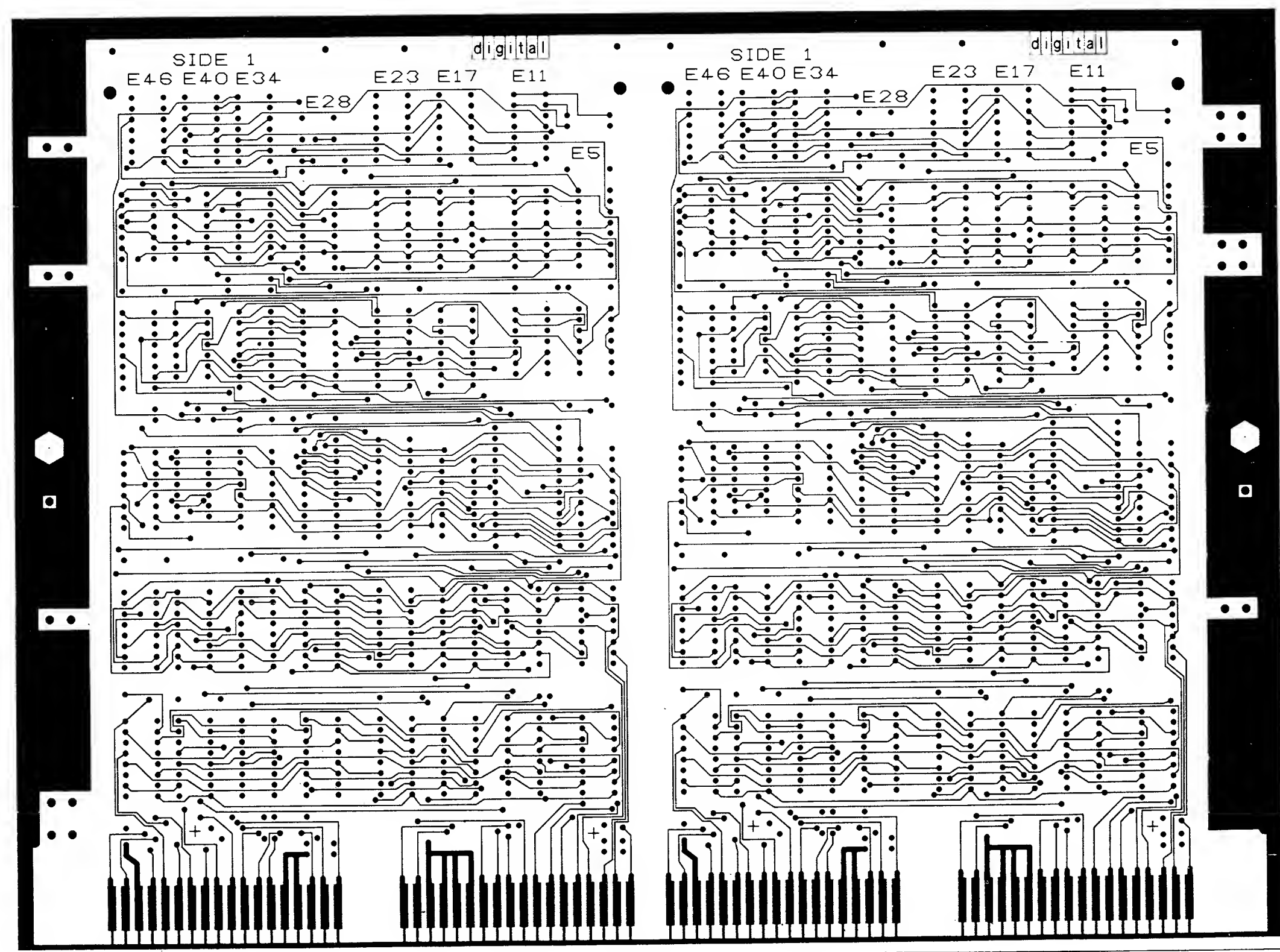


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501384 7B M9313

501384 7B M9313

LAYER 1



REVISIONS		
CHK	CHANGE NO	REV

TITLE		SIZE	CODE	NUMBER		REV.
UET		D	UA	M9313 - 0 - 0		A
SCALE	2 - 1	SHEET 2 OF 3		DIST		

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AYER

5013847B M9313

SIDE 2

UET

5013847B M9313

SIDE 2

UET

REVISIONS		
CHK	CHANGE NO	REV

TITLE		SIZE CODE	NUMBER	REV
UET		D	UA M9313 - 0 - 0	A
SCALE	2 - 1	SHEET	3 OF 3	DIST.

AUTOMATED BY PRTLST.3L(32)

## P A R T S   L I S T

SHEET A1 OF A2

LINE ITEM    DOCUMENT NUMBER

PART NUMBER

DESCRIPTION

QTY PER VARIATION  
00

REFERENCE DESIGNATOR

1	1	5013847-00	VET	1	
1001	1001	1215006-06	SKT, IC	1	XE8
1002	1002	1300005-04	R NETWORK 15-470	1	E27
1003	1003	1300202-00	47.0 .25 W 5.0 % CC	1	R1, R2
1004	1004	1300229-00	100.0 .25 W 5.0 % CC	1	R3, R4
1005	1005	1312628-00	R NETWORK 14-176.5 14-375 16PIN	2	E2, E42
1006	1006	1312628-01	R NETWORK 14-176.5 14-375 16PIN	2	E12, E29
1007	1007	1909704-00	DEC 314A NOR GATE-SINGLE 7IN	1	E37
1008	1008	1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1	E9
1009	1009	1910436-00	DEC 74123 ONE SHOT-DUAL, RETRIG	1	E28
1010	1010	1911469-00	DEC 8640 RECEIVER, BUS, QUAD, U	1	E4
1011	1011	1911579-00	8641 TRANSCEIVER, BUS, QUA	10	E1, E7, E18, E19, E30, E31, E35, E36, E41, E44
1012	1012				CONT
1013	1013	1911983-00	74S133 NAND GATE-POSITIVE 1	1	E43
1014	1014	1912799-03	LS00 NAND-GATE-QUAD 2IN, P	1	E40
1015	1015	1912803-00	74LS04 INVERTER GATE, HEX	2	E17, E39
1016	1016	1910535-00	74S05 INVERTER GATE-HEX 1	1	E33
1017	1017	1912805-00	LS08 AND GATE-QUAD 2IN, P0	1	E46
1018	1018	1912808-00	LS11 AND GATE-TRIPLE 3IN	1	E34
1019	1019	1912810-00	LS20 NAND GATE-DUAL 4IN	1	E23
1020	1020	1912824-00	LS74 FF-D DUAL, EDGE TRIGG	2	E38, E45
1021	1021	1912853-00	LS175 FF-D QUAD	1	E21
1022	1022	1914214-00	LS374 FF-D OCTAL EDGE TRIG	1	E3, E26
1023	1023	1914438-00	DC 013 UNIBUS INTERRUPT-BIP	5	E5, E10, E15, E16, E22
1024	1024	1914845-00	2918 FF-D QUAD TRI-STATE	5	E6, E13, E14, E24, E25
1025	1025	2327281-00	A1-03, A1-04, A1-05	1	E32
1026	1026	1000055-00	2200.0 MMF 250V 20% Y5S DISC	2	C2, C4
1027	1027	1000043-00	1000.0 MMF 250V 20% Y5F DISC	2	C5, C6
1028	1028	1001610-01	.01 MFD50/100V +80-20% DISC	1	C7
1029	1029	1611243-00	DELAY=25-250NS, 10TAPS RCL#L-183	1	E11

REVISION HISTORY		BASIC PART NO: M9313		DRN: M.FUNARO		DATE: 22-MAY-79		D I G I T A L	
ENG:	ECO NUMBER	REV	SECTION A OF A	CHK'D: F.GAROFALO		DATE: 22-MAY-79		TITLE PARTS LIST	
---	INITIAL	A	SECTION VARIATION INDEX	DES.ENG: B.ARMSTRONG		DATE: 22-MAY-79			
SB	M9312-TW001	B	[A] 00	RESP.ENG.: B.ARMSTRONG		DATE: 22-MAY-79		DOCUMENT NUMBER	
			[B]	MFG.ENG.: K.O'BRIEN		DATE: 13-FEB-80		SIZE: CODE: NUMBER REV	
			[C]	ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:		FILE NAME: EDIT #	
			[D]	D-UA-M9313-0-0		B-DD-M9313-0-0		Z1259B.PLS 15	
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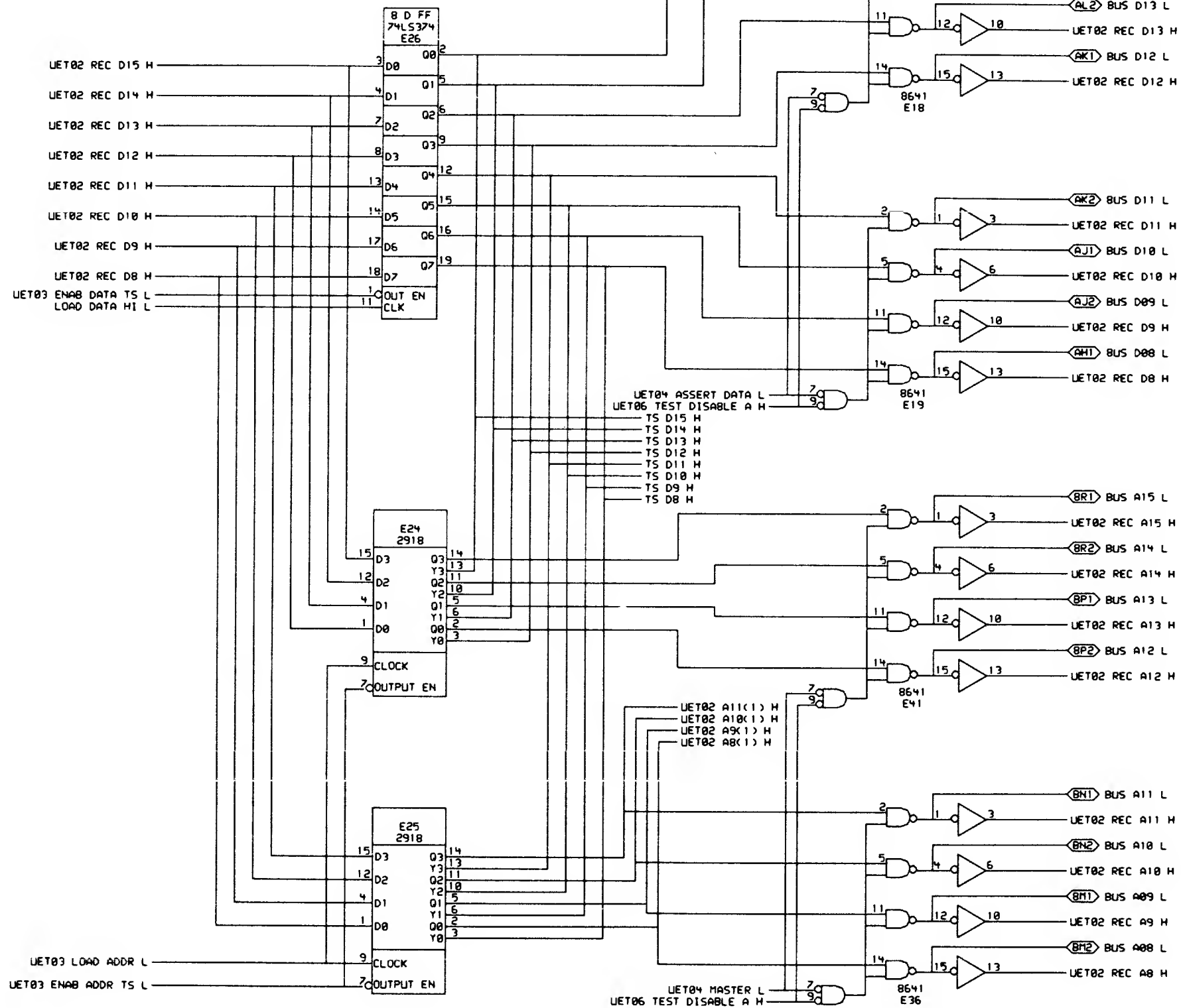
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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
30	30		1913777-00	LS240 DRIVER LINE, OCTAL, T	1	E20
31	31		1302394-00	30.0 K .25 W 5.0 % CC	1	R5, R6
32	32		1305125-00	383.0 .25 W 1.0 % RN550-F10	1	R7, R8
33	33		1012784-00	.047 MFD 50V +80-20% CER	1	C10-C21
34	34	SEE NOTES	1012084-01	8 MFD 25V +75-10% AL EL	1	C8, C9
35	35		1000024-00	470.0 MMF 100V 5%200PPM MICA	1	C1, C3
36	36		9008337-06	HANDLE, FLIP CHIP, MAGENTA	1	
37	37		9000024-01	EYELET, ROLL FLANGE .12100X .192	4	

38 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 10-12084-01

D	I	G	I	T	A	L	TITLE	UET	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M9313-0-DBP	B





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REVISIONS		
CHK	CHANGE NO.	REV

digital	DRN. R.C.FCL	DATE 25 MAR 80	ENG.	DATE	TITLE: UET02			
	CHK'D.	DATE	BOARD LOCATION: AB19		DATA PATH <15:8>			
[160,127] UET02.DRW		17-FEB-80 13:17	NEXT HIGHER ASSEMBLY:		SIZE	CODE	NUMBER	REV.
FIRST USED ON OPTION/MODEL: 11/750		B-DD-M9313-0-0		D		CS	M9313-0-2	A

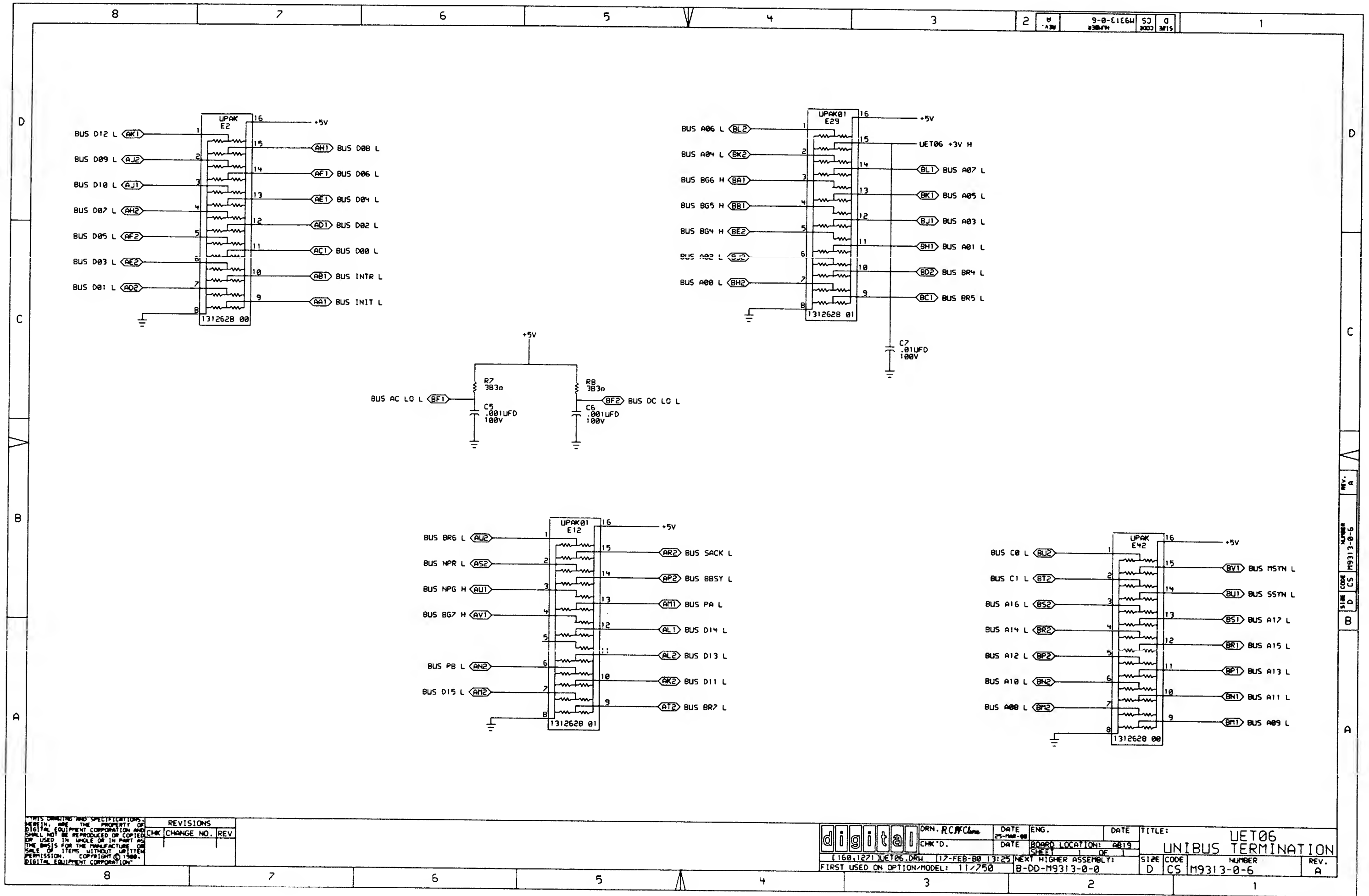












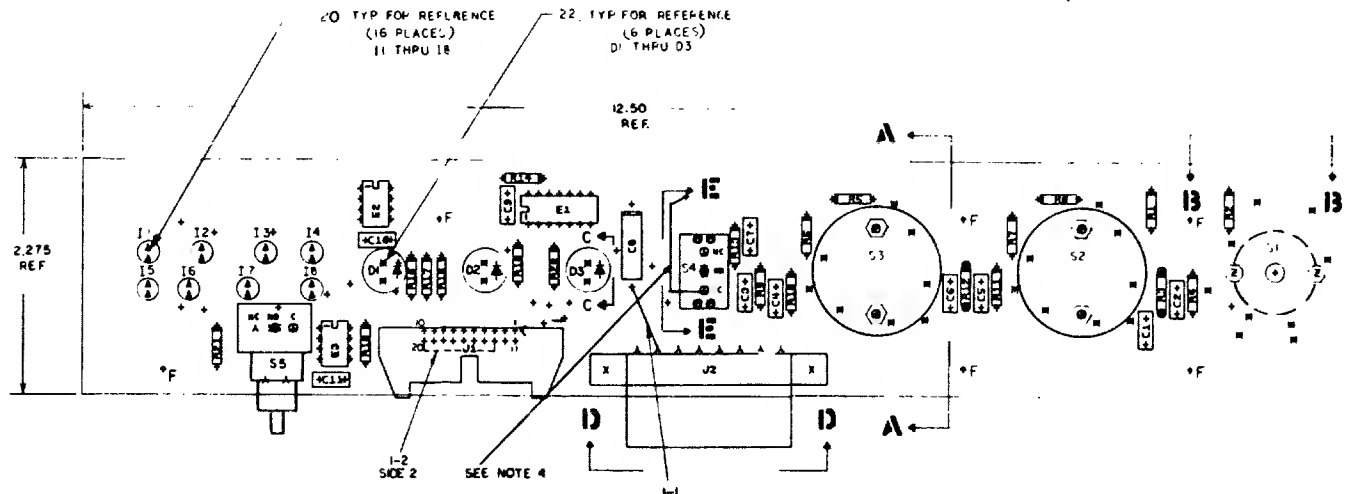
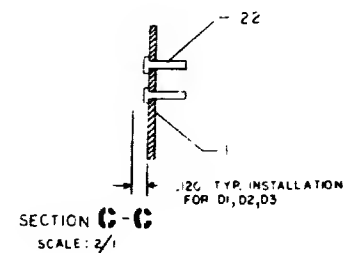
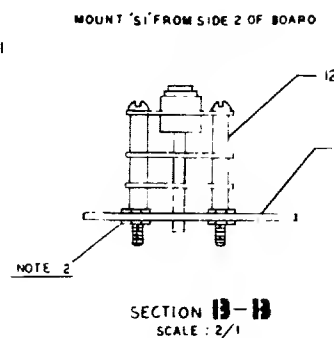
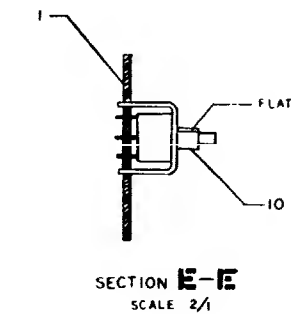
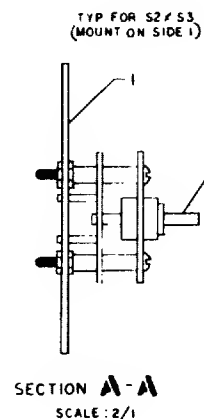
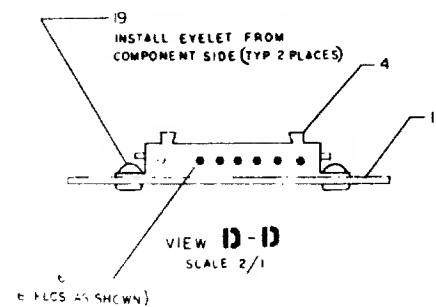
8		7		6		5		4		3		2		1	
D		SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)		D	
		BUS A00 L		06,01		BUS NPG H		06,04		UET02 A0 (1X)		03,02			
		BUS A01 L		06,01		BUS NPR L		06,04		UET02 A9 (1X)		03,02			
		BUS A02 L		06,01		BUS PA L		04,06		UET02 REC A10 H		02,03			
		BUS A03 L		06,01		BUS PB L		04,03,06		UET02 REC A11 H		02,03			
		BUS A04 L		06,01		BUS SACK L		05,06,04		UET02 REC A12 H		02,03			
		BUS A05 L		06,01		BUS SSYN L		06,04		UET02 REC A13 H		02,03			
		BUS A06 L		06,01		LOAD DATA HI L		02,03,04		UET02 REC A14 H		02,03			
		BUS A07 L		06,01		LOAD DATA LO L		01,03,04		UET02 REC A15 H		02,03			
		BUS A08 L		02,06		TS D0 H		01,03		UET02 REC A0 H		02,03			
		BUS A09 L		02,06		TS D1 H		01,03		UET02 REC A9 H		02,03			
		BUS A10 L		02,06		TS D10 H		02,05		UET02 REC D10 H		02,05			
		BUS A11 L		02,06		TS D11 H		02,05		UET02 REC D11 H		02,05			
		BUS A12 L		02,06		TS D12 H		02		UET02 REC D12 H		02			
		BUS A13 L		02,06		TS D13 H		02		UET02 REC D13 H		02			
		BUS A14 L		02,06		TS D14 H		02		UET02 REC D14 H		02			
		BUS A15 L		02,06		TS D15 H		02		UET02 REC D15 H		02,05			
		BUS A16 L		03,06		TS D2 H		01,03		UET02 REC D0 H		02,05			
		BUS A17 L		03,06		TS D3 H		01,03		UET02 REC D9 H		02,05			
		BUS AC LO L		06		TS D4 H		01,03		UET03 A16 (1X)		03			
		BUS B05 L		06,04		TS D5 H		01,03		UET03 A17 (1X)		03			
		BUS B04 H		05,06		TS D6 H		01,03		UET03 ADDR DETECT H		03,04			
		BUS B05 H		05,06		TS D7 H		01,03		UET03 ASSERT SSYN H		03,04			
		BUS B06 H		05,06		TS D8 H		02,05		UET03 C0 (1X)		03			
		BUS B07 H		06,05		TS D9 H		02,05		UET03 C1 (1X)		03,04			
		BUS B04 L		05,06		UET01 A0 (1X)		03,01		UET03 ENAB ADDR TS L		01,03,02			
		BUS B05 L		05,06		UET01 A1 (1X)		03,01		UET03 ENAB CONT TS L		03,05			
		BUS B06 L		05,06		UET01 A2 (1X)		03,01		UET03 ENAB DATA TS L		01,03,02			
		BUS B07 L		06,05		UET01 A3 (1X)		03,01		UET03 ENAB PROM DATA L		03			
		BUS C0 L		03,06		UET01 A4 (1X)		01,03		UET03 LOAD ADDR L		01,02,03			
		BUS C1 L		03,06		UET01 A5 (1X)		01,03		UET03 LOAD CONT HI L		05,03			
		BUS D00 L		06,01		UET01 A6 (1X)		01,03		UET03 LOAD CONT LO L		03,04			
		BUS D01 L		06,01		UET01 A7 (1X)		01,03		UET03 PB (0X)		03			
		BUS D02 L		06,01		UET01 REC A0 H		01,03		UET03 PB (1X)		03			
		BUS D03 L		06,01		UET01 REC A1 H		01,03		UET03 REC A16 H		03			
		BUS D04 L		01,06		UET01 REC A2 H		01,03		UET03 REC A17 H		03			
		BUS D05 L		01,06		UET01 REC A3 H		01,03		UET03 REC C0 H		03			
		BUS D06 L		01,06		UET01 REC A4 H		01,03		UET03 REC C1 H		03,04			
		BUS D07 L		01,06		UET01 REC A5 H		01,03		UET04 ASSERT DATA L		01,02,04			
		BUS D08 L		06,02		UET01 REC A6 H		01,03		UET04 DATI L		03,04			
		BUS D09 L		06,02		UET01 REC A7 H		01,03		UET04 MASTER L		04,01,03,02			
		BUS D10 L		06,02		UET01 REC D0 H		01,04		UET04 NPR (0X)		03,04			
		BUS D11 L		06,02		UET01 REC D1 H		01,03		UET04 NPR (1X)		04			
		BUS D12 L		06,02		UET01 REC D2 H		01,03		UET04 PE (0X)		03,04			
		BUS D13 L		06,02		UET01 REC D3 H		01,03		UET04 REC B05 H		05,04			
		BUS D14 L		06,02		UET01 REC D4 H		01,03		UET04 REC MSYN H		03,04			
		BUS D15 L		06,02		UET01 REC D5 H		01,03		UET04 REC SSYN H		04			
		BUS DC LO L		06		UET01 REC D6 H		01		UET04 REC SSYN L		05,04			
		BUS INIT L		06,05		UET01 REC D7 H		01		UET04 TO (0X)		03,04			
		BUS INTR L		06,04		UET02 A10 (1X)		03,02		UET04 TRANSFER L		04			
		BUS MSYN L		06,04		UET02 A11 (1X)		03,02		UET05 ASSERT INTR H		05,04			

8	7	6	5	4	3	2	1																																																
D		C		B		A																																																	
SIGNAL NAME		PAGE NUMBER(S)		SIGNAL NAME		PAGE NUMBER(S)																																																	
UET05 BR4 (0)H		05		SIGNAL NAME		PAGE NUMBER(S)																																																	
UET05 BR5 (0)H		05																																																					
UET05 BR6 (0)H		05																																																					
UET05 BR7 (0)H		05																																																					
UET05 INIT H		05																																																					
UET05 INIT L		05,03,04																																																					
UET05 INTERRUPT MASTER H		05,04																																																					
UET05 INTERRUPT MASTER L		05,04																																																					
UET05 MASTER 04 L		05																																																					
UET05 MASTER 05 L		05																																																					
UET05 MASTER 06 L		05																																																					
UET05 MASTER 07 L		05																																																					
UET05 SACK L		05																																																					
UET06 +3V H		06,05,03,04																																																					
UET06 TEST DISABLE A H		01,02,03,04																																																					
UET06 TEST DISABLE B H		03																																																					
NOTES: 1. THIS PAGE LISTS THE SCHEMATIC PAGE NUMBER(S) WHERE A SIGNAL NAME IS REFERENCED.																																																							
<div>THIS DRAWING AND SPECIFICATIONS ARE THE PROPERTY OF DIGITAL EQUIPMENT CORPORATION AND SHALL NOT BE REPRODUCED OR COPIED OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION. COPYRIGHT © 1988. DIGITAL EQUIPMENT CORPORATION</div> <table><tr><td colspan="2">REVISIONS</td><td colspan="2">DATE</td><td colspan="2">ENG.</td><td colspan="2">DATE</td><td colspan="2">TITLE</td></tr><tr><td>CHK</td><td>CHANGE NO.</td><td>REV</td><td></td><td></td><td></td><td></td><td></td><td colspan="2">UET08 FORWARD REFERENCE</td></tr><tr><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td></tr><tr><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td><td colspan="2"></td></tr></table> <div>digital [160,1271] UET08.DRW 17-FEB-80 13:30 NEXT HIGHER ASSEMBLY: 8-DD-M9313-0-8 FIRST USED ON OPTION/MODEL: 11/250</div> <table><tr><td>SIZE</td><td>CODE</td><td>NUMBER</td><td>REV.</td></tr><tr><td>D</td><td>CS</td><td>M9313-0-8</td><td>A</td></tr></table>								REVISIONS		DATE		ENG.		DATE		TITLE		CHK	CHANGE NO.	REV						UET08 FORWARD REFERENCE																						SIZE	CODE	NUMBER	REV.	D	CS	M9313-0-8	A
REVISIONS		DATE		ENG.		DATE		TITLE																																															
CHK	CHANGE NO.	REV						UET08 FORWARD REFERENCE																																															
SIZE	CODE	NUMBER	REV.																																																				
D	CS	M9313-0-8	A																																																				
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100% REWORK INSTRUCTIONS  
 WIRE ADDS SIDE 1  
 1. ADD 18 AWG 9107560-03 WIRE AND TUBING  
 9107274-06 BETWEEN BOTTOM LEAD OF C8  
 AND J2-1.  
 WIRE ADDS SIDE 2  
 1. 2 BETWEEN J1-14 AND J1-20

COMPONENT SIDE VIEW



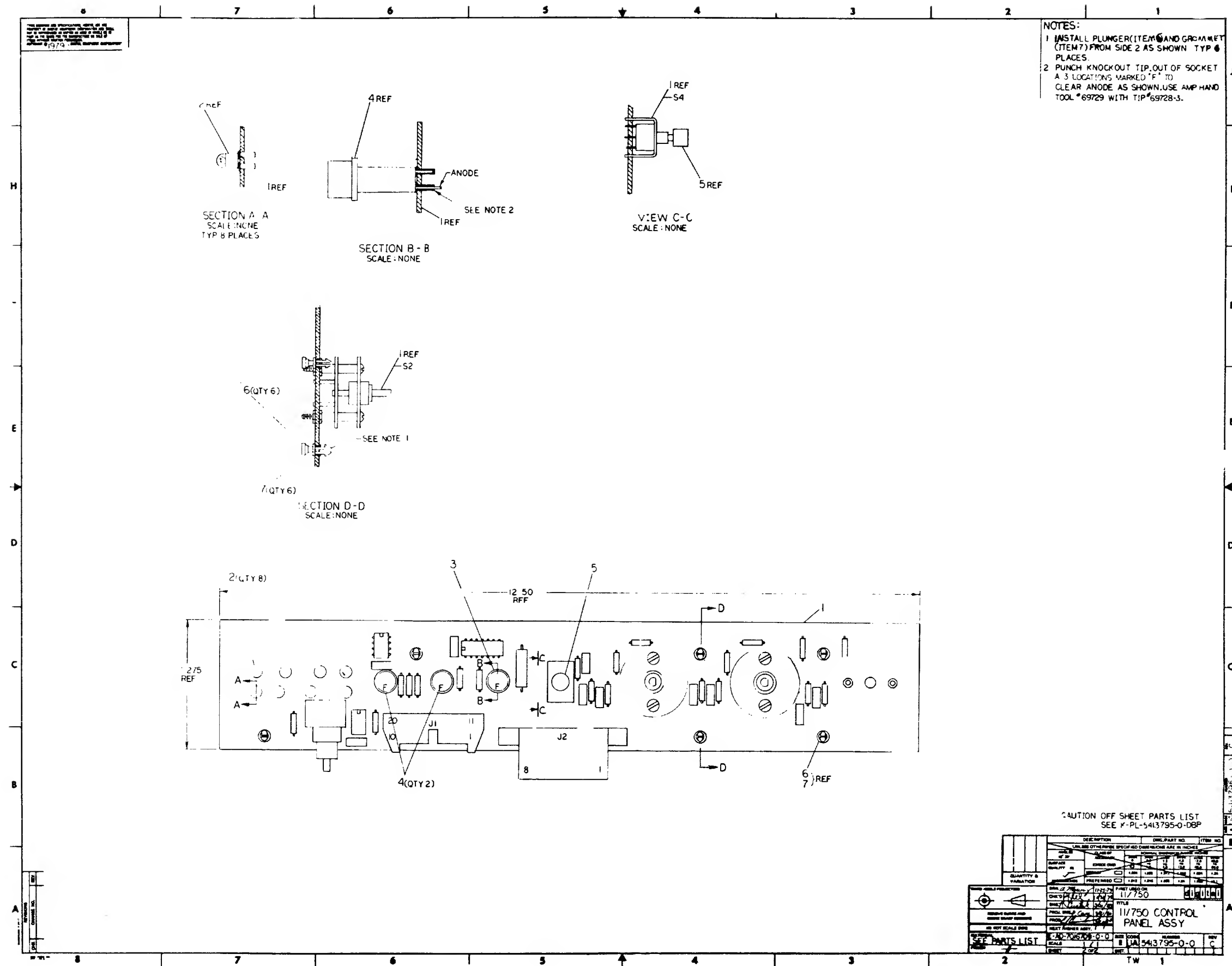
NOTES:  
 1. WELD WIRE FROM L-100ER FAC OF S4 TO  
 FEET THRU ABOVE S4

+

ON CHANGE WIRE

NOTES:  
 1. STEP 2 REPEL 2M156  
 2. STEP 3 REPEL 2M156  
 3. STEP 4 REPEL 2M156  
 4. STEP 5 REPEL 2M156  
 5. STEP 6 REPEL 2M156  
 6. STEP 7 REPEL 2M156  
 7. STEP 8 REPEL 2M156  
 8. STEP 9 REPEL 2M156  
 9. STEP 10 REPEL 2M156  
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 81. STEP 82 REPEL 2M156  
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 83. STEP 84 REPEL 2M156  
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 92. STEP 93 REPEL 2M156  
 93. STEP 94 REPEL 2M156  
 94. STEP 95 REPEL 2M156  
 95. STEP 96 REPEL 2M156  
 96. STEP 97 REPEL 2M156  
 97. STEP 98 REPEL 2M156  
 98. STEP 99 REPEL 2M156  
 99. STEP 100 REPEL 2M156

100% REWORK INSTRUCTIONS



SHEET A1 OF A2

REFERENCE DESIGNATOR

C12

REVISION HISTORY			BASIC PART NO: 5413795		DRN: P. TELLIER		DATE: 17-MAY-83		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A		CHK'D: F. GAROFALO		DATE: 17-MAY-83		TITLE PARTS LIST	
---	INITIAL	B	SECTION VARIATION INDEX		CHK'D: F. GAROFALO		DATE: 17-MAY-83		11/750 CONTROL PANEL	
RC	5413795-TW01A	C	[A]	00	DES. ENG: R. CIESLUK		DATE: 17-MAY-83			
RC	5413795-TW002	D	[B]		RESP. ENG.: D. CANE		DATE: 17-MAY-83			
SB	5413795-TW003	E	[C]		MFG. ENG.: K. O'BRIEN		DATE: 17-MAY-83			
			[D]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:			
			[E]		D-UA-5413795-0-0		B-DD-5413795-0-0			
			[F]							
			[H]							
			[J]							
			[K]							
			[L]							
			[M]							
			[N]							

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AUTOMATED BY PRTLST.4Q(50)

PARTS LIST

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION 00	REFERENCE DESIGNATOR
-----------	-----------------	-------------	-------------	-------------------------	----------------------

30 NOTE: SOME MODULES WILL HAVE 10-05306 INSTEAD OF 1012084-01

D	I	G	I	T	A	L	TITLE	11/750 CONTROL PANEL	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	5413795-0-DBP	E

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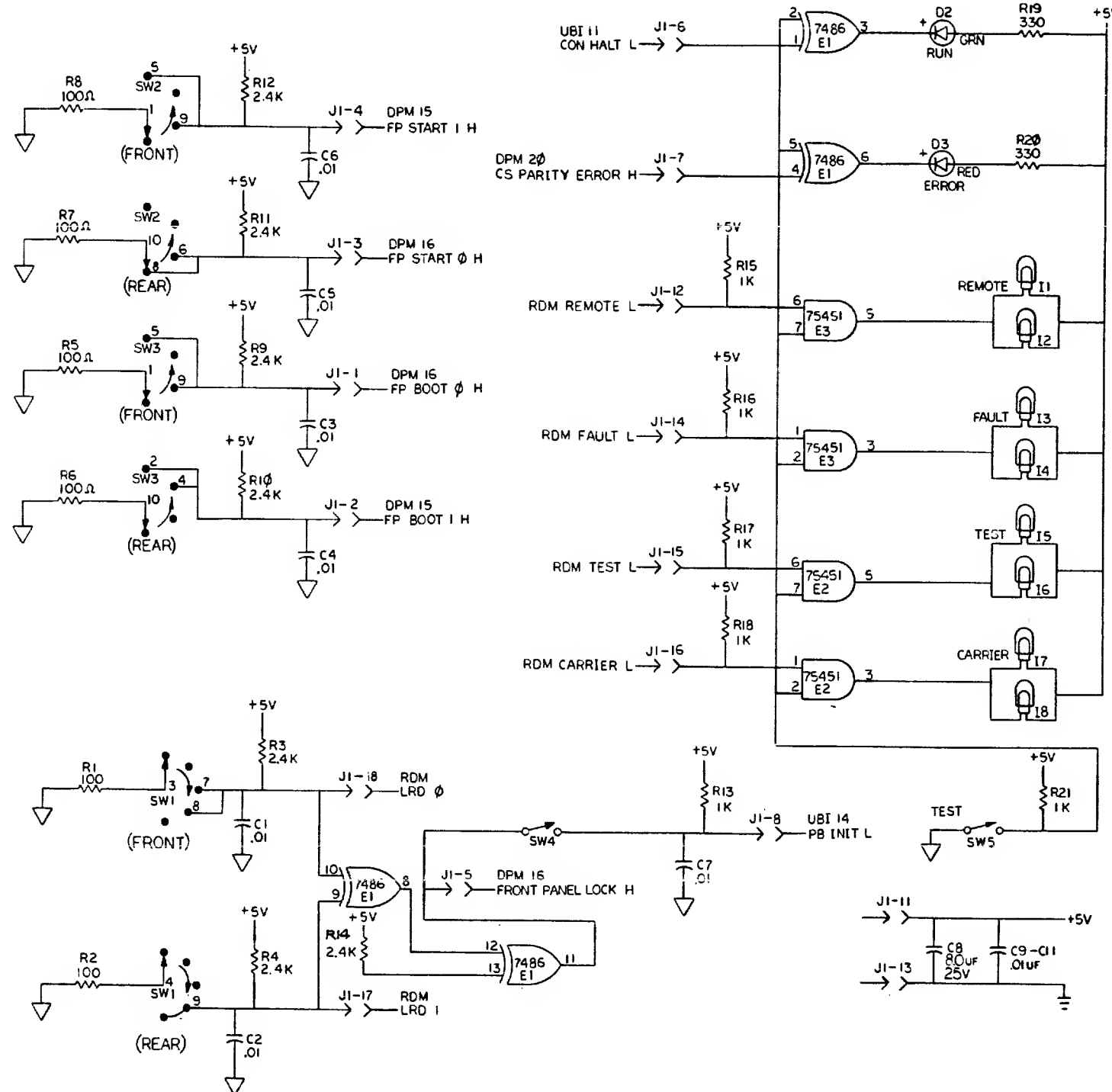
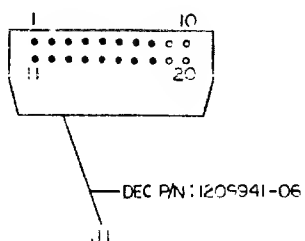
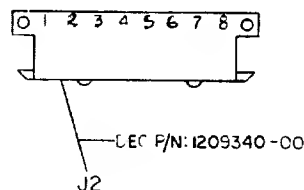
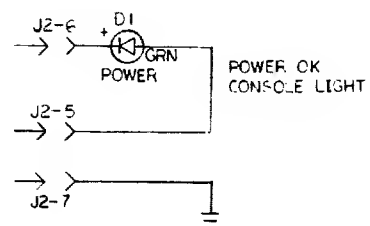
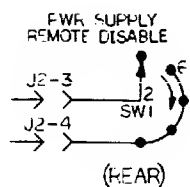
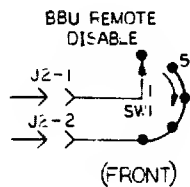
SWITCH 1 (SHOWN IN POSITION ONE)		
SWITCH POS.	FUNCTION	CONNECTION
1	OFF	—
2	SECURE	1-5, 2-6
3	LOCAL	1-5, 2-6, 3-7
4	REMOTE SECURE	1-5, 2-6, 3-8, 4-9
5	REMOTE	1-5, 2-6, 4-9

SWITCH 2 (SHOWN IN POSITION ONE)		
SWITCH POS.	FUNCTION	CONNECTION
1	BOOT	8-10
2	RESTART	1-9, 6-10
3	HALT	—
4	RESTART	1-5

SWITCH 3		
SWITCH POS.	FUNCTION	CONNECTION
1	A	—
2	B	1-9
3	C	4-10
4	D	1-5, 2-10

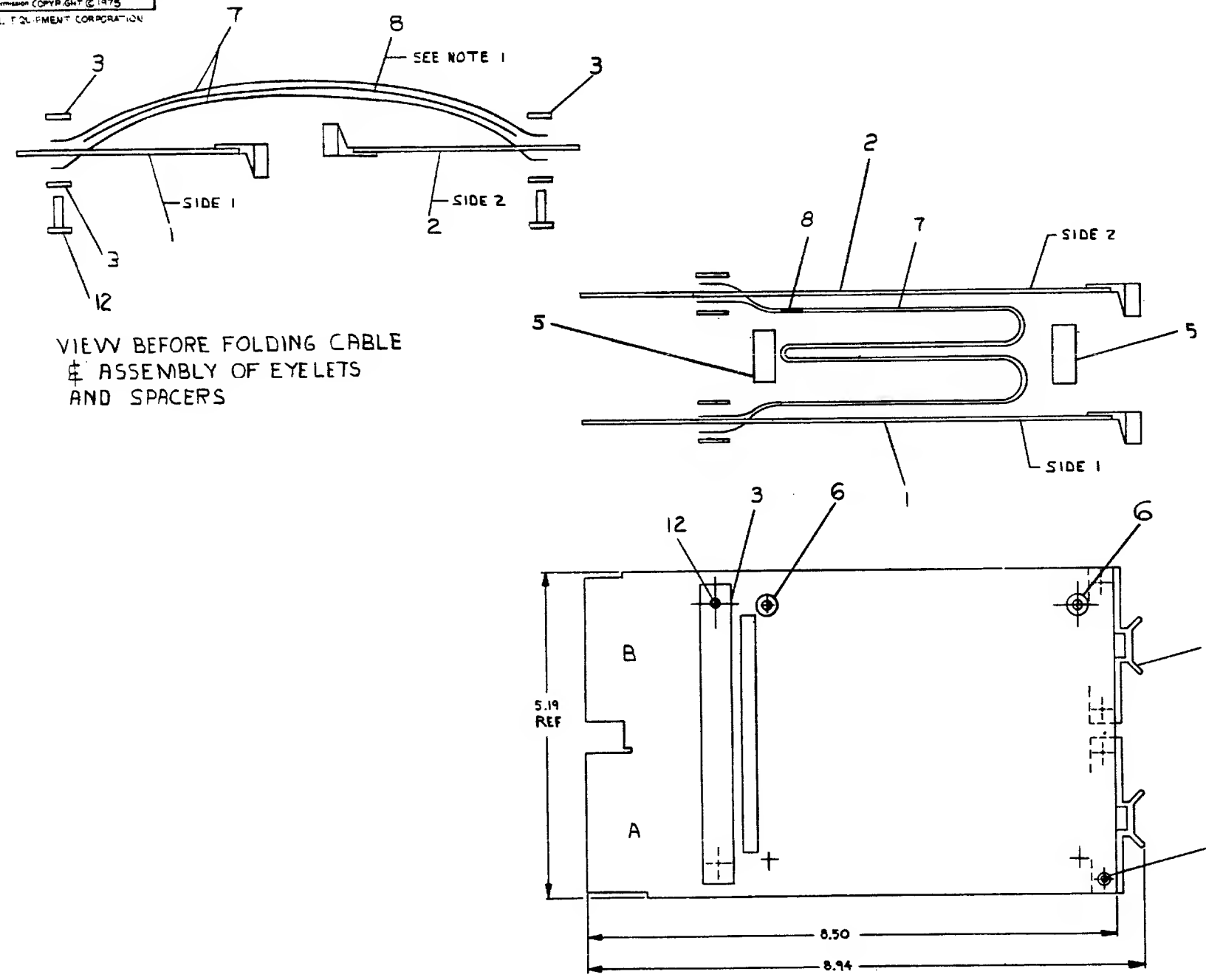
NOTES:

1. SWITCHES 2 AND 3 ARE IN POSITION ONE WHEN FULLY CLOCKWISE.



DRN. <i>B. Chesser</i>	DATE <i>3/2/75</i>	FIRST USED ON	<i>40-00-00</i>
CHK'D <i>B. Chesser</i>	DATE <i>3/2/75</i>	TITLE	11/750 CONTROL PANEL
ENG. <i>B. Chesser</i>	DATE <i>3/2/75</i>	PROJ. ENG. <i>P. Cane</i>	DATE <i>3/2/75</i>
PROD. <i>J. Connerly</i>	DATE <i>3/2/75</i>	NEXT HIGHER ASBY.	
E-D-5413795-00		SIZE	COOR
SCALE		D	CS
SHEET 1 OF 1		NUMBER	5413795-0-1
REV.		C	

- NOTES:
1. FOAM (ITEM#8) TO BE PLACED BETWEEN CABLES (ITEM#7) AND ALL FOLDS.
  2. FLEX PRINTS TO BE BOUND TOGETHER BY WRAPPING WITH BLACK ELECTRICAL TAPE (ITEM#4) EVERY 8 INCHES.



CAUTION: OFF SHEET PARTS LIST EXISTS SEE K-PL-M9202-0-DBP

1	EYELET	9006745	12
A/R	TAPE, ADHESIVE TRANSFER	9009347-01	11
8	EYELET	9006732	10
4	HANDLE, FLY-CHIP MAGENTA	9008337-06	9
A/R	FOAM, GRAY	9008881	8
2	CABLE, FLEXPRINT (2' LG)	1700002-01	7
4	EYELET	9008223	6
4	SPACER	8-ND-7413488-0-0	5
A/R	BLACK ELECTRICAL TAPE	9009507	4
4	CLAMP, CABLE	8-ND-7413489-0-0	3
1	ETCHED CIRCUIT BOARD	0-CS-5411579-0-1	2
1	ETCHED CIRCUIT BOARD	0-CS-5411577-0-1	1

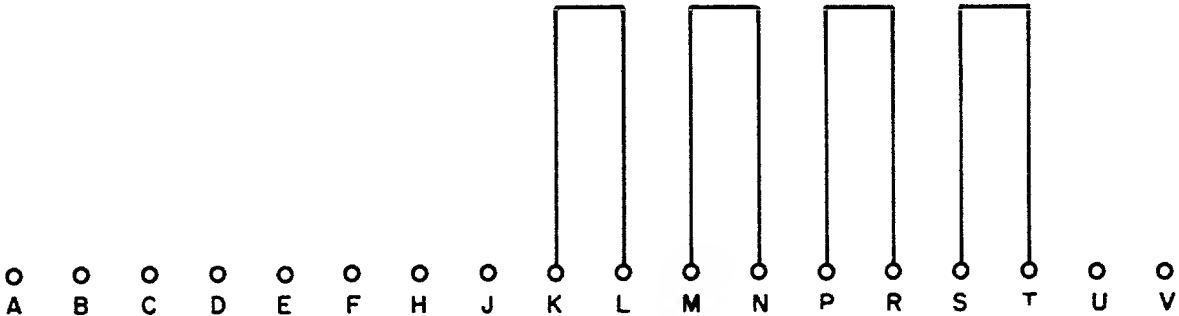
FIRST USED ON OPTION/MODEL	QTY.	DESCRIPTION	PART NO.	ITEM NO.
PDP/11				
UNLESS OTHERWISE SPECIFIED DIMENSION IN INCHES TOLERANCES				
DECIMALS	ANGLES	DATE 1/28/79		
.XXX - .005	10° 30'	DATE 1/27/79		
.XX - .02		DATE 1/27/79		
.X - .1		DATE 1/27/79		
REMOVE BURRS AND BREAK SHARP CORNERS SURFACE QUALITY		DATE 1/27/79		
MATERIAL	NEXT HIGHER ASSY.	DATE 1/27/79		
SEE PARTS LIST		DATE 1/27/79		
FINISH	SCALE NONE	DATE 1/27/79		
	SHEET 1 OF 1	DATE 1/27/79		

PARTS LIST		digital EQUIPMENT CORPORATION	
TITLE		CABLE, UNIBUS (JUMPER)	
SIZE CODE	NUMBER	REV	
DUA	M9202-0-0	C	

REVISIONS	REV	DATE	BY	CHK
CHANGE NO	A	15 April 77	R. SARGENT	
M9202-00001				
15 April 77	B			
15 April 77	C			
15 April 77	D			
15 April 77	E			
15 April 77	F			
15 April 77	G			
15 April 77	H			
15 April 77	I			
15 April 77	J			
15 April 77	K			
15 April 77	L			
15 April 77	M			
15 April 77	N			
15 April 77	O			
15 April 77	P			
15 April 77	Q			
15 April 77	R			
15 April 77	S			
15 April 77	T			
15 April 77	U			
15 April 77	V			
15 April 77	W			
15 April 77	X			
15 April 77	Y			
15 April 77	Z			

[illegible]

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REVISIONS	CHK	CHG NO.	REV.	DRN.	DATE	TRANSISTOR & DIODE CONVERSION CHART				<b>digital</b> EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS	TITLE GRANT CONTINUITY G727			
				<i>BUTLER</i>	<i>11-19-69</i>	DEC	EIA	DEC	EIA		SIZE	CODE	NUMBER	REV.
				CHK'D	DATE						B	CS	G727-0-1	
				<i>P. J. Smith</i>	<i>6-FC-1370</i>									
				ENG.	DATE									
				<i>D. E. Johnson</i>	<i>1/28/70</i>									
				PROD.	DATE									